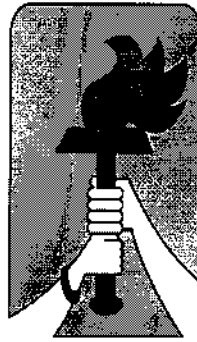


**HINDUSTHAN  
EDUCATIONAL AND**



**CHARITABLE TRUST**

**HICET**

***HINDUSTHAN  
COLLEGE OF ENGINEERING  
AND TECHNOLOGY***

**(An Autonomous Institution)**

**Coimbatore– 641032**

**DEPARTMENT OF ELECTRONICS AND  
COMMUNICATION**

**CURRICULUM & SYLLABUS**

**AY-2023-2024**

**Batch: 2022-2026**

**REGULATIONS 2022**



SEMESTER I											
S. NO	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22MA1101	MATRICES AND CALCULUS	BSC	3	1	0	4	4	40	60	100
<b>THEORY WITH LAB COMPONENT</b>											
2	22CY1151	CHEMISTRY FOR CIRCUIT ENGINEERING	BSC	2	0	2	3	4	50	50	100
3	22HE1151	ENGLISH FOR ENGINEERS	HSC	2	0	2	3	4	50	50	100
4	22EC1151	ELECTRON DEVICES	ESC	2	0	2	3	4	50	50	100
5	22IT1151/ 22CS1152	PYTHON PROGRAMMING AND PRACTICES/ OBJECT ORIENTED PROGRAMMING USING PYTHON(IBM STUDENTS ONLY)	ESC/CC	2	0	2	3	4	50	50	100
<b>EEC COURSES (SE/AE)</b>											
6	22HE1071	UHV	AEC	2	0	0	2	3	40	60	100
7	22HE1072	ENTREPRENEURSHIP & INNOVATION	AEC	1	0	0	1	1	100	0	100
<b>MANDATORY COURSE</b>											
8	22MC1091/ 22MC1092	தமிழரும் தொழில்நுட்பமும்/Indian Constitution	MC	2	0	0	0	2	100	0	100
<b>TOTAL CREDITS</b>				<b>16</b>	<b>1</b>	<b>8</b>	<b>19</b>	<b>26</b>	<b>480</b>	<b>320</b>	<b>800</b>

SEMESTER II											
S. NO	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22MA2102	DIFFERENTIAL EQUATIONS AND LAPLACE TRANSFORM	BSC	3	1	0	4	4	40	60	100
2	22CY2101	ENVIRONMENTAL STUDIES	ESC	2	0	0	2	3	40	60	100
3	22PH2101	BASICS OF MATERIAL SCIENCE	BSC	2	0	0	2	3	40	60	100
<b>THEORY WITH LAB COMPONENT</b>											
4	22PH2151	PHYSICS FOR CIRCUIT ENGINEERING PROGRAMME	BSC	2	0	2	3	4	50	50	100
5	22HE2151	EFFECTIVE TECHNICAL COMMUNICATION	HSC	2	0	2	3	4	50	50	100
6	22CS2255	PROGRAMMING USING C	PCC	2	0	2	3	4	50	50	100
	22CS2253	JAVA FUNDAMENTALS (IBM STUDENTS ONLY)	ICC								
<b>PRACTICAL</b>											
7	22ME2001	ENGINEERING PRACTICES	ESC	0	0	4	2	2	60	40	100
<b>EEC COURSES (SE/AE)</b>											
8	22HE2071	DESIGN THINKING	AEC	1	0	2	2	2	100	0	100
9	22HE2072	SOFT SKILLS AND APTITUDE -I	SEC	1	0	0	1	1	100	0	100
<b>MANDATORY COURSES</b>											
10	22MC2091 22MC2092	தமிழர் மரபு/ Heritage of Tamils	MC	2	0	0	0	2	100	0	100

11	22MC2093	NCC */NSS / YRC / SPORTS / CLUBS / SOCIETY SERVICE - ENROLLMENT (COMMON)	MC	All students shall enroll, on admission, in anyone of the personality and character development programmes and undergo training for about 80 hours							
<b>TOTAL CREDITS</b>				17	1	12	22	29	630	370	1000

SEMESTER III											
S.N O	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22MA3102	COMPLEX ANALYSIS AND TRANSFORMS (common to ECE,EEE,EIE)	BSC	3	1	0	4	4	40	60	100
2	22EC3201	ELECTRONIC CIRCUITS	PCC	3	0	0	3	3	40	60	100
3	22EC3202	SIGNALS AND SYSTEMS	PCC	3	0	0	3	3	40	60	100
4	22EC3203	DIGITAL ELECTRONICS	PCC	3	0	0	3	3	40	60	100
5	22EC3204	CIRCUITS AND NETWORKS	ESC	2	0	0	2	3	100	-	100
<b>THEORY WITH LAB COMPONENT</b>											
6	22EC3251/ 22IT3252	OOPS USING JAVA/RELATIONAL DATABASE MANAGEMENT SYSTEM (IBM STUDENTS ONLY)	ESC/ICC	2	0	2	3	3	50	50	100
<b>PRACTICAL</b>											
7	22EC3001	ELECTRONIC CIRCUITS LABORATORY	PCC	0	0	3	1.5	3	60	40	100
8	22EC3002	DIGITAL ELECTRONICS LABORATORY	PCC	0	0	3	1.5	3	60	40	100
<b>EEC COURSES (SE/AE)</b>											
9	22HE3071	SOFT SKILLS -2	SEC	1	0	0	1	1	100	0	100
10	22EC3901	MINI PROJECT 1	AEC	0	0	0	2	1	100	0	100
<b>TOTAL CREDITS</b>				17	1	8	24	27	610	390	1000

SEMESTER IV											
S.N O	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22HE4101	IPR AND START-UPS	HSC	2	0	0	2	2	40	60	100
2	22EC4201	ELECTRO MAGNETIC FIELDS	PCC	3	0	0	3	3	40	60	100
3	22EC4202	ANALOG COMMUNICATION	PCC	3	0	0	3	3	40	60	100
4	22EC4203	LINEAR INTEGRATED CIRCUITS	PCC	3	0	0	3	3	40	60	100
5	22EC4304	TRANSMISSION LINES AND WAVEGUIDES	PCC	3	0	0	3	3	40	60	100
<b>THEORY WITH LAB COMPONENT</b>											
6	22EC4251/ 22EC4252	CONTROL SYSTEMS/DESIGN THINKING-AN INTRODUCTION (IBM STUDENTS ONLY)	PCC/ICC	2	0	2	3	4	50	50	100
7	22EC4253	DATA COMMUNICATION AND NETWORKS	PCC	2	0	2	3	4	50	50	100
<b>PRACTICAL</b>											

8	22EC4001	LINEAR INTEGRATED CIRCUITS LAB	PCC	0	0	3	1.5	4	60	40	100
9	22EC4002	ANALOG COMMUNICATION LAB	PCC	0	0	3	1.5	4	60	40	100
<b>EEC COURSES (SE/AE)</b>											
10	22HE4071	SOFT SKILLS -3	SEC	1	0	0	1	1	100	0	100
<b>TOTAL CREDITS</b>				<b>19</b>	<b>0</b>	<b>10</b>	<b>24</b>	<b>31</b>	<b>400</b>	<b>500</b>	<b>900</b>

<b>SEMESTER V</b>											
S.N O	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22EC5201	DIGITAL COMMUNICATION	PCC	3	0	0	3	3	40	60	100
2	22EC5202	ANTENNA AND WAVE PROPAGATION	PCC	3	1	0	4	3	40	60	100
3	22EC5203	MICROPROCESSORS AND MICROCONTROLLERS	PCC	3	0	0	3	3	40	60	100
4	22EC53XX/ 22EC5251	PROFESSIONAL ELECTIVE-1/ ANGULAR JS(IBM STUDENTS ONLY)	PEC/ICC	3	0	0	3	3	40	60	100
5	22EC53XX	PROFESSIONAL ELECTIVE-2	PEC	3	0	0	3	3	40	60	100
6	22EC53XX	PROFESSIONAL ELECTIVE-3	PEC	3	0	0	3	3	40	60	100
<b>PRACTICAL</b>											
7	22EC5001	MICROPROCESSORS AND MICROCONTROLLERS LAB	PCC	0	0	3	1.5	3	60	40	100
8	22EC5002	DIGITAL COMMUNICATION LAB	PCC	0	0	3	1.5	3	60	40	100
<b>EEC COURSES (SE/AE)</b>											
9	22HE5071	SOFT SKILLS -4 / FOREIGN LANGUAGES	SEC	1	0	0	1	1	100	0	100
<b>TOTAL CREDITS</b>				<b>19</b>	<b>1</b>	<b>6</b>	<b>23</b>	<b>25</b>	<b>440</b>	<b>460</b>	<b>900</b>

<b>SEMESTER VI</b>											
S.N O	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22HS6101	PROFESSIONAL ETHICS	HSC	3	0	0	3	3	40	60	100
2	22EC63XX/ 22EC6251	PROFESSIONAL ELECTIVE-4/ NODE JS AND MICRO SERVICES (IBM STUDENTS ONLY)	PEC/ICC	3	0	0	3	3	40	60	100
3	22EC63XX / 22EC6252	PROFESSIONAL ELECTIVE-5/ IOT AND SPRING FRAMEWORK (IBM STUDENTS ONLY)	PEC/ICC	3	0	0	3	3	40	60	100
4	22EC64XX	OPEN ELECTIVE - 1*	OEC	3	0	0	3	3	40	60	100
5	22EC64XX	OPEN ELECTIVE - 2*	OEC	3	0	0	3	3	40	60	100
<b>THEORY WITH LAB COMPONENT</b>											
6	22EC6253	DIGITAL SIGNAL PROCESSING	PCC	2	0	2	3	4	50	50	100
7	22EC6254	VLSI DESIGN	PCC	2	0	2	3	4	50	50	100
<b>EEC COURSES (SE/AE)</b>											
8	22EC6901	MINI PROJECT 2	AEC	0	0	0	2	1	100	0	100
<b>TOTAL CREDITS</b>				<b>19</b>	<b>1</b>	<b>6</b>	<b>23</b>	<b>26</b>	<b>400</b>	<b>400</b>	<b>800</b>

SEMESTER VII											
S.N O	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>THEORY</b>											
1	22EC7201	WIRELESS COMMUNICATION NETWORKS	PCC	3	0	0	3	3	40	60	100
2	22EC73XX/ 22EC7251	PROFESSIONAL ELECTIVE-6/ BLOCKCHAIN(IBM STUDENTS ONLY)	PEC/ICC	3	0	0	3	3	40	60	100
3	22EC74XX	OPEN ELECTIVE – 3*	OEC	3	0	0	3	3	40	60	100
4	22EC74XX	OPEN ELECTIVE – 4*	OEC	3	0	0	3	3	40	60	100
<b>THEORY WITH LAB COMPONENT</b>											
5	22EC7001	EMBEDDED SYSTEMS AND IOT	PCC	2	0	2	3	4	50	50	100
6	22EC7001	OPTICAL COMMUNICATION AND MICROWAVE ENGINEERING	PCC	2	0	2	3	4	50	50	100
<b>EEC COURSES (SE/AE)</b>											
7	22EC7901	INTERNSHIP	AEC	-	-	-	2	1	100	0	100
<b>TOTAL CREDITS</b>				<b>19</b>	<b>0</b>	<b>4</b>	<b>20</b>	<b>23</b>	<b>360</b>	<b>340</b>	<b>700</b>

SEMESTER VIII											
S.N O	COURSE CODE	COURSE TITLE	COURSE CATEGORY	L	T	P	C	TCP	CIA	ESE	TOTAL
<b>EEC COURSES (SE/AE)</b>											
1	22EC8901	PROJECT WORK/GRANTED PRODUCT PATENT	AEC	0	0	20	10	20	100	100	200
<b>TOTAL CREDITS</b>				<b>0</b>	<b>0</b>	<b>20</b>	<b>10</b>	<b>20</b>	<b>100</b>	<b>100</b>	<b>200</b>

**Note:**

1. \*As per the AICTE guideline, in Semesters I, II, III & IV NCC one credit subject is added as Value Added Course with Extra Credit. Further, the students who enrolled his/her name in HICET NCC and Air Wing are eligible to undergo this subject. The earned extra credits printed in the Consolidated Mark sheet as per the regulation.
2. NCC course level 1 & Level 2 will be added in the list of open elective subjects in the appropriate semester. Further, the students' who have opted NCC subjects in Semester I, II, III & IV are eligible to undergo NCC Open Elective Subjects.
3. The above-mentioned NCC Courses will be offered to the students who are going to be admitted in the Academic Year 2021 – 22 onwards.

## SEMESTER WISE CREDIT DISTRIBUTION

B.E. / B.TECH.PROGRAMMES										
S.No.	Course Area	Credits per Semester								Total Credits
		I	II	III	IV	V	VI	VII	VIII	
1	HSC	3	3	-	2	-	3	-	-	11
2	BSC	7	9	4	-	-	-	-	-	20
3	ESC	6	4	5	-	-	-	-	-	15
4	PCC	-	3	12	21	13	6	9	-	64
5	PEC	-	-	-	-	9	6	3	-	18
6	OEC	-	-	-	-	-	6	6	-	12
7	EEC	3	3	3	1	1	2	2	10	25
8	MC	□	□							
<b>Total</b>		<b>19</b>	<b>22</b>	<b>24</b>	<b>24</b>	<b>23</b>	<b>23</b>	<b>20</b>	<b>10</b>	<b>165</b>

### Credit Distribution R2022

Semester	I	II	III	IV	V	VI	VII	VIII	Total
Credits	19	22	24	24	23	23	20	10	165

### LIST OF INDUSTRIAL CORE COURSES

S. No.	CODE	Courses	CAT	L	T	P	C	CIA	ESE	TOTAL
1	22CS1152	Object Oriented Programming using Python	ICC	2	0	2	3	50	50	100
2	22CS2153	Java Fundamentals	ICC	2	0	2	3	50	50	100
3	22EC3252	Relational Database Management System	ICC	2	0	2	3	50	50	100
4	22EC4252	Design Thinking-An Introduction	ICC	2	0	2	3	50	50	100
5	22EC5251	Angular JS	ICC	2	0	2	3	50	50	100
6	22EC6251	Node JS and Micro services	ICC	2	0	2	3	50	50	100
7	22EC6252	IoT and Spring Framework	ICC	2	0	2	3	50	50	100
8	22EC7251	Blockchain	ICC	2	0	2	3	50	50	100

## OPEN ELECTIVE I AND II (EMERGING TECHNOLOGIES)

To be offered for the students other than CSE, IT, AI&ML, ECE & BIOMEDICAL

S NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1	22AI6401	Artificial intelligence and Machine Learning Fundamentals	OEC	2	0	2	4	3
2	22CS6401	Blockchain Technology	OEC	2	0	2	4	3
3	22EC6401	Cyber Security	OEC	2	0	2	4	3
4	22EC6402	IoT Concepts and Applications	OEC	2	0	2	4	3
5	22IT6401	Data Science and Analytics	OEC	2	0	2	4	3
6	22BM6401	Augmented and Virtual Reality	OEC	2	0	2	4	3

## OPEN ELECTIVE I AND II

To be offered for the students other than AUTO, AERO, AGRI, MECH, MCTS, CIVIL, EEE, CHEMICAL, FOOD TECH, E&I

S NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1	22AE6401	Space Science	OEC	3	0	0	3	3
2	22MT6401	Introduction to Industrial Engineering	OEC	3	0	0	3	3
3	22MT6402	Industrial Safety and Environment	OEC	3	0	0	3	3
4	22CE6401	Climate Change and its Impact	OEC	3	0	0	3	3
5	22CE6402	Environment and Social Impact Assessment	OEC	3	0	0	3	3
6	22ME6401	Renewable Energy System	OEC	3	0	0	3	3
7	22ME6402	Additive Manufacturing systems	OEC	3	0	0	3	3
8	22EI6401	Introduction to Industrial Instrumentation and Control	OEC	3	0	0	3	3
9	22EI6402	Graphical Programming using Virtual Instrumentation	OEC	3	0	0	3	3
10	22AU6401	Recent Trends in Automotive Technology	OEC	3	0	0	3	3
11	22AU6402	Automotive Vehicle Safety	OEC	3	0	0	3	3
12	22EE6401	Digital Marketing	OEC	3	0	0	3	3
13	22EE6402	Research Methodology	OEC	3	0	0	3	3
14	22FT6401	Traditional Foods	OEC	3	0	0	3	3
15	22AG6401	Urban Agriculture and Organic Farming	OEC	3	0	0	3	3
16	22CH6401	Biomass and Bio Refinery	OEC	3	0	0	3	3

Note: Non-Circuit Departments can add one Open Elective course in the above list to offer for the circuit branches

## OPEN ELECTIVE III

Students shall choose any one of the open elective courses such that the course content or title not belong to their own programme.

S	COURSE	COURSE TITLE	CATEGORY	PERIODS PER WEEK	TOTAL	CREDITS
---	--------	--------------	----------	------------------	-------	---------



3.	22EC5303	Medical Informatics	PEC3	3	0	0	3	3
4.	22EC6301	Medical Image Processing	PEC4	3	0	0	3	3
5.	22EC6302	Artificial Intelligence	PEC5	3	0	0	3	3
6.	22EC7301	Human Computer Interface	PEC6	3	0	0	3	3

#### VERTICAL2:VLSI Design

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5304	PCB Design	PEC1	3	0	0	3	3
2.	22EC5305	Advanced Processors	PEC2	3	0	0	3	3
3.	22EC5306	ASIC Design	PEC3	3	0	0	3	3
4.	22EC6303	Embedded Controllers	PEC4	3	0	0	3	3
5.	22EC6304	Low Power VLSI	PEC5	3	0	0	3	3
6.	22EC7302	Industrial Automation	PEC6	3	0	0	3	3

#### VERTICAL3:Communication systems

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5307	Fiber Optic Communication	PEC1	3	0	0	3	3
2.	22EC5308	Cellular and Mobile Communication	PEC2	3	0	0	3	3
3.	22EC5309	Satellite Communication	PEC3	3	0	0	3	3
4.	22EC6305	Global Positioning Systems	PEC4	3	0	0	3	3
5.	22EC6306	RF System Design	PEC5	3	0	0	3	3
6.	22EC7303	Software Defined Radio	PEC6	3	0	0	3	3

#### VERTICAL4:Wireless Communication

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5310	Network Security	PEC1	3	0	0	3	3
2.	22EC5311	High Speed Networks	PEC2	3	0	0	3	3
3.	22EC5312	Cloud Computing	PEC3	3	0	0	3	3
4.	22EC6307	Wireless Sensors and Networks	PEC4	3	0	0	3	3
5.	22EC6308	Wireless Broadband Communications	PEC5	3	0	0	3	3
6.	22EC7304	Cyber Forensics	PEC6	3	0	0	3	3

#### VERTICAL5:Business and Management

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5311	Total Quality Management	PEC1	3	0	0	3	3
2.	22EC5312	Principles of Management	PEC2	3	0	0	3	3

**Vertical II**  
**Fintech and Block Chain**

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	22MB5231	Financial Management						
2	22MB6231	Fundamentals of Investment	MDC	3	0	0	3	3
3	22MB6232	Banking, Financial Services and Insurance	MDC	3	0	0	3	3
4	22MB7231	Introduction to Blockchain and its Applications	MDC	3	0	0	3	3
5	22MB7232	Fintech Personal Finance and Payments	MDC	3	0	0	3	3
6	22MB8231	Introduction to Fintech	MDC	3	0	0	3	3

**Vertical III**  
**Entrepreneurship**

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	22MB5232	Foundations of Entrepreneurship	MDC	3	0	0	3	3
2	22MB6233	Team Building & Leadership Management for Business	MDC	3	0	0	3	3
3	22MB6234	Creativity & Innovation in Entrepreneurship	MDC	3	0	0	3	3
4	22MB7233	Principles of Marketing Management For Business	MDC	3	0	0	3	3
5	22MB72334	Human Resource Management for Entrepreneurs	MDC	3	0	0	3	3
6	22MB8232	Financing New Business Ventures	MDC	3	0	0	3	3

**Vertical IV**

**Environment and Sustainability**

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	22CE5232	Sustainable infrastructure Development	MDC	3	0	0	3	3
2	22AG6233	Sustainable Agriculture and Environmental Management	MDC	3	0	0	3	3
3	22BM6233	Sustainable Bio Materials	MDC	3	0	0	3	3
4	22ME7233	Materials for Energy Sustainability	MDC	3	0	0	3	3
5	22CE7233	Green Technology	MDC	3	0	0	3	3
6	22CE8232	Environmental Quality Monitoring and Analysis	MDC	3	0	0	3	3

4.	22EC7205/ 22EC7206	Wearable Devices / Long term Evolution Technologies	PEC4	3	0	0	3	3
5.	22EC7207	Industrial IoT	PEC5	3	0	0	3	3
6.	22EC8202	IoT based System Design	PEC6	3	0	0	3	3

**B E (Hons) Electronics and Communication Engineering with Specialization in Semiconductor Chip Design and Testing - TSSOLVE Semiconductors**

**VERTICAL 3: Semiconductor Chip Design and Testing - TSSOLVE  
Semiconductors**

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5206	Digital System Design	PEC1	3	0	0	3	3
2.	22EC6208	CMOS VLSI Design	PEC2	3	0	0	3	3
3.	22EC6209	Low Power VLSI Design	PEC3	3	0	0	3	3
4.	22EC7208	ASIC Design	PEC4	3	0	0	3	3
5.	22EC7209	System Verilog	PEC5	3	0	0	3	3
6.	22EC8203	VLSI Testing and Design for Testability	PEC6	3	0	0	3	3

*P. Hayler*  
Chairman BoS

**Chairman - BoS  
ECE - HICET**

*[Signature]*  
Dean Academics

**Dean (Academics)  
HICET**

*[Signature]*  
Principal

**PRINCIPAL**  
Hindusthan College Of Engineering & Technology  
COIMBATORE - 641 022

## HONOURS

S NO.	Vertical 1 Emerging Technologies in Communication Engineering	Vertical 2 Sensor Technologies and IoT	Vertical 3 Semiconductor Chip Design and Testing - TSOLVE Semiconductors
1	Tele Communication Switching and Networks	Real-Time Embedded Systems Design	Digital System Design
2	Optical Communication Networks	IOT Processors/ Advanced Processor Architectures	CMOS VLSI Design
3	Wireless Broadband Networks / Digital switching Systems	Computer Vision	Low Power VLSI Design
4	Software Defined Networks	Wearable Devices / Long term Evolution Technologies	ASIC Design
5	4G/5G Communication Networks/ Wireless systems and standards	Industrial IoT	System Verilog
6	Underwater Communication	IoT based System Design	VLSI Testing and Design for Testability

### PROFESSIONAL ELECTIVE COURSES: VERTICALS

**B E (Hons) Electronics and Communication Engineering with Specialization in Emerging Technologies in Communication Engineering**

#### VERTICAL1: Emerging Technologies in Communication Engineering

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5204	Tele Communication Switching and Networks	PEC1	3	0	0	3	3
2.	22EC6202	Optical Communication Networks	PEC2	3	0	0	3	3
3.	22EC6203/ 22EC6204	Wireless Broadband Networks / Digital switching Systems	PEC3	3	0	0	3	3
4.	22EC7202	Software Defined Networks	PEC4	3	0	0	3	3
5.	22EC7203/ 22EC7204	4G/5G Communication Networks/ Wireless systems and standards	PEC5	3	0	0	3	3
6.	22EC8201	Underwater Communication	PEC6	3	0	0	3	3

**B E (Hons) Electronics and Communication Engineering with Specialization in Sensor Technologies and IoT**

#### VERTICAL 2: Sensor Technologies and IoT

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	22EC5205	Real-Time Embedded Systems Design	PEC1	3	0	0	3	3
2.	22EC6205/ 22EC6206	IOT Processors/ Advanced Processor Architectures	PEC2	3	0	0	3	3
3.	22EC6207	Computer Vision	PEC3	3	0	0	3	3

Programme	Course Code	Name of the Course	L	T	P	C
B.E./B.Tech	22MA1101	<b>MATRICES AND CALCULUS</b> (Common to all Branches)	3	1	0	4

The learner should be able to

- Course Objective
1. Construct the characteristic polynomial of a matrix and use it to identify eigenvalues and Eigenvectors
  2. Impart the knowledge of sequences and series.
  3. Analyse and discuss the maxima and minima of the functions of several variables.
  4. Evaluate the multiple integrals and apply in solving problems.
  5. Apply vector differential operator for vector function and theorems to solve engineering problems.

Unit	Description	Instructional Hours
I	<b>Matrices</b> Eigen values and Eigen vectors – Properties of Eigen values and Eigen vectors (without proof) - Cayley - Hamilton Theorem. (excluding proof) - Reduction of a quadratic form to canonical form by orthogonal transformation.	12
II	<b>Single Variate Calculus</b> Rolle's Theorem–Lagrange's Mean Value Theorem–Maxima and Minima–Taylor's and Maclaurin's Series.	12
III	<b>Functions of Several Variables</b> Partial derivatives–Total derivative, Jacobian, Maxima, minima and saddle points; Method of Lagrange multipliers	12
IV	<b>Integral Calculus</b> Double integrals in Cartesian coordinates–Area enclosed by plane curves (excluding surface area)– Triple integrals in Cartesian co-ordinates – Volume of solids (Sphere, Ellipsoid, Tetrahedron) using Cartesian co-ordinates.	12
V	<b>Vector Calculus</b> Gradient, divergence and curl; Green's theorem, Stoke's and Gauss divergence theorem (statement only) for cubes only.	12
<b>Total Instructional Hours</b>		<b>60</b>

At the end of the course, the learner will be able to

- Course Outcome
- CO1: Compute Eigen values and Eigen vectors of the given matrix and transform given quadratic form into canonical form.
- CO2: Apply the concept of differentiation to identify the maximum and minimum values of curve.
- CO3: Compute partial derivatives of function of several variables and write Taylor's series for functions with two variables.
- CO4: Evaluate multiple integral and its applications in finding area, volume.
- CO5: Apply the concept of vector calculus in two and three dimensional spaces.

#### TEXTBOOKS:

T1: G.B. Thomas and R.L. Finney, "Calculus and Analytical Geometry", 9<sup>th</sup> Edition Addison Wesley Publishing Company, 2016.

T2: Erwin Kreyszig, "Advanced Engineering Mathematics", John Wiley & Sons, 2019.

T3: K.P. Uma and S. Padma, "Engineering Mathematics I (Matrices and Calculus)", Pearson Ltd, 2022.

#### REFERENCE BOOKS:

R1- Jerrold E. Marsden, Anthony Tromba, "Vector Calculus", W.H. Freeman, 2003

R2- Strauss M.J, G.L. Bradley and K.J. Smith, "Multivariable Calculus", Prentice Hall, 2002.

R3- Veerarajan T, "Engineering Mathematics", McGraw Hill Education (India) Pvt Ltd, New Delhi, 2016.



*P. Hay*  
Chairman, Board of Studies  
ECE - HICET

*[Signature]*  
Dean - Academics  
**Dean (Academics)**  
**HICET**

Programme	Course Code	Name of the Course	L	T	P	C
B.E/B.Tech	22CY1151	<b>Chemistry for Circuit Engineering</b> (ECE, EEE, EJE, BME, CSE, IT, AIML)	2	0	2	3

**The learner should be able to**

- Course Objective
1. Acquire knowledge on the concepts of chemistry involved in day today life.
  2. Identify the water related problems and water treatment techniques.
  3. Enhance the fundamental knowledge on electrochemistry and the mechanism of corrosion and its control.
  4. Gain knowledge on the nuclear energy source and batteries.
  5. Extend the knowledge on the concepts of spectroscopy and its applications.

Unit	Description	Instructional Hours
I	<b>CHEMISTRY IN EVERYDAY LIFE</b> Chemicals in food – Food colors – Artificial sweeteners – Food preservatives. Soaps and Detergents – Soaps – Types of Soap – Detergents – Types of detergents. Drugs – Classification of drugs - Therapeutic Action of Different Classes of Drugs. Chemicals in Cosmetics – Creams – Talcum powders- Deodorants – Perfumes. Plastics – Thermoplastics- Preparation, properties and uses of PVC, Teflon and Thermosetting plastics - Preparation, properties and uses of Polyester and Polyurethane.	6
II	<b>WATER TECHNOLOGY</b> Impurities in Water, Hardness of Water, Boiler feed Water – Boiler troubles -Sludge and scale formation, Caustic embrittlement, priming and foaming, boiler corrosion- -Softening Methods (Zeolite & Ion-Exchange Methods)- Desalination of Brackish Water - Reverse Osmosis, Potable water and treatment. Estimation of total, permanent and temporary hardness of water by EDTA. Determination of Dissolved Oxygen in sewage water by Winkler's method. Estimation of alkalinity of water sample by indicator method.	6+9
III	<b>ELECTROCHEMISTRY AND CORROSION</b> Electrochemical cells – reversible and irreversible cells - EMF- Single electrode potential – Nernst equation (derivation only) – Conductometric titrations. Chemical corrosion – Pilling – Bedworth rule – electrochemical corrosion – different types –galvanic corrosion – differential aeration corrosion – corrosion control – sacrificial anode and impressed cathodic current methods. Conductometric titration of strong acid vs strong base (HClvsNaOH). Estimation of Ferrous iron by Potentiometry.	6+6
IV	<b>ENERGY SOURCES AND STORAGE DEVICES</b> Introduction- nuclear energy- nuclear fission- controlled nuclear fission- nuclear fusion differences between nuclear fission and fusion- nuclear chain reactions- nuclear reactor power generator- classification of nuclear reactor- light water reactor- breeder reactor. Batteries and fuel cells: Types of batteries- alkaline battery- lead storage battery- lithium ion battery- fuel cell H <sub>2</sub> -O <sub>2</sub> fuel cell applications.	6
V	<b>SPECTROSCOPY</b> Beer-Lambert's law – UV-visible spectroscopy and IR spectroscopy – principles – instrumentation (block diagram only) - applications – flame photometry – principle – instrumentation (block diagram only) – estimation of sodium by flame photometry – atomic absorption spectroscopy – principles – instrumentation (block diagram only) – Estimation of nickel by atomic absorption spectroscopy.	6
<b>Total Instructional Hours</b>		<b>45</b>

At the end of the course, the learner will be able to

- CO1: List out the chemicals used in food, soaps and detergents, drugs, cosmetics and plastics  
CO2: Differentiate hard and soft water and solve the related problems on water purification in domestic as well as in industries.  
CO3: Develop knowledge on the basic principles of electrochemistry and understand the causes of corrosion, its consequences to minimize corrosion to improve industrial design  
CO4: Develop knowledge about the renewable energy resources and batteries along with the need of new materials to improve energy storage capabilities  
CO5: List out the applications of spectroscopic techniques in various engineering fields.

Course Outcome

**TEXT BOOKS**

- T1 - P.C.Jain & Monica Jain, "Engineering Chemistry" Dhanpat Rai Pub, Co., New Delhi (2018).  
T2 - O.G.Palanna, "Engineering chemistry" McGraw Hill Education India (2017).

**REFERENCES**

- R1 - Shikha Agarwal "Engineering Chemistry -Fundamentals and Applications, Cambridge University Press, Delhi, 2019  
R2 - S.S.Dara "A Text book of Engineering Chemistry" S.Chand & Co. Ltd., New Delhi (2018).



*P. Hayde*  
Chairman, Board of Studies

**Chairman - BoS**  
**ECE - HICET**

*[Signature]*  
Dean Academics

**Dean (Academics)**  
**HICET**

The student should be able

Course Objective

1. To improve the communicative proficiency of learners.
2. To help learners use language effectively in professional writing.
3. To advance the skills of maintaining the suitable one of communication.
4. To introduce the professional life skills.
5. To impart official communication etiquette.

Unit	Description	Instructional Hours
I	<b>Language Proficiency:</b> Types of Sentences, Functional Units, Framing question. <b>Writing:</b> process description, Writing Checklist. <b>Vocabulary</b> – words on environment. <b>Practical Component: Listening-</b> Watching short videos and answer the questions, <b>Speaking-</b> Self introduction, formal & semi-formal, <b>Reading-</b> Purpose of Reading - Churning & Assimilation, Interpreting Ideas - Interpreting Graphs in Technical Writing.	7+2
II	<b>Language Proficiency:</b> Tenses, Adjectives and adverbs. <b>Writing:</b> Formal letters (letters conveying positive and negative news), Formal and informal email writing (using emoticons, abbreviations & acronyms), reading comprehension. <b>Vocabulary</b> – words on entertainment. <b>Practical Component: Listening-</b> Comprehensions based on TED talks <b>Speaking-</b> Narrating a short story or an event happened in their life <b>Reading</b> - Skimming – Scanning – Reading: Scientific Texts – Literary Texts	7+2
III	<b>Language Proficiency:</b> Prepositions, phrasal verbs. <b>Writing:</b> Formal thanks giving, Congratulating, warning and apologizing letters, cloze test. <b>Vocabulary</b> – words on tools. <b>Practical Component: Listening-</b> Listen to songs and answer the questions <b>Speaking-</b> Just a minute <b>Reading-</b> Reading feature articles (from newspapers and magazines) - Reading to identify point of view and perspective (opinion pieces, editorials etc.)	5+4
IV	<b>Language Proficiency:</b> Subject verb concord, Prefixes & suffixes. <b>Writing:</b> Preparing agenda & minutes, writing an event report. <b>Vocabulary</b> – words on engineering process. <b>Practical Component: Listening-</b> Comprehensions based on Talk of orators or interview shows <b>Speaking-</b> Presentation on a general topic with ppt. <b>Reading-</b> Reading Comprehension - Techniques for Good Comprehension - Sequencing of Sentences.	5+4
V	<b>Language Proficiency:</b> Modal Auxiliaries, Active & passive voice, <b>Writing:</b> Project report (proposal & progress), sequencing of sentences <b>Vocabulary</b> – words on engineering material <b>Practical Component: Listening-</b> Listening- Comprehensions based on Nat Geo/Discovery channel videos <b>Speaking-</b> Preparing posters and presenting as a team. <b>Reading-</b> Biographies, travelogues, technical blogs.	6+3
<b>Total Instructional Hours</b>		<b>45</b>

Course Outcome

- After completion of the course the learner will be able
- CO1: To communicate in a professional forum
  - CO2: To speak or write a content in the proficient language
  - CO3: To maintain and use appropriate one of the communication.
  - CO4: To read, write and present in a professional way.
  - CO5: To follow the etiquettes in formal communication.

**TEXTBOOKS:**

- T1- Norman Whitby, "Business Benchmark-Pre-intermediate to Intermediate", Cambridge University Press, 2016. T2- Raymond Murphy, "Essential English Grammar", Cambridge University Press, 2019.

**REFERENCEBOOKS:**

- R1- Meenakshi Raman and Sangeetha Sharma. "Technical Communication- Principles and Practice", Oxford University Press, 2009.  
 R2- Raymond Murphy, "English Grammar in Use"-4<sup>th</sup> edition Cambridge University Press, 2004.  
 R3- Kamalesh Sadanan "A Foundation Course for the Speakers of Tamil-Part-I&II", Orient Blackswan, 2010.

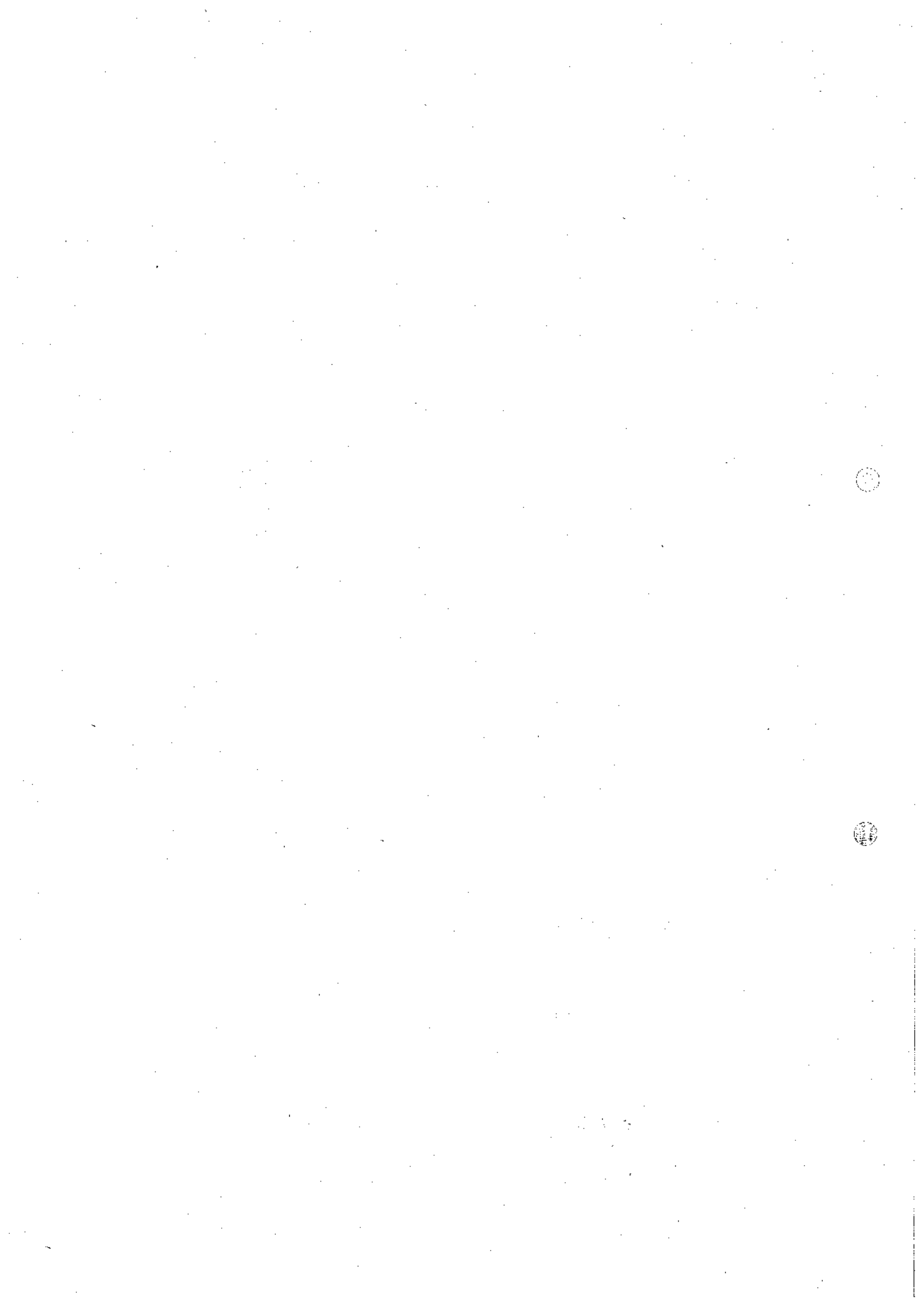


*P. Hayk*  
Chairman, Board of Studies

**Chairman - BOS**  
**ECE - HICET**

*[Signature]*  
Dean - Academics

Dean - Academics  
[Signature]





**The learner should be able**

- Course Objective
1. To know the basics of algorithmic problem solving
  2. To read and write simple Python programs
  3. To develop Python programs with conditionals and loops and to define Python functions and call them
  4. To use Python data structures — lists, tuples, dictionaries
  5. To do input/output with files in Python

Unit	Description	Instructional Hours
I	<b>ALGORITHMIC PROBLEM SOLVING</b> Algorithms, building blocks of algorithms (statements, state, control flow, functions), notation (pseudo code, flow chart, programming language), algorithmic problem solving, simple strategies for developing algorithms (iteration, recursion). <b>Illustrative problems: To find the Greatest Common Divisor (GCD) of two numbers, Fahrenheit to Celsius, Perform Matrix addition.</b>	5+4
II	<b>DATA, STATEMENTS, CONTROL FLOW</b> Data Types, Operators and precedence of operators, expressions, statements, comments; Conditionals: Boolean values and operators, conditional (if), alternative (if-else), chained conditional (if-elif-else); Iteration: state, while, for, break, continue, pass; <b>Simple algorithms and programs: Area of the circle, check the given year is Leap year or not, Factorial of a Number.</b>	5+4
III	<b>FUNCTIONS, STRINGS</b> Functions, parameters and arguments; Fruitful functions: return values, local and global scope, function composition, recursive functions. Strings: string slices, immutability, string functions and methods, string module. <b>Illustrative programs: Perform Linear Search, Selection sort, Sum of all elements in a List, Pattern Programs</b>	5+4
IV	<b>LISTS, TUPLES, DICTIONARIES</b> Lists: list operations, list slices, list methods, list loop, mutability, aliasing, cloning lists, list parameters; Tuples: tuple assignment, tuple as return value; Dictionaries: operations and methods; advanced list processing - list comprehension. <b>Illustrative programs: List Manipulation, Finding Maximum in a List, String processing.</b>	5+4
V	<b>FILES, MODULES, PACKAGES</b> Files and exception: text files, reading and writing files, errors and exceptions, handling exceptions, modules, packages <b>Illustrative programs: Reading writing in a file, word count, Handling Exceptions</b>	9
<b>Total Instructional Hours</b>		<b>45</b>

Course Outcome

At the end of the course, the learner will be able to

CO1: Develop algorithmic solutions to simple computational problems  
 CO2: Read, write, execute by hand simple Python programs  
 CO3: Structure simple Python programs for solving problems and Decompose a Python program into functions  
 CO4: Represent compound data using Python lists, tuples, dictionaries  
 CO5: Read and write data from/to files in Python Programs.

**TEXT BOOKS:**

T1: Guido van Rossum and Fred L. Drake Jr, An Introduction to Python – Revised and updated for Python 3.6.2, Shroff Publishers, First edition (2017).  
 T2: S. Annadurai, S. Shankar, I. Jasmine, M. Revathi, Fundamentals of Python Programming, Mc-Graw Hill Education (India) Private Ltd, 2019.

**REFERENCE BOOKS:**

R1: Charles Dierbach, —Introduction to Computer Science using Python: A Computational Problem- Solving Focus, Wiley India Edition, 2013.  
 R2: Timothy A. Budd, —Exploring Python!, Mc-Graw Hill Education (India) Private Ltd., 2015  
 R3: Robert Sedgewick, Kevin Wayne, Robert Dondero, —Introduction to Programming in Python: An Inter-disciplinary Approach, Pearson India Education Services Pvt. Ltd., 2016

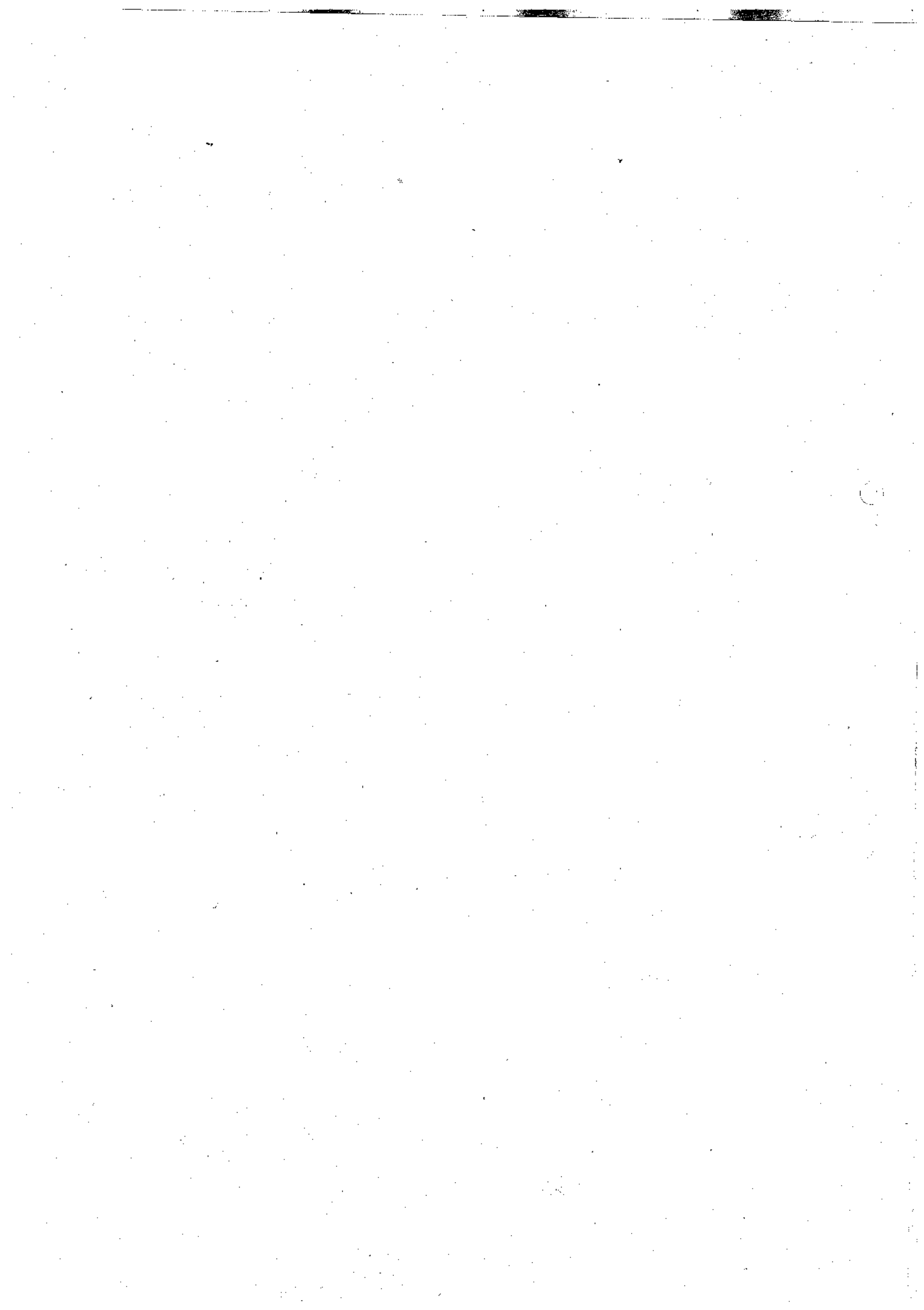
*P. Hayla*  
 Chairman, Board of Studies

**Chairman - BOS  
 ECE - HICET**



*[Signature]*  
 Dean - Academics

**Dean (Academics)  
 HICET**



Programme	Course Code	Name of the Course	L	T	P	C
B.E./B.Tech	22CS1152	<b>OBJECT ORIENTED PROGRAMMING USING PYTHON (CSE, IT, ECE &amp; AIML)</b>	2	0	2	3

**The learner should be able**

Course Objective

1. To read and write simple Python programs.
2. To develop Python programs with conditionals and loops.
3. To define Python functions and call them.
4. To understand OOP concepts and write programs using classes and objects.
5. To do input/output with files in Python.

Unit	Description	Instructional Hours
I	<b>INTRODUCTION TO PYTHON</b> What is Python - Advantages and Disadvantages, Benefits and Limitation- Downloading and Python-installation-Python Versions-Running Python Scripts, Executing scripts with python launcher-Using interpreter interactively- Using variables-String types: normal, raw and Unicode-String operations and functions- Math operator and functions.Illustrative program: find minimum in a list, insert a card in a list of sorted cards, guess an integer number in a range, Towers of Hanoi.	7+2
II	<b>DATA TYPES, STATEMENTS, CONTROL FLOW</b> Data Types(List,Tuple,string,dictionary,set)-Operators and precedence of operators, expressions, statements, comments; Conditionals: Boolean values and operators, conditional (if), alternative (if -else), chained conditional (if -elif-else); Iteration: state, while, for, break, continue, pass. Illustrative programs:Find the square root of a number, To find the given number is Prime or not, Write a Python program which accepts a sequence of comma-separated numbers from user, generate a list and find the sum and average of the numbers.	5+4
III	<b>PYTHON FUNCTIONS</b> Introduction to functions-Global and local variable in python-Decorators in python-Python lambda functions-Exception handling in python. Illustrative programs:Square root, GCD,exponentiation, linear search, binary search, Write a menu driven program to perform the following task:a) A function Sum_DigN() to find the sum of the digits of a given number, b) A recursive function Sum_DigR() to find the same.	5+4
IV	<b>PYTHON OOPS</b> Introduction to oops concept-Python class and objects-Constructor in python-Inheritance-Types of inheritance-Encapsulation in python-Polymorphism in python. Illustrative programs:Write a Python program using class for the calculation of telephone bill. The charges for the calls are fixed as follows: Unit Call Cost/unit Below 100 calls No Charge, only rental amount Rs. 250 100-150 calls Rs. 1.00 151-300 calls Rs. 2.50 301-600 calls Rs. 4.50 Above 600 Rs. 6.00	5+4
V	<b>FILES, PACKAGES</b> File handling in python-Open a file in python-How to read from a file in python-writing to file in python-Python numpy-Python pandas. Illustrative programs:How to display the contents of text file in reverse order? Write the code for the same, not exceeding 10 lines of code, Creating Modules and Packages for arithmetic Operations.	5+4
<b>Total Instructional Hours</b>		<b>45</b>

At the end of the course, the learner will be able to

Course Outcome

- CO1: Understanding the basic concepts to read, write and execute simple python programs.
- CO2: Apply the conditional and looping concepts for solving problems.
- CO3: Apply functions to decompose larger complex programs.
- CO4: Understanding the OOPS concepts and writing programs using classes and objects
- CO5: Understand to read and write data from/to files in Python Programs.

**TEXT BOOKS:**

T1: Guido van Rossum and Fred L. Drake Jr, An Introduction to Python – Revised and updated for Python 3.2, Network Theory Ltd., 2011.

**REFERENCE BOOKS:**

- R1: Charles Dierbach, —Introduction to Computer Science using Python: A Computational Problem-Solving Focus, Wiley India Edition, 2013.
- R2: Timothy A. Budd, —Exploring Python, Mc-Graw Hill Education (India) Private Ltd., 2015.
- R3: Robert Sedgewick, Kevin Wayne, Robert Dondero, —Introduction to Programming in Python: An Inter-disciplinary Approach, Pearson India Education Services Pvt. Ltd., 2016

*P. Jayaram*  
Chairman, Board of Studies  
**Chairman - BoS  
ECE - HICET**



*[Signature]*  
Dean - Academics

JSSR INSTITUTE OF ENGINEERING & TECHNOLOGY  
MYSORE

9

9

Programme	Course Code	Name of the Course	L	T	P	C
B.E./B.Tech	22HE1071	<b>UNIVERSAL HUMAN VALUES</b> (COMMON TO ALL BRANCHES)	2	0	0	2
Course Objective	<ol style="list-style-type: none"> <li>To help the students appreciate the essential complementarity between 'VALUES' and 'SKILLS' to ensure sustained happiness and prosperity which are the core aspirations of all human beings.</li> <li>To facilitate the development of a Holistic perspective among students towards life and profession as well as towards happiness and prosperity based on a correct understanding of the Human reality and the rest of existence. Such a holistic perspective forms the basis of Universal Human Values and movement towards value-based living in a natural way.</li> <li>To highlight plausible implications of such a Holistic understanding in terms of ethical human conduct, trustful and mutually fulfilling human behavior and mutually enriching interaction with Nature.</li> </ol>					
Unit	Description					Instructional Hours
I	<b>Introduction to Value Education</b> Right Understanding, Relationship and Physical Facility (Holistic Development and the Role of Education)-Understanding Value Education - Self-exploration as the Process for Value Education - Continuous Happiness and Prosperity – the Basic Human Aspirations - Happiness and Prosperity – Current Scenario- Method to Fulfill the Basic Human Aspirations					6
II	<b>Harmony in the Human Being and Harmony in the Family</b> Understanding Human being as the Co-existence of the Self and the Body - Distinguishing between the Needs of the Self and the Body - The Body as an Instrument of the Self - Understanding Harmony in the Self- Harmony of the Self with the Body - Programme to ensure self-regulation and Health					6
III	<b>Harmony in the Family and Society</b> Harmony in the Family – the Basic Unit of Human Interaction.Values in Human to Human Relationship'Trust' – the Foundational Value in Relationship Values in Human to Human Relationship'Respect' – as the Right Evaluation-Understanding Harmony in the Society					6
IV	<b>Harmony in the Nature / Existence</b> Understanding Harmony in the Nature.Interconnectedness, self-regulation and Mutual Fulfilment among the Four Orders of Nature- Understanding Existence as Co-existence of mutually interacting units in all pervasivespaceRealizing Existence as Co-existence at All LevelsThe Holistic Perception of Harmony in Existence. Vision for the Universal Human Order					6
V	<b>Implications of the Holistic Understanding – a Look at Professional Ethics</b> Natural Acceptance of Human ValuesDefinitiveness of (Ethical) Human Conduct A Basis for Humanistic Education, Humanistic Constitution and Universal Human Order-Competence in Professional Ethics Holistic Technologies, Production Systems and Management Models-Typical CaseStudiesStrategies for Transition towards Value-based Life and Profession					6
<b>Total Instructional Hours</b>						<b>30</b>
Course Outcome	CO1: To become more aware of holistic vision of life - themselves and their surroundings. CO2: To become more responsible in life, in the Society and in handling problems with sustainable Solutions. CO3: To sensitive towards their commitment towards what they understood towards					

*P. Hayle*  
CHAIRPERSON/BOS

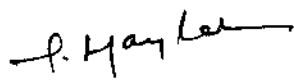
**Chairman - BoS**  
**ECE - HiCET**



DEAN Academics

**Dean (Academics)**  
**HiCET**

	<p>environment and Socially responsible behavior. CO4: To able to apply what have learnt to their own self in different day-to-day settings in real life and In handling problems with sustainable solutions. CO5: To develop competence and capabilities for maintaining Health and Hygiene.</p>
<p><b>Reference Books:</b> R1. <i>A Foundation Course in Human Values and Professional Ethics</i>, R R Gaur, R Asthana, G P Bagaria, 2<sup>nd</sup> Revised Edition, Excel Books, New Delhi, 2019. ISBN 978-93-87034-47-1 R2. <i>Teachers' Manual for A Foundation Course in Human Values and Professional Ethics</i>, R R Gaur, R Asthana, G P Bagaria, 2<sup>nd</sup> Revised Edition, Excel Books, New Delhi, 2019. ISBN 978-93-87034-53-2 R3. <i>Jeevan Vidya: Ek Parichaya</i>, A Nagaraj, Jeevan Vidya Prakashan, Amarkantak, 1999. R4. <i>Human Values</i>, A.N. Tripathi, New Age Intl. Publishers, New Delhi, 2004.</p>	

  
CHAIRPERSON/BOS  
Chairman - BoS  
ECE - HICET



  
DEAN Academics  
Dean (Academics)  
HICET

Programme	Course Code	Name of the Course	L	T	P	C
B.E./B.Tech	22HE1072	ENTREPRENEURSHIP & INNOVATION	1	0	0	1

(Common for all Branches)

The student should be made

**Course Objectives**

1. To acquire the knowledge and skills needed to manage the development of innovation.
2. To recognize and evaluate potential opportunities to monetize these innovations.
3. To plan specific and detailed method to exploit these opportunities.
4. To acquire the resources necessary to implement these plans.
5. To make students understand organizational performance and its importance.

**Module**

**Description**

- |    |   |
|----|---|
| 1  | Entrepreneurial Thinking                      |
| 2  | Innovation Management                         |
| 3  | Design Thinking                               |
| 4  | Opportunity Spotting / Opportunity Evaluation |
| 5  | Industry and Market Research                  |
| 6  | Innovation Strategy and Business Models       |
| 7  | Financial Forecasting                         |
| 8  | Business Plans/ Business Model Canvas         |
| 9  | Entrepreneurial Finance                       |
| 10 | Pitching to Resources Providers / Pitch Deck  |
| 11 | Negotiating Deals                             |
| 12 | New Venture Creation                          |
| 13 | Lean Start-ups                                |
| 14 | Entrepreneurial Ecosystem                     |
| 15 | Velocity Venture                              |

**TOTAL INSTRUCTIONAL HOURS 15**

At the end of the course, the learner will be able to

**Course Outcome**

- CO1: Understand the nature of business opportunities, resources, and industries in critical and creative aspects.
- CO2: Understand the processes by which innovation is fostered, managed, and commercialized.
- CO3: Remember effectively and efficiently the potential of new business opportunities.
- CO4: Assess the market potential for a new venture, including customer need, competitors, and industry attractiveness.
- CO5: Develop a business model for a new venture, including revenue, Margins, operations, Working capital, and investment

**TEXTBOOKS**

- T1: Arya Kumar "Entrepreneurship—Creating and leading an Entrepreneurial Organization", Pearson, Second Edition (2012).
- T2: Emrah Yayici "Design Thinking Methodology", Artbiztech, First Edition (2016).

**REFERENCEBOOKS**

- R1: Christopher Golis "Enterprise & Venture Capital", Allen & Unwin Publication, Fourth Edition (2007).
- R2: Thomas Lockwood & Edger Papke "Innovation by Design", Career Press.com, Second Edition (2017).
- R3: Jonathan Wilson "Essentials of Business Research", Sage Publication, First Edition (2010).

**WEBRESOURCES**

- W1: <https://blof.forgeforward.in/tagged/startup-lessons>
- W2: <https://blof.forgeforward.in/tagged/entrepreneurship>
- W3: <https://blof.forgeforward.in/tagged/minimum-viable-product>
- W4: <https://blof.forgeforward.in/tagged/minimum-viable-product>
- W5: <https://blof.forgeforward.in/tagged/innovation>



*P. Yashwanth*  
 Chairman, Board of Studies  
 Chairman - BoS  
 HICET

*[Signature]*  
 Dean - Academics

**Dean (Academics)  
 HICET**





Programme	Course Code	Name of the Course	L	T	P	C
B.E	22MA3102	COMPLEX ANALYSIS AND TRANSFORMS (EEE, EIE, ECE)	3	1	0	4

The learner should be able to

- Course Objective**
1. Introduction to analytic functions and its properties.
  2. Understand Cauchy's theorem and its applications in evaluation of integral.
  3. Analyze Fourier series which is central to many applications in engineering
  4. Apply Fourier transform techniques in various situations.
  5. Analyze Z transform techniques for discrete time systems

Unit	Description	Instructional Hours
I	<b>COMPLEX DIFFERENTIATION</b> Functions of complex variables – Analytic functions – Cauchy's – Riemann equations and sufficient conditions (excluding proof) – Construction of analytic functions – Milne –Thomson's method – Conformal mapping $w = A+z$ , $Az$ , $1/z$ and bilinear transformations.	12
II	<b>COMPLEX INTEGRATION</b> Cauchy's integral theorem – Cauchy's integral formula –Taylor's and Laurent's series (statement only) –Residues - Cauchy's Residue theorem - Contour Integration with unit circle only.	12
III	<b>FOURIER SERIES</b> Dirichlet's conditions- General Fourier Series – Odd and Even Functions – Change of Interval - Parseval's Identity - Half Range Sine and Cosine Series.- Harmonic analysis	12
IV	<b>FOURIER TRANSFORMS</b> Fourier Transform Pairs - Fourier Sine and Cosine transforms – Properties - Transforms of Simple functions – Convolution Theorem (Statement only) – Parseval's identity (Statement only).	12
V	<b>Z - TRANSFORMS AND DIFFERENCE EQUATIONS</b> Z- Transforms - Elementary properties – Inverse Z - transform (using partial fraction and residues) – Convolution theorem( excluding proof)– Solution of difference equations using Z – transform	12
<b>Total Instructional Hours</b>		<b>60</b>

At the end of the course, the learner will be able to

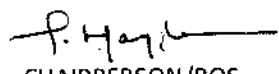
- Course Outcome**
- CO1: Understand the concept of analytic functions and discuss its properties.  
CO2: Evaluate various integrals by using Cauchy's residue theorem and classify singularities and derive Laurent series expansion  
CO3: Understand the principles of Fourier series which helps them to solve physical problems of Engineering  
CO4: Apply Fourier transform techniques which extend its applications.  
CO5: Illustrate the Z- transforms for analyzing discrete-time signals and systems

**TEXT BOOKS:**

- T1 – Erwin Kreyszig, Advanced Engineering Mathematics, John Wiley & Sons, 2019.  
T2 - Veerarajan T, "Engineering Mathematics", McGraw Hill Education (India) Pvt Ltd, New Delhi, 2016.

**REFERENCE BOOKS:**

- R1 - James Ward Brown, Ruel Vance Churchill, Complex Variables and Applications, McGraw-Hill Higher Education, 2004  
R2 - Dennis Zill, Warren S. Wright, Michael R. Cullen, Advanced Engineering Mathematics, Jones & Bartlett Learning, 2011  
R3 - Ian N. Sneddon, Elements of Partial Differential Equations, Courier Corporation, 2013

  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**



  
DEAN/PRINCIPAL

**Dean (Acad)**  
**HICET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	22EC3202	Signals and Systems	3	1	0	3

- Course Objective**
- To understand the basic signals and their properties.
  - To learn the mathematical tool of Fourier series and transforms.
  - To understand the concept of system analysis using Laplace transforms.
  - To understand the discrete signal analysis using transforms.
  - To know discrete system analysis using Z –transform.

Unit	Description	Instructional Hours
I	<b>SIGNALS AND SYSTEM REPRESENTATION &amp; CLASSIFICATION</b> Standard signal representation –continuous and discrete domain. Sampling: Nyquist theorem, Representation of CT signals by samples, Reconstruction of CT signal from samples Mathematical operation on signals, classification of signals and system -analog and discrete.	12
II	<b>CONTINUOUS TIME (CT) SIGNALS</b> Fourier series analysis-Trigonometric form, spectrum of continuous time (CT) signals- Fourier and Laplace transform of standard signals-Region of Convergence (ROC).Inverse Fourier and Laplace transform–partial fraction method, Properties.	12
III	<b>LINEAR TIME INVARIANT- CONTINUOUS TIME (CT) SYSTEMS</b> Block diagram representation of system- Direct form I & II. Applying Fourier and Laplace transform : Transfer function ,impulse response and Frequency response of CT system , Convolution integrals-Integral & Graphical method.	12
IV	<b>DISCRETE TIME SIGNALS</b> DTFT and Inverse DTFT – properties of DTFT - z transform and Inverse z-transform – Region of Convergence, properties of z transform. Convolution sum–Graphical and Matrix method.	12
V	<b>LINEAR TIME INVARIANT-DISCRETE TIME SYSTEMS</b> Block diagram representation of system- Direct form I & II structure .DTFT and Z transform analysis of systems: Transfer function, impulse response, system response and Frequency response, Convolution and de-convolution	12
<b>Total Instructional Hours</b>		<b>60</b>

- Course Outcome**
- CO1 :Understand the signal and system classification and properties
  - CO2: Understand signal spectrum and apply Fourier series to continuous signal spectrum.
  - CO3: Apply Fourier and Laplace transform in LTI system analysis.
  - CO4: Apply DTFT to understand the properties of discrete time signals.
  - CO5: Apply Z-transform for discrete system analysis.

**TEXT BOOKS:**

- T1 - Allan V. Oppenheim, S. Willsky and S.H. Nawab, "Signals and Systems", Pearson, 2007.
- T2 - P Ramakrishna Rao, "Signals and System", Tata McGraw-Hill Education, 2010.

**REFERENCE BOOKS:**

- R1 - M.J. Roberts, "Signals & Systems Analysis using Transform Methods & MATLAB", McGraw Hill, 2017.
- R2 - B. P. Lathi, "Principles of Linear Systems and Signals", Second Edition, Oxford, 2009.
- R3 - Ramesh Babu. P and Anandanatarajan, "Signals and Systems", Fifth edition, Scitech publications, 2017.
- R4 - Nagoor Kani, "Signals and Systems, Simplified", McGrawHill Publication, 2018.

*P. Jayaram*  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academic)**  
**HICET**

Programme	Course Code	Name of the Course	L	T	P	C
BE	22EC3201	Electronic Circuits	3	0	0	3

- Course Objective**
1. To learn about biasing of BJT and JFET circuits.
  2. To study the behavior of small signal amplifiers using BJT.
  3. To provide an insight on the large signal amplifiers and linear wave shaping circuits.
  4. To impart knowledge on feedback amplifiers.
  5. To discuss the operating principles of oscillators and multivibrators.

Unit	Description	Instructional Hours
I	<b>BIASING OF BJT AND FET</b> BJT- Need for biasing, DC Load Line and Bias Point – Various biasing methods of BJT – Thermal stability – Stability factors – Bias compensation techniques using Diode, thermistor and sensistor – Biasing BJT Switching Circuits- JFET – DC Load Line and Bias Point – Various biasing methods of JFET – MOSFET Biasing – Biasing FET Switching Circuit.	9
II	<b>SMALL SIGNAL AMPLIFIERS</b> h-parameter small-signal equivalent circuit –Midband analysis of single stage, CE amplifiers - Low frequency response of CE amplifiers - High frequency $\pi$ model -High frequency response of CE amplifiers, Multistage amplifiers -Darlington Amplifier.	9
III	<b>LARGE SIGNAL AMPLIFIERS AND LINEAR WAVE SHAPING CIRCUITS</b> Classification of large signal amplifiers –Class A , Class B amplifier – Cross over Distortion - Push-Pull amplifier – complementary symmetry push-pull amplifier, Tuned amplifiers -Class C tuned amplifier -Integrator- Differentiator- Clippers- Clampers- Diode comparator .	9
IV	<b>FEEDBACK AMPLIFIERS</b> Block diagram, Loop gain, Gain with feedback, Effects of negative feedback. Sensitivity and desensitivity of gain, Cut-off frequencies, distortion, noise, input impedance and output impedance with feedback. Four types of negative feedback connections - voltage series feedback, voltage shunt feedback, current series feedback and current shunt feedback.	9
V	<b>OSCILLATORS AND MULTIVIBRATORS</b> Classification of oscillator, Barkhausen Criterion - Mechanism for start of oscillation and stabilization of amplitude. General form of an Oscillator, Analysis of Hartley, Colpitt's, RC phase shift and Wien bridge Oscillator- Astable multivibrator–Monostable multivibrator and Bistable multivibrator.	9
<b>Total Instructional Hours</b>		<b>45</b>

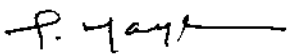
- Course Outcome**
- CO1: Understand various biasing circuit for BJT and JFET amplifiers and apply in solving the problems  
CO2: Understand the low frequency and high frequency response of BJT amplifiers using small signal equivalent circuit.  
CO3: Understand the operation of various types of large signal amplifiers and linear wave shaping circuits  
CO4: Understand the different types of feedback amplifiers with examples  
CO5: Understand various types of oscillators and multivibrators and their applications

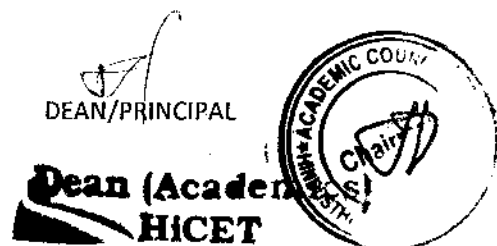
**TEXT BOOKS:**

- T1- S.Salivahanan, N.Suresh Kumar and A.Vallavaraj, "Electronic Devices and Circuits", 3rd Edition, 2012, McGraw Hill. (All units)  
T2- Donald .A. Neamen, "Electronic Circuit Analysis and Design" ,3 rd edition, Tata McGraw Hill, 2010( Unit IV)

**REFERENCE BOOKS:**

- R1- Robert L. Boylestad, Louis Nasheisky, "Electronic Devices and Circuit Theory", 9<sup>th</sup> Edition, 2007.  
R2- Jacob Millman, Christos C. Halkias, "Electronic Devices and Circuits" McGraw Hill , Edition 1991.  
R3- D.Schilling and C.Belove, "Electronic Circuits", 3rd Edition, McGraw Hill, 1989.  
R4- David A. Bell, "Electronic Devices and Circuits", fifth edition, Oxford Higher education

  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	22EC3203	Digital Electronics	3	0	0	3

- Course Objective**
1. To impart knowledge on different methods used for the simplification of Boolean functions
  2. To explain the working of various combinational circuits
  3. To gain knowledge about synchronous sequential circuits.
  4. To gain knowledge about asynchronous sequential circuits.
  5. To impart knowledge on different types of memories.

Unit	Description	Instructional Hours
	<b>DIGITAL FUNDAMENTALS</b>	
I	Boolean operation and expressions- Laws and rules of Boolean algebra -Simplification using Boolean algebra - Sum of Products (SOP) - Product of Sums (POS)- Karnaugh map Minimization- Quine - McCluskey method of minimization- Logic Gates- NAND-NOR implementations.	9
	<b>COMBINATIONAL CIRCUIT DESIGN</b>	
II	Analysis and design of combinational circuits - Circuits for arithmetic operations: adder, subtractor, Carry look ahead adder-BCD adder-Magnitude Comparator-Encoders and Decoders-Multiplexers and Demultiplexers, Parity checker and generators.	9
	<b>SYNCHRONOUS SEQUENTIAL CIRCUITS</b>	
III	Latches- Flip-flops- SR, JK, D, T, and Master-Slave - Edge triggering - Level Triggering-Analysis and design of synchronous sequential circuits: State diagram - State table - State minimization - State assignment, Synchronous Up/Down counters, mod n counters, Shift registers, Universal shift registers.	9
	<b>ASYNCHRONOUS SEQUENTIAL CIRCUITS</b>	
IV	Analysis and design of asynchronous sequential circuits - Reduction of state and flow tables - Race-free state assignment - Hazards.	9
	<b>MEMORY DEVICES AND DIGITAL INTEGRATED CIRCUITS</b>	
V	Classification of memories, Read/write operations- Memory decoding and expansion, Static and Dynamic RAM- PLDs- Architecture and implementation - Digital logic families -Characteristics - TTL, ECL and CMOS logic.	9
<b>Total Instructional Hours</b>		<b>45</b>

- Course Outcome**
- CO1: Able to apply the concepts of Boolean theorem to simplify Boolean expressions  
CO2: Able to understand the working of various combinational circuits.  
CO3: Able to apply the concepts to in various synchronous sequential circuits.  
CO4: Able to apply the concepts to in various asynchronous sequential circuits.  
CO5: Understand the organization of memories and PLDs.

**TEXT BOOKS:**

- T1- M. Morris Mano and Michael D. Ciletti, "Digital Design", 5th Edition, Pearson, 2013.(Unit 1,Unit 2,Unit3,Unit 4, Unit 5)  
T2-Thomas L. Floyd, "Digital Fundamentals", 10th Edition, Pearson Education Inc, 2011.

**REFERENCE BOOKS:**

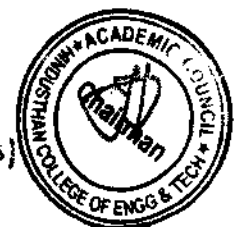
- R1- A.Anandkumar, "Fundamentals of Digital Electronics", fourth edition ,PHI Learning Pvt. Ltd,2016.  
R2- S.Salivahanan and S.Arivazhagan, "Digital Circuits and Design" ,Vikas publishing House Pvt. Ltd ,2013

*P. Havel*  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*J. S.*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**



Programme	Course code	Name of the course	L	T	P	C
B.E.	22EC3251	OBJECT ORIENTED PROGRAMMING USING JAVA	2	0	2	3

**The student should be able**

- Course Objective**
- 1 To understand the concepts of Object Oriented Programming
  - 2 To impart the fundamental concepts of core JAVA.
  - 3 To enable the students to gain programming skills in JAVA.
  - 4 To know how to handle exceptions.
  - 5 To understand multithread programming logic

Unit	Description	Instructional Hours
	<b>INTRODUCTION TO OBJECT ORIENTED PROGRAMMING</b>	
I	Object oriented programming concepts – objects-classes- methods and messages- abstraction and encapsulation-inheritance- abstract classes- polymorphism-Benefits of OOP, Application of OOP-Java Evolution-Features of Java-Difference of Java from C and C++.	15
	<b>OVERVIEW OF JAVA LANGUAGE</b>	
II	Basics of Java programming, Data types, constants -Variables and Arrays, Operators and expressions , Decision making and branching –looping –Classes, Objects and Methods- access specifiers – static members –Constructors-this keyword-finalize method	15
	<b>PACKAGES AND INTERFACES</b>	
III	Java API Packages –Naming conventions-creating, accessing, using Packages-Inheritance– Method Overriding- Abstract class Interfaces: Multiple inheritance-defining, extending, implementing interfaces- -final keyword	15
	<b>EXCEPTION HANDLING</b>	
IV	Fundamentals-Exception types –Uncaught exceptions-Using try and catch-Multiple Catch-Nested try-Throws-Finally-Built in Exceptions-Throwing own exceptions	15
	<b>MULTITHREAD PROGRAMMING</b>	
V	Creating Threads- Extending thread class-Stopping and Blocking Thread-Life cycle –Using Thread-Thread Exceptions-Thread priority-Synchronization-Runnable Interface-Inter thread communication	15
	<b>Total Instructional Hours</b>	<b>60</b>

**S.No List of Experiments**

- 1 Ramu went to a restaurant to had his meals. He is charged with Rs. 70.50. The tax should be 8% of the meal cost. The tip should be 10% of the total after adding the tax. Write a java program to display the mealcost,taxamount, tip amount, and total billon the screen.
- 2 Rhea Pandey's teacher has asked her to prepare well for the lesson on seasons. When her teacher tells a month, she needs to say the season corresponding to that month. Write a java program to solve the above task.  
Spring – March to May, Summer – June to August,  
Autumn – September to November and,  
Winter – December to February.  
Month should be in the range 1 to 12. If not the output should be "Invalid month"  
Write a Java program to find the eligibility of admission for a professional course based on the following criteria:  
Eligibility Criteria:  
Marks in Maths >=65 and Marks in Phy >=55 and Marks in Chem >=50 and  
Total in all three subject >=190 or Total in Maths and Physics >=140  
Input the marks obtained in Physics :65  
Input the marks obtained in Chemistry :51  
Input the marks obtained in Mathematics :72  
Total marks of Maths, Physics and Chemistry: 188  
Total marks of Maths and Physics: 137

*P. Jayakumar*  
CHAIRPERSON/BOS  
Chairman - HICE  
ECE - HICE

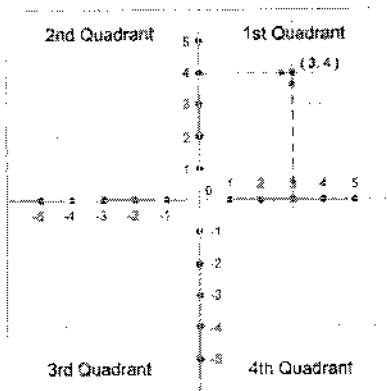


*[Signature]*  
DEAN/PRINCIPAL  
Dean (Ac)  
HICE



The candidate is not eligible.

Write a Java program to accept a coordinate point in a XY coordinate system and determine in which quadrant the coordinate point lies.



XYZ Technologies is in the process of increment the salary of the employees. This increment is done based on their salary and their performance appraisal rating.

If the appraisal rating is between 1 and 3, the increment is 10% of the salary.

If the appraisal rating is between 3.1 and 4, the increment is 25% of the salary.

If the appraisal rating is between 4.1 and 5, the increment is 30% of the salary.

Help them to do this, by writing a Java program that displays the incremented salary.

Note: If either the salary is 0 or negative (or) if the appraisal rating is not in the range 1 to 5 (inclusive), then the output should be "Invalid Input". XYZ TECHNOLOGIES

Draft a java Program to Calculate Average of 'n' Numbers Using Arrays

Suppose you have a Piggie Bank with an initial amount of \$50 and you have to add some more amount to it. Create a class 'Add Amount' with a data member named 'amount' with an initial value of \$50. Now make two constructors of this class as follows:

1 - without any parameter - no amount will be added to the Piggie Bank

2 - having a parameter which is the amount that will be added to the Piggie Bank

Create an object of the 'AddAmount' class and display the final amount in the Piggie Bank.

Write a java program for multilevel inheritance

Write a java program to create an abstract class named Shape that contains an empty method named number Of Sides( ). Provide three classes named Trapezoid, Triangle and Hexagon such that each one of the classes extends the class Shape. Each one of the classes contains only the method number Of Sides( ) that shows the number of sides in the given geometrical figures

Write a java program in which you will declare two interface sum and sub inherits these interfaces through class A1 and display their content.

Write a java program for multiple exception handling.

Write a java program to implement multithreading

CO1 Understand the concepts of OOPs

CO2 Design the syntax, semantics and classes in Java language

CO3 Design program using User Defined packages and interfaces

CO4 Develop applications using Exception handling in java

CO5 Implement the use of multithread programming.

Course Outcome

**TEXT BOOK:**

T1 Herbert Schild, "Java The Complete Reference", Eighth Edition, McGraw Hill, 2011

T2 E Balagurusamy, "Programming with JAVA", Fifth Edition, McGraw Hill, 2015.

**REFERENCES:**

R1 E.Balagurusamy, "Programming with java A Primer", fifth edition, McGraw - Hill 2014

R2 H.M.Deitel, P.J.Deitel, "Java : how to program", Fifth edition, Prentice Hall of India private limited, 2003.

R3 Cay S. Horstmann, "Core Java Fundamentals", Volume 1, 11 th Edition, Prentice Hall, 2018

*f. May M*  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**



Programme	Course Code	Name of the Course	L	P	T	C
BE	22EC3002	Digital Electronics Lab	0	0	3	1.5

**Course Objective**

1. Demonstrate the formal procedures for the analysis and design of combinational circuits
2. Use appropriate design technique to design the different sequential circuits.
3. Apply the concepts of Hardware Description Language for designing digital circuits.

**Exp.No.**

**Description of the Experiments**

Design, implement and test the following digital circuits,

- 1 4-bit binary Adder / Subtractor using IC 7483.
- 2 BCD adder using IC 7483.
- 3 Multiplexer and De-multiplexer using logic gates.
- 4 Encoder and Decoder using logic gates.
- 5 Parity checker and generator.
- 6 4 – bit binary ripple counter.
- 7 3-bit synchronous up / down counter.
- 8 4 – bit shift register using Flip – flops.

**Software Experiments**

- 1 Adder / Subtractor Circuits and BCD adder using Verilog code
- 2 Magnitude Comparator and ALU using Verilog code
- 3 Synchronous Counters using Verilog code
- 4 Asynchronous counters using Verilog code
- 5 Sequence Detector using Verilog code for digital lab

**Total Practical Hours 45**

**Course Outcome**

- CO1: Analyze the performance of various combinational circuits.  
 CO2: Design and develop various synchronous logic circuits.  
 CO3: Formulate the design procedure of combinational and sequential digital circuits using Hardware Description Language

*f. Hay*  
 CHAIRPERSON/BOS

**Chairman - RoS  
 ECE - HICET**



*[Signature]*  
 DEAN/PRINCIPAL

**Dean (Academic)  
 HICET**



Programme	Course Code	Name of the Course	L	P	T	C
BE	22EC3001	Electronic Circuits Lab	0	0	3	1.5

**Course Objective**

1. To introduce methods of biasing transistors.
2. To design and analysis transistor as amplifiers.
3. To analyze and design wave shaping circuits and signal generator.
4. To simulate various electronic circuits using multisim.

**Exp.No.**

**Description of the Experiments**

- Design, construct and test the following biasing circuits and find the transient analysis and frequency response of Single BJT and FET.
- 1 a) Fixed bias  
b) Self bias
  - 2 Current series Feedback Amplifiers
  - 3 RC Phase shift oscillator
  - 4 Hartley Oscillator
  - 5 Class C tuned Amplifier
  - 6 Class B and
  - 7 Class AB Amplifiers
  - 8 Common Collector Amplifier
  - 9 Astable multivibrator

**Simulation Experiments**

- 10 Darlington Amplifier
- 11 Colpitt's Oscillator
- 12 Integrator, Differentiator, Clipper and Clamper circuits.
- 13 Monostable multivibrator

**Total Practical Hours 45**

**Course Outcome**

- CO1: Design and analyze the biasing circuits for various amplifier configurations  
CO2: Construct and analyze the performance of signal generators for a specified frequency.  
CO3: Analyze the performance of electronic circuits using PSPICE.

*P. May*  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*J. Jayaraman*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**





Programme	Course Code	Name of the Course	L	T	P	C
B.E-ECE/I	22EC3204	Circuits and Networks	2	0	0	2

Course Objectives

CO1: To teach the fundamental concepts and introduce mesh and nodal analysis techniques for DC and AC Circuits  
CO2: To introduce various network reduction techniques and different network theorems used for circuit analysis  
CO3: To introduce the phenomenon of resonance in coupled circuits  
CO4: To impart knowledge on transient response of the electric circuits  
CO5: To study two port networks and their characterization

Unit	Description	Instructional Hours
I	<b>BASIC CONCEPTS OF DC AND AC CIRCUITS</b> Introduction to Basic Circuit Elements, Ohm's Law – Kirchhoff's Voltage law – Kirchhoff's Current law–Resistors in series and parallel Combinations, A.C Circuits –Complex Impedance, Mesh and Nodal analysis for D.C and A.C. circuits	9
II	<b>NETWORK REDUCTION AND THEOREMS</b> Network Reduction: Voltage and Current Division, Source Transformation, T & $\pi$ Networks- Star-Delta conversion. Network theorems: Superposition theorem, Thevenin's theorem, Norton's theorem, Reciprocity theorem, Millman's theorem, and Maximum power transfer theorem, Application of Network theorems to DC and AC Circuits.	9
III	<b>RESONANCE AND COUPLED CIRCUITS</b> Resonance – Series and Parallel resonance – Variation of impedance with frequency -Variation in current through and voltage across L and C with frequency – Bandwidth – Q factor -Selectivity. Self-inductance – Mutual inductance – Dot rule – Coefficient of coupling –Series, Parallel connection of coupled inductors – Single tuned and double tuned coupled circuits	9
IV	<b>TRANSIENT ANALYSIS</b> Natural response-Forced response – Transient response of RC, RL and RLC circuits to excitation by Step Signal, Impulse Signal and exponential sources – Complete response of RC, RL and RLC Circuits to sinusoidal excitation.	9
V	<b>TWO PORT NETWORKS</b> Two port networks, Z parameters, Y parameters, Transmission (ABCD) parameters, Hybrid(H) Parameters, Interconnection of Two Port Networks (Series, Parallel And Cascade)	9
<b>Total Instructional Hours</b>		<b>45</b>

**After completing this course, the students will be able to:**  
CO1: Explain the circuit's behaviour using circuit laws and apply mesh analysis/ nodal analysis to determine behaviour of the given DC and AC circuit  
CO2: Apply network reduction techniques/network theorems and determine behaviour of the given DC and AC circuit  
CO3: Understand and compute the transient response of RC, RL and RLC Circuits  
CO4: Understand the frequency response of series and parallel RLC circuits and explain the behavior of coupled circuits  
CO5: Understand the characterization of two-port networks and apply for interconnection of two port networks

**TEXTBOOKS:**

T1- William H. Hayt, Jr. Jack E. Kemmerly and Steven M. Durbin, —Engineering Circuit AnalysisI , McGraw Hill Science Engineering, Eighth Edition, 11th Reprint 2016.  
T2- Joseph Edminister and Mahmood Nahvi, —Electric CircuitsI, Schaum's Outline Series, Tata McGraw Hill Publishing Company, New Delhi, Fifth Edition Reprint 2016.

**REFERENCEBOOKS:**

R1- Hayt and Kemmerly, "Engineering Circuit Analysis", McGraw Hill Education, New Delhi, 8<sup>th</sup> Ed, 2013.  
R2- Van Valkenberg, "Network Analysis", Prentice Hall India Learning Pvt. Ltd., 3<sup>rd</sup> Edition, 1980.  
R3- K. S. Suresh Kumar, "Electric Circuit Analysis", Pearson Publications, 2013.  
R4- Chakrabarti, "Circuit Theory Analysis and Synthesis", Dhanpat Rai & Co., Seventh - Revised edition, 2018  
R5- R. Gupta, "Network Analysis and Synthesis", S. Chand & Company Ltd, 2010.

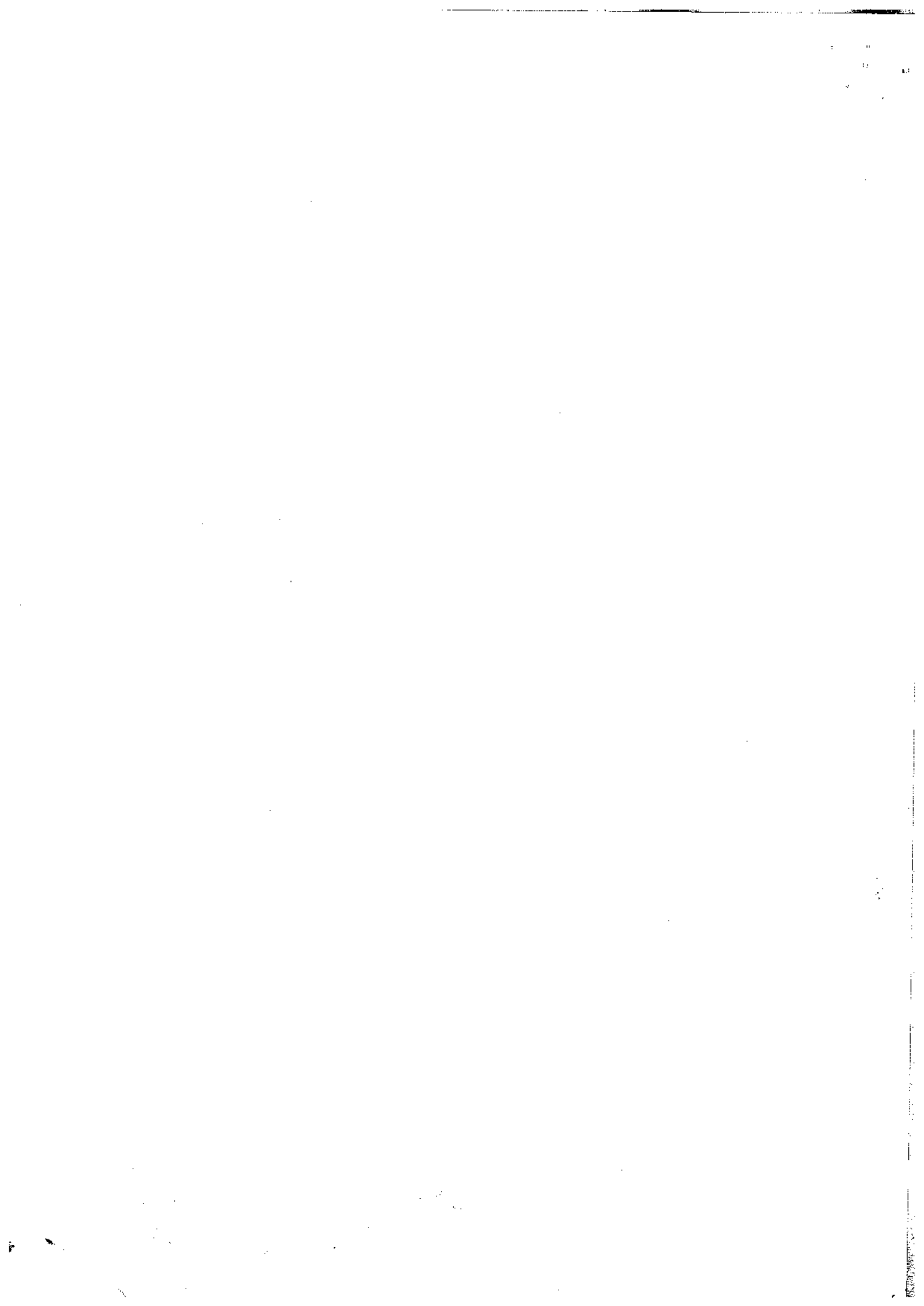
*P. May*  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**

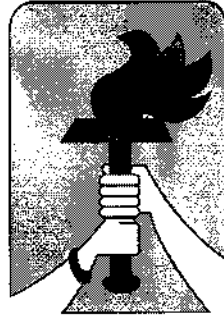




PROGRAMME	COURSE CODE	NAME OF THE COURSE	L	T	P	C
B.Tech	22IT2252	Relational Database Management System	2	0	2	3
<b>Course Objective</b>	1. List and explain the fundamental concepts of a relational database system. 2. Utilize a wide range of features available in a DBMS package. 3. Develop the logical design of the database using data modeling concepts such as entity relationship diagrams. 4. Manipulate a database using SQL. 5. Assess the quality and ease of use of data modeling and diagramming tools.					
Unit	Description					Instructional Hours
<b>I</b>	<b>UNDERSTANDING DATABASE CONCEPTS</b> Introduction-tables-Primary keys-Foreign keys-Installation of SQLite-Installation of DB2 data database-Database storage introduction-Database normalization-Indexes and how they are used in databases-Configure non-clustered indexes-Configure clustered indexes					9
<b>II</b>	<b>ENTITIES AND RELATIONSHIPS</b> Introduction to Entities and Relationships-Entities and Their Attributes-Domains-Basic Data Relationships-Documenting Relationships-Dealing with Many-to-Many Relationships Relationships and Business Rules-Data Modeling Versus Data Flow-Schemas					3+6(P)
<b>III</b>	<b>RELATIONAL DATABASE DESIGN THEORY</b> Introduction to The Relational Data Model-Understanding Relations-Primary Keys Representing Data Relationships-Views-The Data Dictionary-Normalization-Translating an ER Diagram into Relations-Normal Forms-Types of Normal Forms- Database Design and Performance Tuning introduction-Indexing-Clustering-Partitioning-Understand data definition language (DDL)					5+4(P)
<b>IV</b>	<b>USING INTERACTIVE SQL AND MANIPULATE A RELATIONAL DATABASE</b> Introduction to manipulating data-Understand data manipulation language (DML)-JDBC As The Fundamental Java API - JDBC basics-JPA as the JAVA ORM API-From JDBC to JPA					5+4(P)
<b>V</b>	<b>DATABASE IMPLEMENTATION ISSUES</b> Database Security Introduction-Sources of External Security Threats-Sources of Internal Threats-External Remedies-Internal Solutions-Understanding Database Backup and Restore-Understand different types of backups-Define a backup and recovery strategy-Test your knowledge					5+4(P)
<b>TOTAL INSTRUCTIONAL HOURS</b>						<b>45</b>
S.No	List of Experiments					
1	Creating indexes					
2	Create Tables					
3	Create Stored Procedures and Functions					
4	Read data using SELECT statements					
5	Query Multiple tables with join statements					
6	Create database and connecting to table using Java API					
7	CRUD Operation using JPA					

8	Set permissions on database
9	Restore a database
Course Outcome	CO1: Describe the fundamental elements of relational database management systems CO2: Explain the basic concepts of relational data model, entity-relationship model, relational CO3: Improve the database design by normalization. CO4: Design ER-models to represent simple database application scenarios CO5: Convert the ER-model to relational tables, populate relational database and formulate SQL queries on data.
<b>TEXT BOOKS:</b> T1: IBM Course Ware	
<b>REFERENCE BOOKS:</b> R1: Database Design and Relational Theor-Normal Forms and All That Jazz.,2019 R2: Pro SQL Server Relational Database Design and Implementation-Louis Davidson, Jessica Moss.,2016 R3: Relational Theory for Computer Professionals-C.J. Date.,2013	

**HINDUSTHAN  
EDUCATIONAL AND**



**CHARITABLE TRUST**

**HICET**

***HINDUSTHAN  
COLLEGE OF ENGINEERING AND TECHNOLOGY***

**(An Autonomous Institution)**

**Coimbatore – 641032**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
ENGINEERING**

**Curriculum and Syllabus for the Batch 2021-2025**

**2019 REGULATIONS**



**DETAILS OF CHANGES CARRIED OUT IN CURRICULUM & SYLLABUS**

**CBCS PATTERN**

**UNDERGRADUATE PROGRAMMES**

**B.E ELECTRONICS AND COMMUNICATION ENGINEERING (UG)**

**REGULATION-2019 (Revised on July 2023)**

(For the students admitted during the academic year 2021-2025 and onwards)

**SEMESTER I**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	21HE1101	Technical English	HS	2	1	0	3	40	60	100
2	21MA1103	Calculus and Differential Equations	BS	3	1	0	4	40	60	100
<b>THEORY WITH LAB COMPONENT</b>										
3	21PH1151	Applied Physics	BS	2	0	2	3	50	50	100
4	21CY1151	Chemistry for Engineers	BS	2	0	2	3	50	50	100
5	21CS1151/ 21CS1152	Python Programming and Practices/ Object Oriented Programming using Python(IBM)	ES	2	0	2	3	50	50	100
6	21EC1153	Electron devices and Electric Circuits	ES	2	0	2	3	50	50	100
<b>PRACTICAL</b>										
7	21HE1001	Language Competency Enhancement Course-I	HS	0	0	2	1	0	100	100
<b>MANDATORY COURSES</b>										
8	21HE1072	Career Guidance Level – I Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100
<b>Total</b>				<b>15</b>	<b>2</b>	<b>10</b>	<b>20</b>	<b>350</b>	<b>450</b>	<b>800</b>
As Per AICTE Norms 3 Weeks Induction Programme is Added in The First Semester as an Audit Course										

**SEMESTER II**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL	
<b>THEORY</b>											
1	21HE2101	Business English for Engineers	HS	2	1	0	3	40	60	100	
2	21MA2103	Linear Algebra, Numerical Methods and Transform Calculus	BS	3	1	0	4	40	60	100	
<b>THEORY WITH LAB COMPONENT</b>											
3	21PH2151	Material Science	BS	2	0	2	3	50	50	100	
4	21CY2151	Environmental Studies	BS	2	0	2	3	50	50	100	
5	212CS2152 /21CS2153	Essentials of C&C++Programming/ Java Fundamentals(IBM)	ES	2	0	2	3	50	50	100	
6	21ME2154	Engineering Graphics	ES	1	0	4	3	50	50	100	
<b>PRACTICAL</b>											
7	21ME2001	Engineering Practices	ES	0	0	4	2	50	50	100	
8	21HE2001	Language Competency Enhancement Course-II	HS	0	0	2	1	0	100	100	
<b>MANDATORY COURSES</b>											
9	21HE2072	Career Guidance Level – II Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100	
10	21HE2073	Entrepreneurship & Innovation	EEC	1	0	0	0	100	0	100	
				<b>Total</b>	<b>15</b>	<b>2</b>	<b>16</b>	<b>22</b>	<b>500</b>	<b>500</b>	<b>1000</b>

**SEMESTER III**

S.No	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL	
<b>THEORY</b>											
1	21MA3102	Fourier analysis and transforms	BS	3	1	0	4	40	60	100	
2	21EC3201	Digital Electronics	PC	3	0	0	3	40	60	100	
3	21EC3202	Signals and Systems	PC	3	1	0	4	40	60	100	
4	21EC3203	Electronic Circuits	PC	3	0	0	3	40	60	100	
<b>THEORY WITH LAB COMPONENT</b>											
5	21CS3252/ 21IT3252	Oops using Java/ Relational Database Management System(IBM)	PC	2	0	2	3	50	50	100	
<b>PRACTICAL</b>											
6	21EC3001	Electronic circuits lab	PC	0	0	3	1.5	50	50	100	
7	21EC3002	Digital Electronics Lab	PC	0	0	3	1.5	50	50	100	
<b>MANDATORY COURSES</b>											
8	21MC3191	Indian Constitution	MC	2	0	0	0	100	0	100	
9	21HE3072	Career Guidance Level – III Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100	
10	21HE3073	Leadership Management Skills	EEC	1	0	0	0	100	0	100	
				<b>Total</b>	<b>19</b>	<b>2</b>	<b>8</b>	<b>20</b>	<b>550</b>	<b>450</b>	<b>1000</b>



**SEMESTER IV**

S.No	Course Code	Course Title	Category	L	T	P	C	CIA	ES E	TOTAL
<b>THEORY</b>										
1	21MA4104	Probability and Random Processes	BS	3	1	0	4	40	60	100
2	21EC4201	Electro Magnetic Fields and waves	PC	3	1	0	4	40	60	100
3	21EC4202	Analog Communication	PC	3	1	0	4	40	60	100
4	21EC4203	Linear Integrated Circuits	PC	3	0	0	3	40	60	100
<b>THEORY WITH LAB COMPONENT</b>										
5	21EC4251/ 21EC4252	Control Systems/ Design Thinking-An Introduction(IBM)	PC	2	0	2	3	50	50	100
<b>PRACTICAL</b>										
6	21EC4001	Linear Integrated Circuits Lab	PC	0	0	3	1.5	50	50	100
7	21EC4002	Analog communication Lab	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
8	21MC4191	Essence of Indian tradition knowledge/Value Education	MC	2	0	0	0	100	0	100
9	21HE4072	Career Guidance Level – IV Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100
10	21HE4073	Ideation Skills	EEC	2	0	0	0	100	0	100
<b>Total</b>				<b>20</b>	<b>3</b>	<b>8</b>	<b>21</b>	<b>550</b>	<b>450</b>	<b>1000</b>

**SEMESTER V**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	21EC5201	Microprocessor and Microcontroller	PC	3	0	0	3	40	60	100
2	21EC5202	Transmission lines and WaveGuides	PC	3	1	0	4	40	60	100
3	21EC5203	VLSI Design	PC	3	0	0	3	40	60	100
4	21EC53XX	Professional Elective –I	PE	3	0	0	3	40	60	100
<b>THEORY WITH LAB COMPONENT</b>										
5	21EC5251	Data Communication and Networks	PC	2	0	2	3	50	50	100
6	21EC5252	Digital Signal Processing	PC	2	0	2	3	50	50	100
7	21CS5331	Angular JS(for IBM students)	PC	2	0	2	3	50	50	100
<b>PRACTICALS</b>										
8	21EC5001	VLSI Design Lab	PC	0	0	3	1.5	50	50	100
9	21EC5002	Microprocessors and Microcontrollers Lab	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
10	21HE5071	Soft Skills - I	EEC	1	0	0	1	100	0	100
11	21HE5072	Design Thinking	EEC	1	0	0	1	100	0	100
<b>Total</b>				<b>18</b>	<b>1</b>	<b>10</b>	<b>24</b>	<b>500</b>	<b>500</b>	<b>1000</b>

**SEMESTER VI**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	21EC6202	Antenna and Wave Propagation	PC	3	1	0	4	40	60	100
2	21EC6181	Principles of Management	HS	3	0	0	3	40	60	100
3	21EC63XX/ /21CS6351	<b>Professional Elective – II/Node JS and Microservices(IBM)</b>	PE	3	0	0	3	40	60	100
4	21XX64XX	<b>Open Elective– I</b>	OE	3	0	0	3	40	60	100
<b>THEORY WITH LAB COMPONENTS</b>										
5	21EC6251/ 21CS6255	<b>Embedded Systems and IOT/IOT and Spring Framework(IBM)</b>	PC	2	0	3	3.5	50	50	100
6	21EC6253	Digital Communication	PC	2	0	3	3.5	50	50	100
<b>PRACTICALS</b>										
7		Project Based Learning	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
8	21EC6701	Internship	EEC	-	-	-	1	100	0	100
9	21HE6071	Soft Skills - II	EEC	1	0	0	1	100	0	100
10	21HE6072	Intellectual Property Rights (IPR)	EEC	1	0	0	1	100	0	100
<b>Total</b>				<b>19</b>	<b>1</b>	<b>6</b>	<b>24</b>	<b>550</b>	<b>450</b>	<b>1000</b>

**SEMESTER VII**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	21EC7201	Digital Image Processing	PC	3	0	0	3	40	60	100
2	21EC7202	Optical and Microwave Engineering	PC	3	0	0	3	40	60	100
3	21EC73XX/ 21EC7331	<b>Professional Elective-III/Block Chain(IBM)</b>	PE	3	0	0	3	40	60	100
4	21XX74XX	<b>Open Elective – II</b>	OE	3	0	0	3	40	60	100
<b>THEORY WITH LAB COMPONENTS</b>										
5	21EC7251	Wireless Communication	PC	2	0	2	3	50	50	100
<b>PRACTICALS</b>										
6	21EC7001	Digital Image processing Lab	PC	0	0	3	1.5	50	50	100
7	21EC7002	Optical Communication and Microwave Lab	PC	0	0	3	1.5	50	50	100
<b>PROJECT WORK</b>										
8	21EC7901	Project Work – Phase I	EEC	0	0	4	2	50	50	100
<b>Total</b>				<b>14</b>	<b>0</b>	<b>12</b>	<b>20</b>	<b>300</b>	<b>500</b>	<b>800</b>

**SEMESTER VIII**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	21EC83XX	<b>Professional Elective –IV</b>	PE	3	0	0	3	40	60	100
2	21EC83XX	<b>Professional Elective- V</b>	PE	3	0	0	3	40	60	100

PROJECT WORK										
3	21CH8901	Project Work – Phase II	EEC	0	0	16	8	100	100	200
<b>Total</b>				<b>6</b>	<b>0</b>	<b>16</b>	<b>14</b>	<b>150</b>	<b>250</b>	<b>400</b>

**TOTAL NO OF CREDITS: 165**

**LIST OF PROFESSIONAL ELECTIVES**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>PROFESSIONAL ELECTIVE I</b>										
1	21EC5301	Measurements and Instrumentation	PE	3	0	0	3	40	60	100
2	21EC5302	PCB Design	PE	3	0	0	3	40	60	100
3	21EC5303	RF System Design	PE	3	0	0	3	40	60	100
4	21EC5304	Network Security	PE	3	0	0	3	40	60	100
5	21EC5181	Total Quality Management	PE	3	0	0	3	40	60	100
<b>PROFESSIONAL ELECTIVE II</b>										
1	21EC6301	Medical Electronics	PE	3	0	0	3	40	60	100
2	21EC6302	Industrial Automation	PE	3	0	0	3	40	60	100
3	21EC6303	Mobile Communication	PE	3	0	0	3	40	60	100
4	21EC6304	High Speed Networks	PE	3	0	0	3	40	60	100
5	21EC6182	E-Commerce Technology	PE	3	0	0	3	40	60	100
6	21EC6305	Virtual Reality and Augmented Reality	PE	3	0	0	3	40	60	100
<b>PROFESSIONAL ELECTIVE III</b>										
1	21EC7301	Robotics	PE	3	0	0	3	40	60	100
2	21EC7302	ASIC Design	PE	3	0	0	3	40	60	100
3	21EC7303	Global Positioning Systems	PE	3	0	0	3	40	60	100
4	21EC7181	Entrepreneurship Development	PE	3	0	0	3	40	60	100
5	21EC7305	Cyber Forensics	PE	3	0	0	3	40	60	100
6	21EC7306	Embedded Controllers	PE	3	0	0	3	40	60	100
<b>PROFESSIONAL ELECTIVE IV</b>										
1	21EC8301	Neural networks and Deep learning	PE	3	0	0	3	40	60	100
2	21EC8303	Satellite Communication	PE	3	0	0	3	40	60	100
3	21EC8304	Wireless Sensors and Networks	PE	3	0	0	3	40	60	100
4	21EC8181	Foundation Skills in Integrated Product Development	PE	3	0	0	3	40	60	100
5	21EC8305	Medical Image Processing	PE	3	0	0	3	40	60	100

6	21EC8311	Computer Communication and Internet Protocol	PE	3	0	0	3	40	60	100
7	21EC8312	Cloud Computing	PE	3	0	0	3	40	60	100
<b>PROFESSIONAL ELECTIVE V</b>										
1	21EC8306	Artificial Intelligence	PE	3	0	0	3	40	60	100
2	21EC8307	Low Power VLSI	PE	3	0	0	3	40	60	100
3	21EC8308	Software Defined Radio	PE	3	0	0	3	40	60	100
4	21EC8309	Photonic Networks	PE	3	0	0	3	40	60	100
5	21EC8182	Intellectual Property Rights and Innovations	PE	3	0	0	3	40	60	100
6	21EC8310	Fundamentals of Nano Science	PE	3	0	0	3	40	60	100

### LIST OF INDUSTRIAL CORE COURSES

S.No.	CODE	Courses	CAT	L	T	P	C	CIA	ESE	TOTAL
1	21CS1152	Object Oriented Programming using Python	IC	2	0	2	3	50	50	100
2	21CS2153	Java Fundamentals	IC	2	0	2	3	50	50	100
3	21IT3252	Relational Database Management System	IC	2	0	2	3	50	50	100
4	21EC4252	Design Thinking-An Introduction	IC	2	0	2	3	50	50	100
5	21CS5331	Angular JS	IC	2	0	2	3	50	50	100
6	21CS6351	Node JS and Micro services	IC	2	0	2	3	50	50	100
7	21CS6255	IoT and Spring Framework	IC	2	0	2	3	50	50	100
8	21EC7331	Blockchain	IC	2	0	2	3	50	50	100

### LIST OF OPEN ELECTIVES

ELECTRONICS AND COMMUNICATION ENGINEERING										
S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
1	21EC6401	Consumer Electronics	OE	3	0	0	3	40	60	100
2	21EC7401	Introduction to IOT	OE	3	0	0	3	40	60	100
LIFE SKILL COURSES										
3	21LSZ401	General Studies for Competitive Examinations	OE	3	0	0	3	40	60	100
4	21LSZ402	Human Rights, Women's	OE	3	0	0	3	40	60	100

		Rights and Gender Equality								
5	21LSZ403	Indian Ethos and Human Values	OE	3	0	0	3	40	60	100
6	21LSZ404	Indian Constitution and Political System	OE	3	0	0	3	40	60	100
7	21LSZ405	Yoga for Human Excellence	OE	3	0	0	3	40	60	100
<b>NCC COURSES</b>										
(Only for the students' who have opted NCC subjects in Semester I, II, III & IV are eligible)										
8	21HEZ401	NCC course level 1	OE	3	0	0	3	40	60	100
9	21HEZ402	NCC course level 2	OE	3	0	0	3	40	60	100

**(Note: Z Stands for semester, students can't choose twice the course)**

## MINOR Verticals

### Internet of Things

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21EC5231	Microprocessors and Microcontrollers	MDC	3	0	0	3	3
2	21EC6231	Introduction to Internet of Things	MDC	3	0	0	3	3
3	21EC6232	Introduction to Security of Cyber Physical Systems	MDC	3	0	0	3	3
4	21EC7231	Ubiquitous Sensing, Computing and Communication	MDC	3	0	0	3	3
5	21EC7232	Embedded Systems for IoT	MDC	3	0	0	3	3
6	21EC8231	IoT with Arduino, ESP, and Raspberry Pi	MDC	3	0	0	3	3

### Vertical II

#### Fintech and Block Chain

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21CS5602	Financial Management	MDC	3	0	0	3	3
2	21MB6231	Fundamentals of Investment	MDC	3	0	0	3	3
3	21MB6232	Banking, Financial Services and Insurance	MDC	3	0	0	3	3

4	21MB7231	Introduction to Blockchain and its Applications	MDC	3	0	0	3	3
5	21MB7232	Fintech Personal Finance and Payments	MDC	3	0	0	3	3
6	21MB8231	Introduction to Fintech	MDC	3	0	0	3	3

### Vertical III

#### Entrepreneurship

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21BA5601	Foundation of Entrepreneurship	MDC	3	0	0	3	3
2	21BA6601	Introduction to Business Venture	MDC	3	0	0	3	3
3	21BA6602	Team Building & Leadership Management for Business	MDC	3	0	0	3	3
4	21BA7601	Creativity & Innovation in Entrepreneurship	MDC	3	0	0	3	3
5	21BA7602	Principles of Marketing Management for Business	MDC	3	0	0	3	3
6	21BA8601	Human Resource Management for Entrepreneurs	MDC	3	0	0	3	3
7	21BA8602	Financing New Business Ventures	MDC	3	0	0	3	3

### Vertical IV

#### Environment and Sustainability

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21CEXXX	Sustainable Infrastructure Development	MDC	3	0	0	3	3
2	21AG6233	Sustainable Agriculture and Environmental Management	MDC	3	0	0	3	3
3	21BM6233	Sustainable Bio Materials	MDC	3	0	0	3	3
4	21ME7233	Materials for Energy Sustainability	MDC	3	0	0	3	3
5	21CE7233	Green Technology	MDC	3	0	0	3	3
6	21CE8232	Environmental Quality Monitoring and Analysis	MDC	3	0	0	3	3

## HONOURS

S NO.	Vertical 1 Emerging Technologies in Communication Engineering	Vertical 2 Sensor Technologies and IoT	Vertical 3 Semiconductor Chip Design and Testing - TESSOLVE Semiconductors
1	Tele Communication Switching and Networks	Real-Time Embedded Systems Design	Digital System Design
	Optical Communication Networks	IOT Processors/ Advanced Processor Architectures	CMOS VLSI Design
3	Wireless Broadband Networks / Digital switching Systems	Computer Vision	Low Power VLSI Design
4	Software Defined Networks	Wearable Devices / Long term Evolution Technologies	ASIC Design
5	4G/5G Communication Networks/ Wireless systems and standards	Industrial IoT	System Verilog
6	Underwater Communication	IoT based System Design	VLSI Testing and Design for Testability

### PROFESSIONAL ELECTIVE COURSES: VERTICALS

**B E (Hons) Electronics and Communication Engineering with Specialization in Emerging Technologies in  
Communication Engineering**

#### VERTICAL1: Emerging Technologies in Communication Engineering

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	21EC5204	Tele Communication Switching and Networks	PEC1	3	0	0	3	3
2.	21EC6202	Optical Communication Networks	PEC2	3	0	0	3	3
3.	21EC6203/ 21EC6204	Wireless Broadband Networks / Digital switching Systems	PEC3	3	0	0	3	3
4.	21EC7202	Software Defined Networks	PEC4	3	0	0	3	3
5.	21EC7203/ 21EC7204	4G/5G Communication Networks/ Wireless systems and standards	PEC5	3	0	0	3	3
6.	21EC8201	Underwater Communication	PEC6	3	0	0	3	3

**B E (Hons) Electronics and Communication Engineering with Specialization in Sensor Technologies and IoT**

**VERTICAL 2: Sensor Technologies and IoT**

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	21EC5205	Real-Time Embedded Systems Design	PEC1	3	0	0	3	3
2.	21EC6205/ 21EC6206	IoT Processors/ Advanced Processor Architectures	PEC2	3	0	0	3	3
3.	21EC6207	Computer Vision	PEC3	3	0	0	3	3
4.	21EC7205/ 21EC7206	Wearable Devices / Long term Evolution Technologies	PEC4	3	0	0	3	3
5.	21EC7207	Industrial IoT	PEC5	3	0	0	3	3
6.	21EC8202	IoT based System Design	PEC6	3	0	0	3	3

**B E (Hons) Electronics and Communication Engineering with Specialization in Semiconductor Chip Design and Testing - TSSOLVE Semiconductors**

**VERTICAL 3: Semiconductor Chip Design and Testing - TESSOLVE Semiconductors**

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1.	21EC5206	Digital System Design	PEC1	3	0	0	3	3
2.	21EC6208	CMOS VLSI Design	PEC2	3	0	0	3	3
3.	21EC6209	Low Power VLSI Design	PEC3	3	0	0	3	3
4.	21EC7208	ASIC Design	PEC4	3	0	0	3	3
5.	21EC7209	System Verilog	PEC5	3	0	0	3	3
6.	21EC8203	VLSI Testing and Design for Testability	PEC6	3	0	0	3	3



## HONOURS

### B E (Hons) Electronics and Communication Engineering with Specialization in Advanced Communication Systems

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21EC5204	Information Theory and Coding	PC	3	0	0	3	3
2	21EC6203	Cognitive Radio Network	PC	3	0	0	3	3
3	21EC6204	Advanced Wireless Broadband Communications	PC	3	0	0	3	3
4	21EC7203	Mobile and Vehicular Communication	PC	3	0	0	3	3
5	21EC7204	5G Technology	PC	3	0	0	3	3
6	21EC8201	Massive MIMO and mmWave Systems	PC	3	0	0	3	3

### B E (Hons) Electronics and Communication Engineering with Specialization in Micro electronics and VLSI

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21EC5205	Analog VLSI Design	PC	3	0	0	3	3
2	21EC6205	Signal and Image Processing	PC	3	0	0	3	3
3	21EC6206	VLSI Signal Processing	PC	3	0	0	3	3
4	21EC7205	Reconfigurable Computing	PC	3	0	0	3	3
5	21EC7206	Evolvable Hardware	PC	3	0	0	3	3
6	21EC8202	Solar Power Electronics	PC	3	0	0	3	3

### B E (Hons) Electronics and Communication Engineering with Specialization in Wireless technology

S No.	Course Code	Course Title	Category	Periods Per week			Total Contact Periods	Credits
				L	T	P		
1	21EC5206	Wireless Broadband Networks	PC	3	0	0	3	3
2	21EC6207	Wireless Communication Techniques	PC	3	0	0	3	3
3	21EC6208	Wireless Sensor Network Design	PC	3	0	0	3	3
4	21EC7207	Access Technologies	PC	3	0	0	3	3
5	21EC7208	Free Space Optical Communication	PC	3	0	0	3	3
6	21EC8203	Antenna Design and Testing	PC	3	0	0	3	3

**CREDIT DISTRIBUTION**

Semester	I	II	III	IV	V	VI	VII	VIII	Total
Credits	20	22	20	21	24	24	20	14	165

\* Student can earn extra credit 35 over and above the total credits

*P. Hayle*  
Chairman, Board of Studies  
**Chairman - BoS**  
**ECE - HICET**

*[Signature]*  
Dean Academics  
**Dean (Academics)**  
**HICET**

Principal  
**PRINCIPAL**  
Hindusthan College Of Engineering & Technology  
COMBATORE - 641 032.

Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5201	MICROPROCESSOR AND MICRO CONTROLLER	3	0	0	3

- Course Objective**
1. Study the Architecture of 8085 and 8086 microprocessor.
  2. Learn the design aspects of I/O and Memory Interfacing circuits.
  3. Study about communication and bus interfacing.
  4. Study the Architecture of 8051 microcontroller
  5. Study the concepts of microcontroller interfacing

Unit	Description	Instructional Hours
I	<b>THE 8085 AND 8086 MICROPROCESSORS</b> Introduction to 8085 – Microprocessor architecture – Addressing modes - Instruction set - Introduction to 8086 – Microprocessor architecture – Addressing modes - Instruction set- Assembly language programming – Modular Programming - Interrupts and interrupt service routines.	9
II	<b>8086 SYSTEM BUS STRUCTURE</b> 8086 signals – Basic configurations – System bus timing – System design using 8086 – Introduction to Multiprogramming – Multiprocessor configurations – Coprocessor, Closely coupled and loosely Coupled configurations – Introduction to advanced processors	9
III	<b>I/O INTERFACING</b> Parallel communication interface – Serial communication interface – D/A and A/D Interface – Timer Interface – Keyboard /display controller – Interrupt controller – DMA controller – Programming and applications Case studies: Traffic Light control, LED display, LCD display.	9
IV	<b>MICROCONTROLLER AND INTERFACING MICROCONTROLLER</b> Architecture of 8051 – Special Function Registers (SFRs) - I/O Pins Ports and Circuits – Instruction set - Addressing modes - Assembly language programming. Programming 8051 Timers - Serial Port Programming - Interrupts Programming – LCD & Keyboard Interfacing - ADC, DAC & Sensor Interfacing - Stepper Motor	9
V	<b>ARM PROCESSOR</b> Arcon RISC Machine – Architectural Inheritance – Core & Architectures - Registers – Pipeline - Interrupts – ARM organization - ARM processor family – Co-processors - ARM instruction set- Thumb Instruction set - Instruction cycle timings - The ARM Programmer’s model- ARM Assembly Language Programming	9
<b>Total Instructional Hours</b>		<b>45</b>

**Course Outcome**

- CO1: Design and implement programs on 8086 microprocessors.  
 CO2: Design I/O circuits.  
 CO3: Design Memory Interfacing circuits.  
 CO4: Design and implement 8051 microcontroller-based systems.  
 CO5: Design various interfacing and its programming methodologies

**TEXT BOOKS:**

- T1-Ramesh S. Goankar, “Microprocessor Architecture, Programming and Applications with 8085”, 5th Edition, Prentice Hall (Unit 1)  
 T2- Yu-Cheng Liu, Glenn A.Gibson, “Microcomputer Systems: The 8086 / 8088 Family - Architecture,

*P. Nayak*  
 CHAIRPERSON/BOS  
**Chairman - RoS**  
**ECE - HICET**



DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**



Programming and Design”, Prentice Hall of India, 2011. (Unit 1, 2, 3)

T3- Mohamed Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, “The 8051 Microcontroller and Embedded

Systems: Using Assembly and C”, Second Edition, Pearson education, 2011 (Unit 4, 5)

**REFERENCE BOOKS:**

R1 – Douglas V.Hall, “Microprocessors and Interfacing, Programming and Hardware”, TMH,2012

R2- Krishna Kant, “Microprocessors and microcontrollers architecture programming and system design 8085 8086 8051 8096 PHI Learning Private Limited”, 2014

Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5202	Transmission Lines and Wave Guides	3	1	0	4
<b>Course Objective</b>	1.To teach general theory on transmission lines and its characteristics 2. To give thorough understanding about high frequency line, power and impedance measurements 3. To impart technical knowledge in impedance matching using smith chart 4. To study the behavior of guided waves between parallel planes 5. To provide an insight on the characteristics of guided waves in in rectangular and circular waveguides					

Unit	Description	Instructional Hours
I	<b>TRANSMISSION LINE THEORY</b> General theory of Transmission lines - the transmission line - general solution - The infinite line - Wavelength, velocity of propagation - Waveform distortion – Distortion less line - Loading and different methods of loading - Line not terminated in $Z_0$ - Reflection coefficient - calculation of current, voltage, power delivered and efficiency of transmission - Input and transfer impedance - Open and short circuited lines - reflection factor and reflection loss	12
II	<b>HIGH FREQUENCY TRANSMISSION LINES</b> Transmission line equations at radio frequencies - Line of Zero dissipation - Voltage and current on the dissipation-less line, Standing Waves, Nodes, Standing Wave Ratio - Input impedance of the dissipation-less line - Open and short-circuited lines - Power and impedance measurement on lines - Reflection losses - Measurement of VSWR and wavelength.	12
III	<b>IMPEDANCE MATCHING IN HIGH FREQUENCY LINES</b> Impedance matching: Quarter wave transformer - Impedance matching by stubs - Single stub and double stub matching - Smith chart - Solutions of problems using Smith chart - Single and double stub matching using Smith chart.	12
IV	<b>GUIDED WAVES</b> Waves between parallel planes-Transverse Electric Waves-Transverse Magnetic Waves- Characteristics of TE and TM waves-Transverse Electromagnetic waves- Velocity of propagation-Attenuation in parallel plane guides- Wave Impedances	12

*P. Hay*  
CHAIRPERSON/BOS

**Chairman - BOS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**

## WAVEGUIDES

V	Rectangular Waveguides - TM Waves in Rectangular guides -TE Waves in Rectangular Waveguides - Impossibility of TEM waves in waveguides -Bessel functions -TM and TE waves in Circular waveguides -Wave Impedance and Characteristic Impedances.	12
---	---	----

**Total Instructional Hours** 60

### Course Outcome

After completion of the course the learner will be able to  
CO1: Understand the characteristics of transmission lines and its losses  
CO2: Understand the standing wave ratio and input impedance in high frequency transmission lines  
CO3: Understand impedance matching for high frequency lines using smith charts  
CO4: Understand the behavior of guided waves between parallel planes  
CO5: Understand the Characteristics of guided waves in rectangular and circular waveguides

### TEXT BOOKS:

T1-John D Ryder, "Networks, Lines and Fields", 2nd Edition, Prentice Hall India, 2015. (UNIT I to III)  
T2-E.C. Jordan and K.G. Balmain, "Electromagnetic Waves and Radiating Systems"2nd Edition, Prentice Hall of India, 2006. (UNIT IV & V)

### REFERENCE BOOKS:

R1-Reinhold Ludwig and Powel Bretchko, RF Circuit Design – Theory and Applications, Pearson Education Asia, First Edition,2001.  
R2 - D. K. Misra, —Radio Frequency and Microwave Communication Circuits- Analysis and Design, John Wiley & Sons, 2004.  
R3 - Mathew M. Radmanesh, —Radio Frequency & Microwave Electronics, Pearson Education Asia, Second Edition,2002.  
R4 - G.S.N Raju, "Electromagnetic Field Theory and Transmission Lines Pearson Education, First edition 2005.

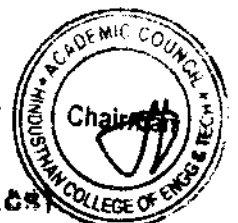
  
CHAIRPERSON/BOS

**Chairman - BoS  
ECE - HiCET**



  
DEAN/PRINCIPAL

**Dean (Academics)  
HiCET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5203	VLSI Design	3	0	0	3

- Course Objective**
- 1.To learn the fundamentals of CMOS and MOS design
  - 2.To understand silicon processing
  - 3.To familiarize with VLSI combinational logic and sequential logic circuits design
  - 4.To learn high speed processing material and data path
  5. To learn hardware description language – Verilog for digital system design

Unit	Description	Instructional Hours
I	<b>INTRODUCTION TO CMOS CIRCUITS AND MOS TRANSISTOR THEORY</b> MOS transistors – CMOS logic – Circuits and System Representation – An example – Introduction to nMOS, pMOS enhancement transistor – MOS device design equation – Basic DC equation, second order effects – Complementary CMOS inverter – DC characteristics	9
II	<b>CMOS PROCESSING, CIRCUIT CHARACTERIZATION AND PERFORMANCE ESTIMATION</b> Silicon semiconductor technology: An overview – Basic CMOS technology –CMOS process enhancement –Resistance estimation – Capacitance estimation – Switching characteristics – Analytical delay models, Gate delays – Power dissipation.	9
III	<b>CMOS CIRCUITS AND LOGIC DESIGN</b> CMOS logic gate design –physical design of simple logic gates – INVERTER, NAND and NOR gates, Complex logic gates layout, CMOS standard cell design – CMOS logic structures – Pseudo nMOS logic, Dynamic CMOS logic, Clocked CMOS logic, Pass transistor logic – Clocking strategies – Clocked system, latches and registers, system timing, setup and hold time, single phase memory structure PLL clock technique, two phase clocking	9
IV	<b>INTRODUCTION TO GaAs TECHNOLOGY &amp; ARITHMETIC BUILDING BLOC</b> Ultra-fast systems – Gallium arsenide crystal structure – Technology development – Architectures for ripple carry adders, carry look ahead adders, High speed adders, accumulators, Multipliers	9
V	<b>VERILOG PROGRAMMING INTRODUCTION</b> Hierarchical modelling concepts – Basic concepts – Modules and ports – Gate level modelling – Behavioral modelling – Data flow modelling: An introduction.	9
<b>Total Instructional Hours</b>		<b>45</b>

- Course Outcome**
- After completion of the course the learner will be able to
- CO1: Ability to analyze CMOS and MOS transistors.
  - CO2: Ability to analyze passive components required for physical design.
  - CO3: Ability to analyze timing issues of sequential logic
  - CO4: Ability to understand advanced semiconductor processing materials and data processing architecture
  - CO5: Ability to write a program using an efficient model for digital circuits.

**TEXT BOOKS:**

- T1-- Neil H E Weste and Kamran Eshraghian, "Principles of CMOS VLSI Design: A system Perspective", Second edition, Pearson education Asia.
- T2- Douglas A Pucknell and Kamran Eshraghian, "Basic VLSI Design", Third edition, Prentice Hall of India.

**REFERENCE BOOKS:**

- R1- -Samir Palnitkar, "Verilog HDL – A guide top Digital Design and Synthesis", Second edition, Pearson education.
- R2 - Neil H E Weste and David money Haris, "CMOS VLSI Design: A circuits and systems Perspective" Addison

*P. Hariharan*  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

Wesley, New Delhi, 2010.

R3 - Sung-Mo Kanga and Yusuf Leblebici, "CMOS Digital Integrated Circuits- Analysis and Design", Tata McGraw Hill, New Delhi, 2004.

Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5251	Data Communication and Networks	2	0	2	3

- Course Objective**
1. To understand the state-of-the-art in network models
  2. To analyze the flow control and error control algorithms in a network.
  3. To familiarize the various aspects of routing algorithms.
  4. Be exposed to the required functionality of each network application.
  5. To familiarize with various wide area network.

Unit	Description	Instructional Hours
I	<b>PHYSICAL LAYER</b> OSI reference model , TCP/IP Protocol suite.Line Configuration, Encoding and Decoding, Multiplexing-transmission media - Circuit Switching, Packet Switching, Message Switching. <i>Simulation of Network Topology – Star, Bus and Ring</i>	6+5
II	<b>LINK LAYER ALGORITHMS AND PROTOCOLS</b> Flow control and error control, stop and wait, Sliding windows ,Local Area Networks - IEEE 802 standards, LLC, MAC layer protocols – CSMA/CD Ethernet, Token Ring,FDDI. <i>Study And Compare the performance of Stop And Wait Protocol,Study And Compare the performance of Selective Repeat Protocol,Go Back N Protocol</i>	6+5
III	<b>ROUTING ALGORITHMS AND PROTOCOLS</b> Routing Algorithms- RIP, OSPF, BGP, multicast routing (DVMRP, PIM)- IPv4 -IPv6. UDP-TCP-congestion Control Algorithms. <i>Simulation of Distance Vector Routing Algorithm,Link State Routing Algorithm,Study of Network Simulator (Ns) ,Simulation of Congestion Control Algorithms Using Ns.</i>	6+5
IV	<b>APPLICATION LAYER</b> Domain Name system – Remote logging, Electronic Mail, File Transfer - WWW and HTTP- Simple Network Management Protocol – Data Security.	6
V	<b>WIDE AREA NETWORKS</b> Integrated Services Digital Network (ISDN), B-ISDN, Frame delay and Asynchronous Transfer Mode (ATM) Protocol	6
<b>Total Instructional Hours</b>		<b>30+15=45</b>

- Course Outcome**
- After completion of the course the learner will be able to
- CO1: Demonstrate the networking strategies.
  - CO2: Identify the technical issues related to networking technologies.
  - CO3: Discriminate various routing techniques.
  - CO4: Illustrate the web applications
  - CO5: Implement various network algorithms and protocols

**TEXT BOOKS:**

- T1 - Behrouz A Forouzan , "Data Communication and Networking", McGraw-Hill, New Delhi, 2012.  
T2 - Larry L. Peterson, Bruce S. Davie, "Computer Networks: A Systems Approach", Fifth Edition, MorganKaufmann Publishers, 2011

**REFERENCE BOOKS:**

- R1 - Andrew S Tanenbaum, "Computer networks", Prentice Hall of India, New Delhi, 2010.  
R2 - William Stallings, "Data and Computer Communication", Prentice Hall of India, New Delhi, 2007

*P. Jayaram*  
CHAIRPERSON/BOS  
**Chairman - Bos**  
**ECE - HICE**



*[Signature]*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**



Programme	Course code	Name of the course	L	T	P	C
BE	21EC5202	Digital Signal Processing	2	0	2	3
Course Objective	1. To learn discrete Fourier transform and its properties. 2. To know the characteristics of IIR filters. 3. To learn the design of Finite Impulse Response filters. 4. To understand Finite word length effects. 5. To study the concept of Multirate Signal Processing.					

Unit	Description	Instructional Hours
I	<b>DISCRETE FOURIER TRANSFORM</b> Introduction to DFT-FFT Algorithms –Radix 2 FFT algorithms, Decimation in time Algorithms, Decimation in frequency Algorithms, Inverse DFT using FFT	7
II	<b>IIR FILTER DESIGN</b> IIR filter design: Butterworth approximation using Impulse Invariance Transform and Bilinear transformation, Chebyshev approximation using Impulse Invariance Transform and Bilinear transformation. (LPF)	7
III	<b>FIR FILTER DESIGN</b> Linear phase realization of FIR filters-Design of linear phase FIR filters using Windows (Rectangular Window, Hamming Window, Hanning Window)- FIR filter Design using Frequency sampling method.	7
IV	<b>FINITE WORDLENGTH EFFECTS</b> Quantization by Truncation and Rounding – Quantization of filter coefficients – Product quantization error - Limit cycle oscillations in recursive systems: Zero input limit cycle oscillation, Overflow limit cycle oscillation – Scaling to prevent Overflow.	7
V	<b>MULTI RATE DIGITAL SIGNAL PROCESSING</b> Decimation, Interpolation, Sampling rate conversion by a rational factor, Applications of Multirate Signal Processing: Subband Coding of Speech signals.	7
	<b>List of Experiments</b>	
	1. Spectral analysis using FFT algorithms.	
	2. Filtering very long sequence using sectioned convolution.	
	3. Design of FIR filters using Rectangular, Hamming and Hanning windows.	
	4. Design of Digital IIR filters using Bilinear and Impulse Invariant Transforms.	10
	5. Analysis of limit cycle oscillations in recursive digital filters due to quantization.	
	<b>Total Instructional Hours</b>	45

On the completion of the course the students could able to:

Course Outcome	Description
CO1:	Apply DFT for the analysis of digital signals & systems.
CO2:	Design IIR Butterworth and Chebyshev filters
CO3:	Design FIR filters.
CO4:	Illustrate Finite word length effect on filters.
CO5:	Design and Implement Multirate Filters.

#### TEXT BOOKS

- T1 - John G. Proakis & Dimitris G. Manolakis, "Digital Signal Processing – Principles, Algorithms & Applications", Fourth Edition, Pearson Education / Prentice Hall, 2007. (Unit I, II, III, IV)  
 T2 - A. Nagoor Kani, "Digital Signal Processing", 2010 Edition Mc Graw Hill Education (India) Pvt. Ltd (Unit V)

#### REFERENCE BOOK

- R1 - Emmanuel C. Ifeachor, & Barrie W. Jervis, "Digital Signal Processing", Second Edition, Pearson Education, Prentice Hall, 2002

*P. Jayaram*  
CHAIRPERSON/BOS

**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**



R2- Sanjit K. Mitra, "Digital Signal Processing – A Computer Based Approach", Mc Graw Hill, 2007

R3- Andreas Antoniou, "Digital Signal Processing", Mc Graw Hill, 2006

R4-. Oppenheim, R.W. Schafer and J.R. Buck, "Discrete-Time Signal Processing", 8th Indian Reprint, Pearson,2004

Programme	Course code	Name of the course	L	T	P	C
BE	21EC5001	VLSI Design Lab	0	0	3	1.5

- Course Objective
1. To learn Hardware Descriptive Language (Verilog).
  2. To learn fusing of logical modules on FPGAs.
  3. To learn the fundamental principles of VLSI circuit design in digital and analog domain.

Expt.No. **Description of the Experiments**

- Write Verilog Code for the following circuits and their Test Bench for verification, do the initial timing verification and observe the waveform.
1. Basic logical gates.
  2. Half and full adder
  3. 8-bit adder.
  4. Flip flop -RS, D and JK
  5. 4 bit up/down counter
  6. Multiplier minimum 4 bit
- 2 Synthesize and implement 8 bit adder, 4 bit up/down counter and multiplier (minimum 4 bit) in a FPGA.
- 3 Design an Inverter using CMOS and complete the design flow mentioned below:  
i. Draw the schematic and verify the DC Analysis and Transient Analysis  
ii. Draw the Layout and verify the Design Rule Check and ERC  
iii. Check for Layout verses schematic  
iv. Extract RC and back annotate the same and verify the Design  
v. Verify for Time, Power and Area.

**Total Instructional Hours 45**

Course Outcome

- CO1: Write HDL code for basic as well as advanced digital integrated circuits.  
CO2: Import the logic modules into FPGA Boards and Synthesize digital logics on FPGA  
CO3: Design the layouts of Analog IC Blocks using EDA tools.  
CO4: Simulate the layouts of Analog IC Blocks using EDA tools.  
CO5: Extract the layouts of Analog IC Blocks using EDA tools.

  
CHAIRPERSON/BOS

**Chairman - BoS  
ECE - HICET**



  
DEAN/PRINCIPAL

**Dean (Academics)  
HICET**



Programme	Course code	Name of the course	L	T	P	C
BE	21EC5002	Microprocessor and Micro Controller Lab	0	0	3	1.5

- Course Objective
1. Introduce ALP concepts and features
  2. Write ALP for arithmetic and logical operations in 8086 and 8051
  3. Differentiate Serial and Parallel Interface
  4. Interface different I/Os with Microprocessors
  5. Be familiar with MASM

Expt.No. Description of the Experiments

- Using 8086 Micro processor and MASM software**
1. Basic arithmetic and Logical operations.
  2. Code conversion and decimal arithmetic
  3. Matrix operations
  4. Searching
  5. Sorting
- Using 8086 Micro processor and Interfacing**
6. Parallel interface
  7. Key board and Display interface
  8. Serial interface
  9. A/D and D/A interface
- Using 8051 Micro controller**
10. Basic arithmetic and Logical operations
  11. Square and Cube program, Find 2's complement of a number
  12. Stepper motor control interface

Total Instructional Hours 45

- Course Outcome
- CO1: Write ALP Programmes for fixed and Floating Point and Arithmetic
  - CO2: Interface different I/Os with processor
  - CO3: Generate waveforms using Microprocessors
  - CO4: Execute Programs in 8051
  - CO5: Explain the difference between simulator and Emulator

*P. Hayde*  
CHAIRPERSON/BOS  
**Chairman - BoS  
ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academic)  
HICET**

Programme	Course Code	Course Title	L	T	P	C
BE/BTECH	21HE5071	Soft Skills - I	1	0	0	1

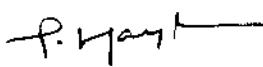
**Course Objectives:**

- 1.To employ soft skills to enhance employability and ensure workplace and career success.
- 2.To enrich students' numerical ability of an individual and is available in technical flavor.
- 3.To interpret things objectively, to be able to perceive and interpret trends to make generalizations and be able to analyze assumptions behind an argument/statement.

Unit	Description	Instructional Hours
I	<b>Introduction to Soft Skills:</b> Introduction- Objective -Hard vs Soft Skills - Measuring Soft Skills- Structure of the Soft Skills -Self Management-Critical Thinking-Reflective thinking and writing- p2p Interaction	3
II	<b>Art of Communication:</b> Verbal Communication - Effective Communication - Active listening -Paraphrasing - Feedback - Non-Verbal Communication – Roles-Types- How nonverbal communication can go wrong- How to Improve nonverbal Communication - Importance of feelings in communication - dealing with feelings in communication.	4
III	<b>World of Teams:</b> Self Enhancement - importance of developing assertive skills- developing self-confidence – developing emotional intelligence - Importance of Team work – Team vs. Group - Attributes of a successful team – Barriers involved - Working with Groups – Dealing with People- Group Decision Making.	3
IV	<b>Quantitative Aptitude:</b> Averages - Profit and loss - Partnerships - Time and work - Time, Speed and Distance - Problems based on trains - Problems based on boats and streams	3
V	<b>Logical Reasoning:</b> Clocks - Calendars - Direction Sense - Data Interpretation: Tables, Pie Chart, Bar Graph - Data Sufficiency	2
<b>Course Outcome:</b>	CO1: Students will have clarity on their career exploration process and to match their skills and interests with a chosen career path.	
	CO2: Students will develop knowledge, skills, and judgment around human communication that facilitate their ability to work collaboratively with others	
	CO3: Students will understand how teamwork can support leadership skills	
	CO4: Students will be able to make sense of problems, develop strategies to find solutions, and persevere in solving them.	
	CO5: Students will demonstrate an enhanced ability to draw logical conclusions and implications to solve logical problems.	

#### REFERENCE BOOKS:

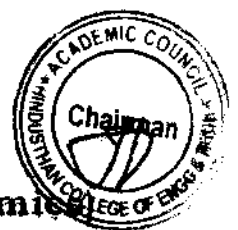
- R1: Soft Skills Training: A Workbook to Develop Skills for Employment - Frederick H. Wentz
- R2: How to prepare for data interpretation for CAT by Arun Sharma.
- R3: How to Crack TEST OF REASONING in all competitive examinations by Jaikishan and Premkishan.
- R4: A New Approach To Reasoning Verbal & Non-Verbal By B.S. Sijwali
- R5: Quantitative Aptitude for Competitive Examinations - Dr. R.S. Aggarwal, S. Chand

  
 CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



  
 DEAN/PRINCIPAL

**Dean (Academic)**  
**HICET**



Programme	Course code	Name of the course	L	T	P	C
BE	21EC5002	Microprocessor and Micro Controller Lab	0	0	3	1.5

- Course Objective
1. Introduce ALP concepts and features
  2. Write ALP for arithmetic and logical operations in 8086 and 8051
  3. Differentiate Serial and Parallel Interface
  4. Interface different I/Os with Microprocessors
  5. Be familiar with MASM

**Expt.No. Description of the Experiments**

- Using 8086 Micro processor and MASM software**
1. Basic arithmetic and Logical operations.
  2. Code conversion and decimal arithmetic
  3. Matrix operations
  4. Searching
  5. Sorting
- Using 8086 Micro processor and Interfacing**
6. Parallel interface
  7. Key board and Display interface
  8. Serial interface
  9. A/D and D/A interface
- Using 8051 Micro controller**
10. Basic arithmetic and Logical operations
  11. Square and Cube program, Find 2's complement of a number
  12. Stepper motor control interface

**Total Instructional Hours 45**

- Course Outcome
- CO1: Write ALP Programmes for fixed and Floating Point and Arithmetic
  - CO2: Interface different I/Os with processor
  - CO3: Generate waveforms using Microprocessors
  - CO4: Execute Programs in 8051
  - CO5: Explain the difference between simulator and Emulator

*P. H. Jayaraman*  
 CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
 DEAN/PRINCIPAL  
**Dean (Academic)**  
**HICET**

<b>Programme</b>	<b>Course Code</b>	<b>Course Title</b>	<b>L</b>	<b>T</b>	<b>P</b>	<b>C</b>
BE/BTECH	21HE5072	Design Thinking	1	0	0	1

- Course Objective**
- To expose students to the design process
  - To develop and test innovative ideas through a rapid iteration cycle.
  - To provide an authentic opportunity for students to develop teamwork and leadership skills

<b>Unit</b>	<b>Description</b>	<b>Instructional Hours</b>
	<b>DESIGN ABILITY</b>	
I	Asking Designers about what they Do – Deconstructing what Designers Do – Watching what Designers Do – Thinking about what Designers Do – The Natural Intelligence of Design Sources	4
	<b>DESIGNING TO WIN</b>	
II	Formula One Designing – Radical Innovations – City Car Design – Learning From Failures – Design Process and Working Methods	4
	<b>DESIGN TO PLEASE AND DESIGNING TOGETHER</b>	
III	Background – Product Innovations – Teamwork versus Individual work – Roles and Responsibilities – Avoiding and Resolving Conflicts.	4
	<b>DESIGN EXPERTISE</b>	
IV	Design Process – Creative Design - Design Intelligence – Development of Expertise – Novice to Expert. Critical Thinking – Case studies: Brief history of Albert Einstein, Isaac Newton and Nikola Tesla	3
<b>Total Instructional Hours</b>		<b>15</b>

**Course Outcome**

Upon completion of the course, students will be able to

CO1: Develop a strong understanding of the Design Process

CO2: Learn to develop and test innovative ideas through a rapid iteration cycle.

CO3: Develop teamwork and leadership skills

**TEXT BOOKS:**

T1 - 1. Nigel Cross, "Design Thinking", Kindle Edition.

**REFERENCE BOOKS:**

R1 - Tom Kelley, "Creative Confidence", 2013.

R2 - 3. Tim Brown, "Change by Design", 2009.

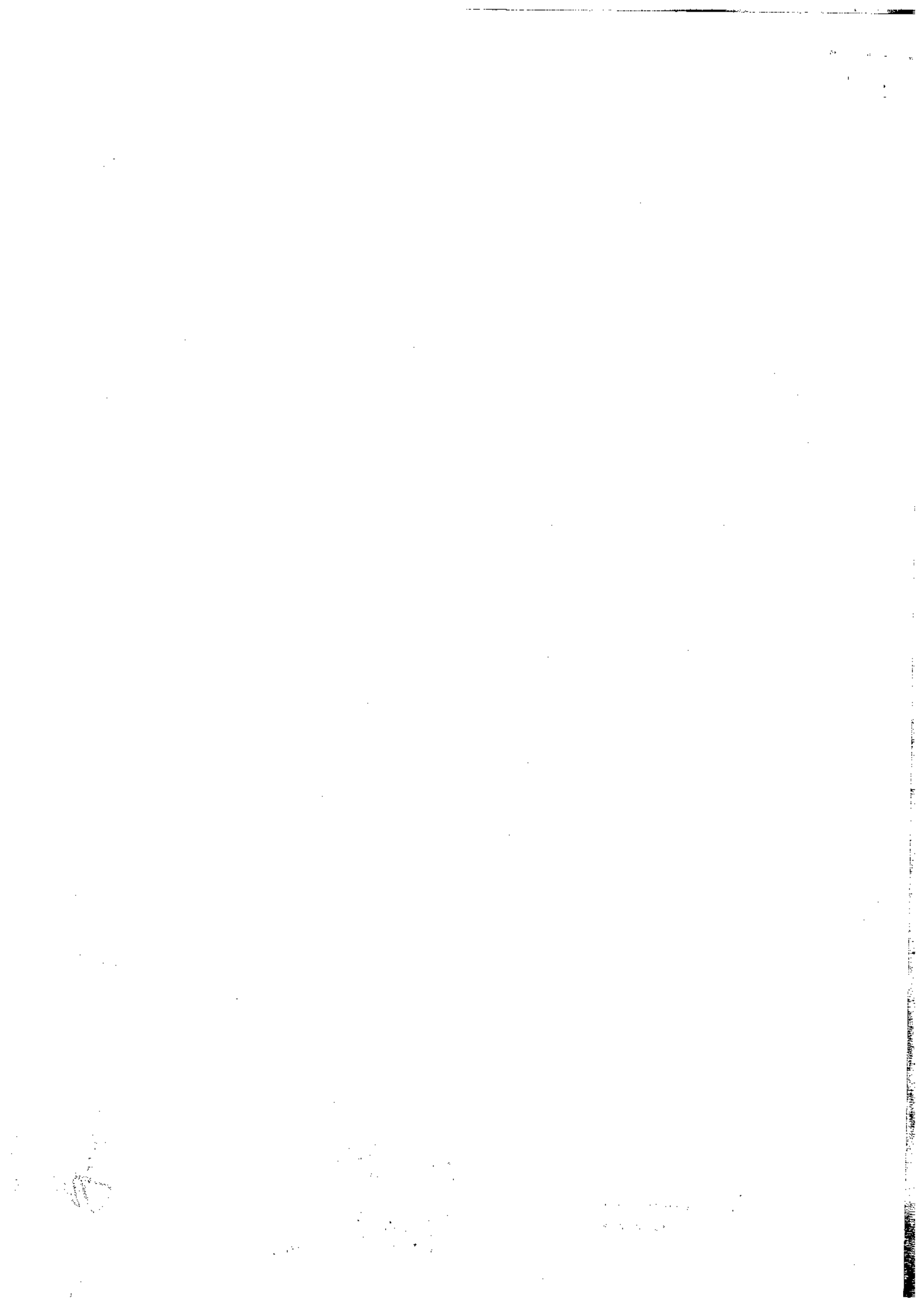
*P. Mayya*  
 CHAIRPERSON/BOB  
**Chairman - BoS**  
**ECE - HICET**



DEAN/PRINCIPAL

**Dean (Academic)**  
**HICET**





Programme	Course Code	Name of the Course	T	P	C	
BE	21EC5301	Measurements and Instrumentation	3	0	0	3

- The student should be able to
- Course Objective**
1. Know the concept of measurements and learn the use of DC & AC bridges.
  2. Understand the working principle of electronic instruments.
  3. Learn the use of different types of signal generators and analyzers.
  4. Understand the use of different types of transducers.
  5. Learn the principle of working and applications of digital data acquisition system and fiber optic measurements.

Unit	Description	Instructional Hours
I	<b>INTRODUCTION TO MEASUREMENT SYSTEMS &amp; INDICATING EQUIPMENTS</b> Introduction to Instruments & their Representation, Static & Dynamic characteristics of Instruments, Types Of Errors-Error Analysis. PMMC, DC Ammeters & Voltmeters, Multimeter or VOM, Calibration of DC Instruments Bridge Measurements: Wheatstone, Kelvin, Maxwell, Schering and Wien Bridge.	9
II	<b>ELECTRONIC INSTRUMENTS FOR MEASURING &amp; RECORDING</b> AC Voltmeter using Rectifier, True RMS-Responding voltmeters, Electronic Multimeter Digital Voltmeter, Q meter, Cathode Ray Oscilloscope (CRO), Recorders: Galvanometric, Servo type Potentiometric, Magnetic type & Digital Recorder.	9
III	<b>SIGNAL GENERATION &amp; SIGNAL ANALYSIS</b> Sine wave generator, Frequency synthesized signal generator, Sweep frequency generators Function generators-Audio frequency signal generation. Wave analyzers -Harmonic distortion analyzer -spectrum analysis.	9
IV	<b>TRANSDUCERS</b> Classification of Transducers-Selecting a Transducer -Strain Gages-Displacement Transducers- Pressure Measurements, Temperature Measurements- Non-Electrical, Electrical & radiation methods. Flow Measurements.	9
V	<b>DATA ACQUISITION SYSTEMS AND FIBER OPTIC MEASUREMENTS</b> Elements of a Digital Data Acquisition System - Interfacing Transducers to Electronic Control & Measuring Systems - Multiplexing - Computer Controlled Test Systems: Testing of Audio Amplifier & Radio Receiver, - IEEE 488 Bus & Electrical interface - Fiber Optic Measurements: Power Measurement and System Loss - Optical Time Domains Reflectometer.	9
<b>Total Instructional Hours</b>		<b>45</b>

- Course Outcome**
- After completion of the course the learner will be able to
- CO1: Understand the measurements concept and usage of AC/DC bridges.  
CO2: Explore knowledge on Electronic Instruments.  
CO3: Explain the different types of Signal generators and CRO.  
CO4: Identify various types of transducers and their working.  
CO5: Learn the various process of computer controlled instrumentation.

#### TEXT BOOKS:

- T1- Albert D.Helfrick and William D.Cooper, Modern Electronic Instrumentation and Measurement Techniques, PHI, 2003. (Unit I to V)  
T2- Ernest O.Doebelin, Measurements System-Application & Design, McGraw-Hill,1990,Fourth Edition.(Unit I to Unit V)

#### REFERENCE BOOKS:

- R1 - B.C.Nakara, K.K.Chaudhry, Instrumentation Measurement and Analysis , McGraw - Hill , 2004. . (Unit I & IV)  
R2 - J.B.Gupta, "A Course In Electronics And Electrical Measurements And Instrumentation" S.K.Kataria and sons,2013

*P. Mayale*  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**



DEAN/PRINCIPAL  
**Dean (Academic)**  
**HICET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5302	PCB Design	3	0	0	3

- Course Objective**
1. To describe the basics, layout planning and design in the field of Printed Circuit boards.
  2. To design the PCB deals with the various considerations for special circuits.
  3. To learn the Image Transfer, Plating and Etching techniques.
  4. To know the different technology involves in the Printed Circuit Boards.
  5. To summarize the PCB Technology trends.

Unit	Description	Instructional Hours
	<b>BASICS OF PRINTER CIRCUIT BOARDS</b>	
I	Component of a PCB – Classification of PCB - Manufacturing of Basic PCB – Layout planning: General PCB considerations – Electrical Design Considerations –Layout Design: Layout Scale – Layout Sketch / Design – Layout considerations.	9
	<b>DESIGN CONSIDERATIONS FOR SPECIAL CIRCUITS</b>	
II	Design Rules for Analog Circuits: Components and Placement – Signal Conductors – Supply and Ground Connectors – General Rules for design of Analog PCBs. Design Rules for Digital Circuits: Transmission Lines. Design rules for PCBs for High frequency circuits, Fast Pulse Circuits, Microwave Circuits and Power Electronic Circuits.	9
	<b>IMAGE TRANSFER, PLATING AND ETCHING TECHNIQUES</b>	
III	Image Transfer Techniques: Laminates Surface Preparation – Screen Printing – Pattern Transferring Techniques – Printing Inks – Printing Process - Photo Painting - Laser Diode Imaging(LDI) - Plating Process : need for Plating – Plating Techniques - Special plating Techniques - Etching Techniques : Etching Parameters – Equipment and Techniques - Optimizing Etchant Economy	9
	<b>TECHNOLOGY OF PRINTED CIRCUIT BOARDS</b>	
IV	Film Master Production: Emulsion Parameters – Film Emulsions – Dimensional Stability of Film Masters – Reprographic Cameras – Film Processing - Film Registration - Photo printing: Basic processes for Double sided PCBs – Wet Film resists and Dry Film resists.	9
	<b>PCB TECHNOLOGY TRENDS</b>	
V	Fine-line Conductors with Ultra Thin copper Foil - Multilayer Boards - Multiwire Boards – Subtractive - Semi-Additive Processes – Additive Processes – Flexible Printed Circuit Boards – Metal Core Circuit Boards – Mechanical Milling of PCBs.	9
<b>Total Instructional Hours</b>		<b>45</b>

*P. Jay Kumar*  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**





**Course  
Outcome**

After completion of the course the learner will be able to


- CO1: Explain the basics PCB and layout design considerations.
- CO2: Enumerate PCB Design considerations in Special circuits.
- CO3: Enhance the knowledge in image transfer, plating and Etching techniques in PCBs.
- CO4: Recognize the various Technologies in Printed Circuit boards.
- CO5: Summarize the PCB technology trends.

**TEXT BOOKS:**

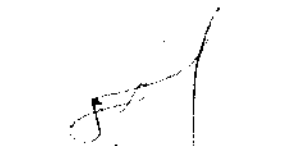
- T1-R.S. Khandpur, "Printed Circuit Boards Design, Fabrication, Assemble and Testing", TMH, 2005. (Unit 1,2 & 3)
- T2 - Walter C Bosshart, "Printed Circuits Boards Design and Technology" - Tata McGraw- Hill , 2008. (Unit 4 & 5)

**REFERENCE BOOKS:**

- R1 - ChristoperT.Robertson, "PCB Designers Reference: Basics", Prentice Hall, First edition, 2003.
- R2 - C.F.Coombs, "Printed Circuits Handbook", McGraw-Hill, 2001.

  
CHAIRPERSON/BOS  
**Chairman - BOS  
ECE - HICET**



  
DEAN/PRINCIPAL  
**Dean (Academic)**  
**HICET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5303	RF System Design	3	0	0	3

**Course Objective**

The student should be able to

1. To learn the various passive and active components for radio frequency circuits
2. To gain knowledge on microstrip line filters
3. To understand the working principle of active RF components for various applications.
4. To design biasing circuits for RF amplifiers
5. To learn various RF oscillators, Mixers, power dividers and couplers

Unit	Description	Instructional Hours
I	<b>INTRODUCTION TO RF DESIGN</b> Importance of RF design, Electromagnetic Spectrum, RF behavior of passive Components, Chip components and Circuit Board considerations, Scattering Parameters, Smith Chart and applications.	9
II	<b>RF FILTER DESIGN</b> Overview, Basic resonator and filter configuration, Special Filter Realizations, Filter Implementations, Unit element, Kurodas Identity, Coupled Filters.	9
III	<b>ACTIVE RF COMPONENTS &amp; APPLICATIONS</b> RF Diodes, BJT, RF FETs, High electron mobility transistors; Matching and Biasing Networks – Impedance matching using discrete components, Microstrip line matching networks, Amplifier classes of operation and biasing networks.	9
IV	<b>RF AMPLIFIER DESIGN</b> Characteristics, Amplifier Power relations, Stability Considerations, Constant gain circles, Constant VSWR circles, Broadband, Low power, High power and multistage amplifiers.	9
V	<b>OSCILLATORS, MIXERS &amp; APPLICATIONS</b> Basic oscillator Model - high frequency oscillator configuration - Basic characteristics of mixers - RF couplers - Wilkinson divider - Detector and demodulator circuits.	9
<b>Total Instructional Hours</b>		<b>45</b>

**Course Outcome**

After completion of the course the learner will be able to

- CO1: Describe the various passive and active components for radio frequency circuits
- CO2: Design and analyze microstrip line filters
- CO3: Design matching networks using Smith chart
- CO4: Design biasing circuits for RF amplifiers
- CO5: Describe various RF oscillators, Mixers, power dividers and couplers.

**TEXT BOOKS:**

- T1 - Reinhold Ludwig and Powel Bretchko, —RF Circuit Design – Theory and ApplicationsI, Pearson Education Asia, First Edition, 2011
- T2- Joseph. J.Cari, —Secrets of RF Circuit DesignI, McGraw Hill Publishers, Third Edition, 2000

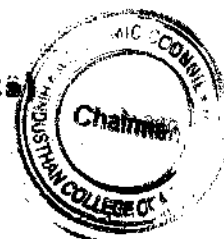
**REFERENCE BOOKS:**

- R1 -Matthew M.Radmanesh, — Radio frequency and Microwave Electronics I, Pearson Education Asia, 2nd Edition ,2002.
- R2- Ulrich L. Rohde and David P. NewKirk, —RF/ microwave Circuit DesignI, John Wiley & Sons USA, 2000
- R3-Roland E. Best, —Phase –Locked loops: Design, simulation and ApplicationsI, McGraw Hill Publishers , 5th Edition,2003

*P. Jayaram*  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



**Dean (Academic)**  
**HICET**



BE	21EC5304	Network Security	3	0	0	3
Course Objective	<ol style="list-style-type: none"> <li>1. To impart knowledge on the Network security services, attacks and mechanisms.</li> <li>2. To introduce the principles of block ciphers and stream ciphers</li> <li>3. To enlighten the concepts of public key cryptography and the authentication techniques.</li> <li>4. To give a clear idea on various Data Integrity algorithms and the methods used for key distribution</li> <li>5. To understand the security services provided to internet.</li> </ol>					

Unit	Description	Instructional Hours
	<b>INTRODUCTION</b>	
I	OSI security architecture –Security Services, Mechanisms and attacks-Network security model-Symmetric cipher model- substitution techniques, transposition techniques, steganography.	9
	<b>SYMMETRIC CIPHERS</b>	
II	Block cipher principles- Data Encryption Standard(DES)-Advanced Encryption Standard (AES)-Multiple Encryption-Triple DES- modes of block cipher-stream ciphers-RC5 algorithm.	9
	<b>ASYMMETRIC CIPHERS</b>	
III	Principles of public key cryptosystems-RSA algorithm-Key management – Diffie Hellman Key exchange- El Gamal cryptography-Elliptic curve arithmetic-Elliptic curve cryptography.	9
	<b>MUTUAL TRUST , AUTHENTICATION AND DATA INTEGRITY</b>	
IV	Mutual trust, Symmetric key distribution using symmetric encryption-symmetric key distribution using asymmetric encryption-distribution of public keys-X.509 Authentication services-Remote user- Authentication principles-Kerberos, Data integrity : Security of hash function and MAC –SHA - HMAC –DSS .	9
	<b>INTERNET SECURITY:</b>	
V	Security Services for E-mail-Pretty Good Privacy-S/MIME. Overview of IP Security – IP security policy-Encapsulation Security Payload (ESP)-SSL/TLS Basic Protocol-combining security associations-Internet key exchange.	9

**Total Instructional Hours**

**45**

Course Outcome

- CO1: Analyze and apply the appropriate Cryptographic technique to overcome the security attacks.  
 CO2: Categorize Symmetric and asymmetric ciphers .  
 CO3: Develop Symmetric and asymmetric ciphers.  
 CO4: Develop a secured system with authentication and integrity services.  
 CO5: Apply the necessary internet security algorithm for various applications.

**TEXT BOOKS:**

- T1- William Stallings, Cryptography and Network Security, 6<sup>th</sup> Edition, Pearson Education, March 2013.  
 T2- Behrouz A. Ferouzan, "Cryptography & Network Security", 3<sup>rd</sup> Edition, Tata Mc Graw Hill, 2007.

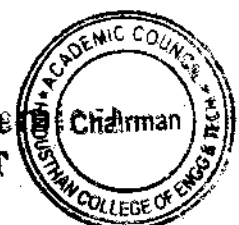
**REFERENCE BOOKS :**

- R1 - Charlie Kaufman, Radia Perlman and Mike Speciner, "Network Security", 2<sup>nd</sup> Edition, Prentice Hall of India, 2002.  
 R2 - Bruce Schneier and Neils Ferguson, "Practical Cryptography", First Edition, Wiley Dream tech India Pvt Ltd, 2003

*P. Hany*  
 CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
 DEAN / PRINCIPAL  
**Dean (Acad)**  
**HICET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC5181	Total Quality Management	3	0	0	3

- Course Objective**
- The student should be able to
1. To learn the quality philosophies and tools in the managerial perspective.
  2. To learn the quality philosophies
  3. To learn the various tools of TQM
  4. To apply the statistical techniques in quality management
  5. To make the students aware about the quality loss and its role in economy

Unit	Description	Instructional Hours
<b>UNIT I INTRODUCTION</b>		
I	Introduction - Need for quality - Evolution of quality - Definitions of quality - Dimensions of product and service quality - Basic concepts of TQM - TQM Framework - Contributions of Deming, Juran 95 and Crosby - Barriers to TQM - Customer focus - Customer orientation, Customer satisfaction, Customer complaints, Customer retention.	9
<b>TQM PRINCIPLES</b>		
II	Leadership - Quality Statements, Strategic quality planning, Quality Councils - Employee involvement - Motivation, Empowerment, Team and Teamwork, Recognition and Reward, Performance appraisal - Continuous process improvement - PDCA cycle, 5S, Kaizen - Supplier partnership - Partnering, Supplier selection, Supplier Rating.	9
<b>TQM TOOLS AND TECHNIQUES I</b>		
III	The seven traditional tools of quality - New management tools - Six sigma: Concepts, Methodology, applications to manufacturing, service sector including IT - Bench marking - Reason to bench mark, Bench marking process - FMEA - Stages, Types.	9
<b>TQM TOOLS AND TECHNIQUES II</b>		
IV	Quality Circles - Cost of Quality - Quality Function Deployment (QFD) - Taguchi quality loss function - TPM - Concepts, improvement needs - Performance measures.	9
<b>QUALITY MANAGEMENT SYSTEM</b>		
V	Introduction—Benefits of ISO Registration—ISO 9000 Series of Standards—Sector-Specific Standards—AS 9100, TS16949 and TL 9000-- ISO 9001 Requirements—Implementation—Documentation—Internal Audits—Registration- ENVIRONMENTAL MANAGEMENT SYSTEM: Introduction—ISO 14000 Series Standards—Concepts of ISO 14001—Requirements of ISO 14001—Benefits of EMS.	9
<b>Total Instructional Hours</b>		<b>45</b>

**Course Outcome**

- After completion of the course the learner will be able to
- CO1: To make the students clear about the quality concepts.  
CO2: The different contributions of quality experts  
CO3: To apply the quality philosophies and tools  
CO4: To facilitate continuous improvement practices and ensure customer delight  
CO5: To make them understand the importance of quality awards as a competitive advantage.

**TEXT BOOKS:**

- T1 - Dale H.Besterfield et al, Total Quality Management, Third edition, Pearson Education (First Indian Reprints 2004).  
T2 - Subburaj Ramasamy, Total Quality Management, Sixth edition, Tata McGraw Hill Education(India) Pvt Ltd, Reprint 2015.

**REFERENCE BOOKS:**

- R1- James R. Evans and William M. Lindsay, "The Management and Control of Quality", 8 th Edition, First Indian Edition, Cengage Learning, 2012.  
R2- Janakiraman. B and Gopal .R.K., "Total Quality Management - Text and Cases", Prentice Hall (India) Pvt. Ltd., 2006..  
R3- Suganthi.L and Anand Samuel, "Total Quality Management:", Prentice Hall (India) Pvt. Ltd., 2006.  
R4- ISO9001-2015 standards

*P. Jayala*

CHAIRPERSON/BOs

**Chairman - BoS  
ECE - HICET**



DEAN/PRINCIPAL  
**Dean (Academics)  
HICET**



Programme	Course Code	Name of the Course	L	T	P	C
B.E.	22CEXXXX	SUSTAINABLE INFRASTRUCTURE DEVELOPMENT	3	0	0	3

- Course Objective**
1. To gain knowledge on concepts and socio-economic policies of sustainable development.
  2. To examine the strategies for implementing sustainable development programmes.
  3. To learn the various sustainability and performance indicators, their assessment techniques and constraints
  4. To explore the different approaches for resource management for a sustainable urban planning.
  5. To understand the principles of urban planning and built-in environment.

Unit	Description	Instructional Hours
	<b>INTRODUCTION TO SUSTAINABLE DEVELOPMENT</b>	
I	Definitions and principles of Sustainable Development - History and emergence of the concept of Sustainable Development - Environment and Development linkages- Globalization and environment – Millennium Development Goals: Status (global and Indian) Impacts on approach to development policy and practice in India, future directions.	9
	<b>ENVIRONMENTAL SUSTAINABILITY</b>	
II	Land, Water and Food production - Moving towards sustainability: Energy powering Sustainable Development - Financing the environment and Sustainable Development.	9
	<b>SUSTAINABILITY INDICATORS</b>	
III	Sustainability indicators – Hurdles to Sustainability-Operational Guidelines-Interconnected prerequisites for sustainable development - Science and Technology for sustainable development – Performance indicators of sustainability and Assessment mechanism – Constraints and barriers for sustainable development.	9
	<b>URBAN PLANNING AND ENVIRONMENT</b>	
IV	Environment and Resources, Sustainability Assessment, Future Scenarios, Form of Urban Region, Managing the change, Integrated Planning, Sustainable Development.	9
	<b>THE BUILT-IN ENVIRONMENT</b>	
V	Urban Form, Land Use, Compact Development, Principles of street design- complete streets, Transport Integrated Urban land use Planning, Guidelines for Environmentally Sound Transportation.	9
	<b>Total Instructional Hours</b>	<b>45</b>

- Course Outcome**
- The students will be able to:
- CO1: Describe the concepts and socio-economic policies of sustainable development.
- CO2: Recognize and identify the strategies for implementing sustainable development programmes.
- CO3: Comprehend the various sustainability and performance indicators, their assessment techniques and constraints
- CO4: Identify the different approaches for resource management for a sustainable urban planning
- CO5: Illustrate the principles of urban planning and built-in environment.

**REFERENCE BOOKS:**

- R1. Gilg A Wand Yarwood R, "Rural Change and Sustainability-Agriculture, the Environment and Communities", CABI Edited by S J Essex, September 2005.
- R2. Ganesh Somayaji and Sakarama Somayaji, "Environmental Concerns and Sustainable development: Some perspectives from India", Editors: publisher TERI Press, ISBN 8179932249.
- R3. James H. Weaver, Michael T. Rock, Kenneth Kustere, "Achieving Broad-Based Sustainable Development: Governance, Environment, and Growth with Equity", Kumarian Press, West Hartford, CT. Publication Year, 1997.
- R4. Kirkby, J, O'Keefe P. and Timberlake, "Sustainable development" Earth Scan Publication, London, 1996.
- R5. Kerry Turner. R, "Sustainable Environmental Management", Principles and Practice Publisher: Belhaven Press, ISBN: 1852930039.
- R6. Munier N, "Introduction to Sustainability", Springer 2005

*P. H. Jayaram*

Chairman, BOS

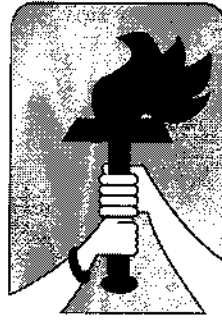
**Chairman - BoS  
ECE - HICET**



*[Signature]*  
Dean - Academics  
**Dean (Academics)  
HICET**

rics)

**HINDUSTHAN  
EDUCATIONAL AND**



**CHARITABLE TRUST**

**HICET**

***HINDUSTHAN***  
***COLLEGE OF ENGINEERING AND TECHNOLOGY***

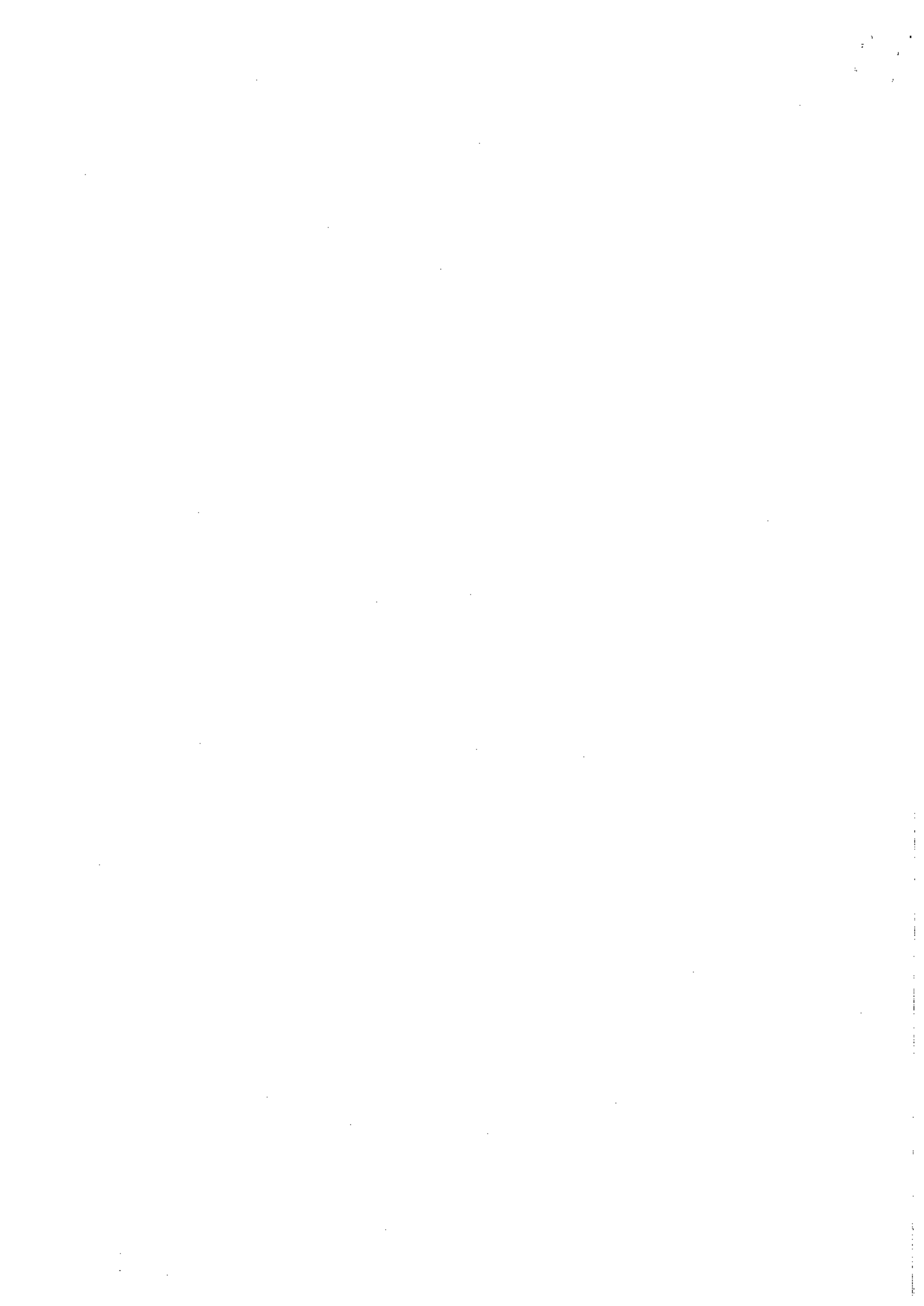
**(An Autonomous Institution)**

**Coimbatore – 641032**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
ENGINEERING**

**Revised Curriculum and Syllabus for the Batch 2020-2024**

**2019 REGULATIONS**





**DETAILS OF CHANGES CARRIED OUT IN CURRICULUM & SYLLABUS**

**CBCS PATTERN**

**UNDERGRADUATE PROGRAMMES**

**B.E ELECTRONICS AND COMMUNICATION ENGINEERING (UG)**

**REGULATION-2019 (Revised on July 2021)**

**Amendments on June 2023**

(For the students admitted during the academic year 2020-2024 and onwards)

**SEMESTER I**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19HE1101	Technical English	HS	2	1	0	3	25	75	100
2	19MA1103	Calculus and Differential Equations	BS	3	1	0	4	25	75	100
<b>THEORY WITH LAB COMPONENT</b>										
3	19PH1151	Applied Physics	BS	2	0	2	3	50	50	100
4	19CY1151	Chemistry for Engineers	BS	2	0	2	3	50	50	100
5	19CS1151	Python Programming and Practices	ES	2	0	2	3	50	50	100
6	19EC1153	Electron devices and Electric Circuits	ES	2	0	2	3	50	50	100
<b>PRACTICAL</b>										
7	19HE1071	Language Competency Enhancement Course-I	HS	0	0	2	1	0	100	100
<b>MANDATORY COURSES</b>										
8	19HE1072	Career Guidance Level – I Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100
<b>Total :</b>				<b>15</b>	<b>2</b>	<b>10</b>	<b>20</b>	<b>350</b>	<b>450</b>	<b>800</b>
As Per AICTE Norms 3 Weeks Induction Programme is Added in The First Semester as an Audit Course										

**SEMESTER II**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19HE2101	Business English for Engineers	HS	2	1	0	3	25	75	100
2	19MA2103	Linear Algebra, Numerical Methods and Transform Calculus	BS	3	1	0	4	25	75	100
<b>THEORY WITH LAB COMPONENT</b>										
3	19PH2151	Material Science	BS	2	0	2	3	50	50	100
4	19CY2151	Environmental Studies	BS	2	0	2	3	50	50	100
5	19CS2152	Essentials of C&C++ Programming	ES	2	0	2	3	50	50	100
6	19ME2154	Engineering Graphics	ES	1	0	4	3	50	50	100
<b>PRACTICAL</b>										
7	19ME2001	Engineering Practices	ES	0	0	4	2	50	50	100
8	19HE2071	Language Competency Enhancement Course-II	HS	0	0	2	1	0	100	100
<b>MANDATORY COURSES</b>										
9	19HE2072	Career Guidance Level – II Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100
10	19HE2073	Entrepreneurship & Innovation	EEC	1	0	0	0	100	0	100
<b>Total :</b>				<b>15</b>	<b>2</b>	<b>16</b>	<b>22</b>	<b>500</b>	<b>500</b>	<b>1000</b>

**SEMESTER III**

S.No	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19MA3102	Fourier analysis and transforms	BS	3	1	0	4	25	75	100
2	19EC3201	Digital Electronics	PC	3	0	0	3	25	75	100
3	19EC3202	Signals and Systems	PC	3	1	0	4	25	75	100
4	19EC3203R	Electronic Circuits	PC	3	0	0	3	25	75	100
<b>THEORY WITH LAB COMPONENT</b>										
5	19CS3252	Oops using Java	PC	2	0	2	3	50	50	100
<b>PRACTICAL</b>										
6	19EC3001	Electronic circuits lab	PC	0	0	3	1.5	50	50	100
7	19EC3002	Digital Electronics Lab	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
8	19MC3191	Indian Constitution	MC	2	0	0	0	100	0	100
9	19HE3072	Career Guidance Level – III Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100
10	19HE3073	Leadership Management Skills	EEC	1	0	0	0	100	0	100
<b>Total</b>				<b>19</b>	<b>2</b>	<b>8</b>	<b>20</b>	<b>550</b>	<b>450</b>	<b>1000</b>

**SEMESTER IV**

S.No	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19MA4104	Probability and Random Processes	BS	3	1	0	4	25	75	100
2	19EC4201R	Electro Magnetic Fields and waves	PC	3	1	0	4	25	75	100
3	19EC4202R	Analog Communication	PC	3	1	0	4	25	75	100
4	19EC4203	Linear Integrated Circuits	PC	3	0	0	3	25	75	100
<b>THEORY WITH LAB COMPONENT</b>										
5	19EC4251	Control Systems	PC	2	0	2	3	50	50	100
<b>PRACTICAL</b>										
6	19EC4001	Linear Integrated Circuits Lab	PC	0	0	3	1.5	50	50	100
7	19EC4002	Analog communication Lab	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
8	19MC4191	Essence of Indian tradition knowledge/Value Education	MC	2	0	0	0	100	0	100
9	19HE4072	Career Guidance Level – IV Personality, Aptitude and Career Development	EEC	2	0	0	0	100	0	100
10	19HE4073	Ideation Skills	EEC	2	0	0	0	100	0	100
<b>Total</b>				<b>20</b>	<b>3</b>	<b>8</b>	<b>21</b>	<b>550</b>	<b>450</b>	<b>1000</b>

**SEMESTER V**

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19EC5201	Microprocessor and Microcontroller	PC	3	0	0	3	25	75	100
2	19EC5202	Transmission lines and WaveGuides	PC	3	1	0	4	25	75	100
3	19EC5203	VLSI Design	PC	3	0	0	3	25	75	100
4	19EC53XX	Professional Elective -I	PE	3	0	0	3	25	75	100
<b>THEORY WITH LAB COMPONENT</b>										
5	19EC5251	Data Communication and Networks	PC	2	0	2	3	50	50	100
6	19EC5252	Digital Signal Processing	PC	2	0	2	3	50	50	100
<b>PRACTICALS</b>										
7	19EC5001	VLSI Design Lab	PC	0	0	3	1.5	50	50	100

8	19EC5002	Microprocessors and Microcontrollers Lab	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
9	19HE5071	Soft Skills - I	EEC	1	0	0	1	100	0	100
10	19HE5072	Design Thinking	EEC	1	0	0	1	100	0	100
<b>Total</b>				<b>18</b>	<b>1</b>	<b>10</b>	<b>24</b>	<b>500</b>	<b>500</b>	<b>1000</b>

### SEMESTER VI

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19EC6201	Digital Communication	PC	3	0	0	3	25	75	100
2	19EC6202	Antenna and Wave Propagation	PC	3	1	0	4	25	75	100
3	19EC6181	Principles of Management	HS	3	0	0	3	25	75	100
4	19EC63XX	Professional Elective - II	PE	3	0	0	3	25	75	100
5	19XX64XX	Open Elective- I	OE	3	0	0	3	25	75	100
<b>THEORY WITH LAB COMPONENTS</b>										
6	19EC6251	Embedded Systems and IOT	PC	2	0	3	3.5	50	50	100
<b>PRACTICALS</b>										
7	19EC6001	Digital Communication Lab	PC	0	0	3	1.5	50	50	100
<b>MANDATORY COURSES</b>										
8	19EC6701	Internship	EEC	-	-	-	1	100	0	100
9	19HE6071	Soft Skills - II	EEC	1	0	0	1	100	0	100
10	19HE6072	Intellectual Property Rights (IPR)	EEC	1	0	0	1	100	0	100
<b>Total</b>				<b>19</b>	<b>1</b>	<b>6</b>	<b>24</b>	<b>525</b>	<b>475</b>	<b>1000</b>

### SEMESTER VII

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19EC7201	Digital Image Processing	PC	3	0	0	3	25	75	100
2	19EC7202	Optical and Microwave Engineering	PC	3	0	0	3	25	75	100
3	19EC73XX	Professional Elective-III	PE	3	0	0	3	25	75	100
4	19XX74XX	Open Elective - II	OE	3	0	0	3	25	75	100
<b>THEORY WITH LAB COMPONENTS</b>										
5	19EC7251	Wireless Communication	PC	2	0	2	3	50	50	100
<b>PRACTICALS</b>										
6	19EC7001	Digital Image processing Lab	PC	0	0	3	1.5	50	50	100
7	19EC7002	Optical Communication and Microwave Lab	PC	0	0	3	1.5	50	50	100

PROJECT WORK										
8	19EC7901	Project Work – Phase I	EEC	0	0	4	2	50	50	100
<b>Total</b>				<b>14</b>	<b>0</b>	<b>12</b>	<b>20</b>	<b>300</b>	<b>500</b>	<b>800</b>

### SEMESTER VIII

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>THEORY</b>										
1	19EC83XX	Professional Elective –IV	PE	3	0	0	3	25	75	100
2	19EC83XX	Professional Elective- V	PE	3	0	0	3	25	75	100
<b>PROJECT WORK</b>										
3	19CH8901	Project Work – Phase II	EEC	0	0	16	8	100	100	200
<b>Total</b>				<b>6</b>	<b>0</b>	<b>16</b>	<b>14</b>	<b>150</b>	<b>250</b>	<b>400</b>

**TOTAL NO OF CREDITS: 165**

### LIST OF PROFESSIONAL ELECTIVES

S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
<b>PROFESSIONAL ELECTIVE I</b>										
1	19EC5301	Measurements and Instrumentation	PE	3	0	0	3	25	75	100
2	19EC5302	PCB Design	PE	3	0	0	3	25	75	100
3	19EC5303	RF System Design	PE	3	0	0	3	25	75	100
4	19EC5304	Network Security	PE	3	0	0	3	25	75	100
5	19EC5181	Total Quality Management	PE	3	0	0	3	25	75	100
<b>PROFESSIONAL ELECTIVE II</b>										
1	19EC6301	Medical Electronics	PE	3	0	0	3	25	75	100
2	19EC6302	Industrial Automation	PE	3	0	0	3	25	75	100
3	19EC6303	Mobile Communication	PE	3	0	0	3	25	75	100
4	19EC6304	High Speed Networks	PE	3	0	0	3	25	75	100
5	19EC6182	E-Commerce Technology	PE	3	0	0	3	25	75	100
<b>PROFESSIONAL ELECTIVE III</b>										
1	19EC7301	Robotics	PE	3	0	0	3	25	75	100
2	19EC7302	ASIC Design	PE	3	0	0	3	25	75	100
3	19EC7303	Global Positioning Systems	PE	3	0	0	3	25	75	100
4	19EC7304	Cloud Computing	PE	3	0	0	3	25	75	100
5	19EC7181	Entrepreneurship Development	PE	3	0	0	3	25	75	100
6	19EC7305	Digital Design Verification	PE	2	0	2	3	25	75	100
7	19EC7306	Embedded Controllers and IoT System Design	PE	2	0	2	3	25	75	100

PROFESSIONAL ELECTIVE IV										
1	19EC8301	Neural networks and Deep learning	PE	3	0	0	3	25	75	100
2	19EC8302	Embedded Controllers	PE	3	0	0	3	25	75	100
3	19EC8303	Satellite Communication	PE	3	0	0	3	25	75	100
4	19EC8304	Wireless Sensors and Networks	PE	3	0	0	3	25	75	100
5	19EC8181	Foundation Skills in Integrated Product Development	PE	3	0	0	3	25	75	100
PROFESSIONAL ELECTIVE V										
1	19EC8306	Artificial Intelligence	PE	3	0	0	3	25	75	100
2	19EC8307	Low Power VLSI	PE	3	0	0	3	25	75	100
3	19EC8308	Software Defined Radio	PE	3	0	0	3	25	75	100
4	19EC8309	Photonic Networks	PE	3	0	0	3	25	75	100
5	19EC8182	Intellectual Property Rights and Innovations	PE	3	0	0	3	25	75	100

### LIST OF OPEN ELECTIVES

ELECTRONICS AND COMMUNICATION ENGINEERING										
S.No.	Course Code	Course Title	Category	L	T	P	C	CIA	ESE	TOTAL
1	19EC6401	Consumer Electronics	OE	3	0	0	3	25	75	100
2	19EC7401	Introduction to IOT	OE	3	0	0	3	25	75	100
LIFE SKILL COURSES										
3	19LSZ401	General Studies for Competitive Examinations	OE	3	0	0	3	25	75	100
4	19LSZ402	Human Rights, Women's Rights and Gender Equality	OE	3	0	0	3	25	75	100
5	19LSZ403	Indian Ethos and Human Values	OE	3	0	0	3	25	75	100
6	19LSZ404	Indian Constitution and Political System	OE	3	0	0	3	25	75	100
7	19LSZ405	Yoga for Human Excellence	OE	3	0	0	3	25	75	100

		Excellence								
--	--	------------	--	--	--	--	--	--	--	--

(Note: Z Stands for semester, students can't choose twice the course)


### LIST OF INDUSTRIAL CORE COURSES

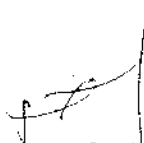
S.No.	CODE	Courses	CAT	L	T	P	C	CIA	ESE	TOTAL
1	19CS1152	Object Oriented Programming using Python	IC	2	0	2	3	50	50	100
2	19CS2153	Java Fundamentals	IC	2	0	2	3	50	50	100
3	19IT3252	Relational Database Management System	IC	2	0	2	3	50	50	100
4	19CS4204	Design Thinking	IC	2	0	2	3	50	50	100
5	19CS5231	Angular JS	IC	3	0	0	3	25	75	100
6	19CS6231	Node JS and Micro services	IC	3	0	0	3	25	75	100
7	19EC6252	IoT and Spring Framework	IC	3	0	0	3	25	75	100
8	19EC7305	Blockchain	IC	3	0	0	3	25	75	100


### CREDIT DISTRIBUTION

Semester	I	II	III	IV	V	VI	VII	VIII	Total
Credits	20	22	20	21	24	24	20	14	165

\* Student can earn extra credit 35 over and above the total credits

  
Chairman, Board of Studies  
**Chairman - BoS**  
**ECE - HICET**

  
Dean - Academics  
**Dean (Academics)**  
**HICET**

  
**PRINCIPAL**  
Ananthan College Of Engineering & Tech  
COMBATORE - 643 032





Programme	Course Code	Name of the Course	L	T	P	C
BE	19EC7201	Digital Image Processing	3	0	0	3

- Course Objective**
1. To study the formation of an image and its acquisition.
  2. To know about image enhancement in both time and frequency domains.
  3. To be familiar with and restoration and segmentation techniques.
  4. To know the widely used image compression algorithms.
  5. To understand the image recognition concepts and image representation in the form of features

Unit	Description	Instructional Hours
I	<b>DIGITAL IMAGE FUNDAMENTALS</b> Introduction – Fundamental Steps in Digital Image Processing –Components of an Image Processing System, Elements of Visual Perception – Image Sensing and Acquisition – Image Sampling and Quantization – RGB and HSI color models.	9
II	<b>IMAGE ENHANCEMENT</b> <b>Spatial Domain:</b> Gray level transformations – Histogram processing: Histogram equalization – Basics of Spatial Filtering –Smoothing and Sharpening Spatial Filtering - Homomorphic filtering, Color image enhancement <b>Frequency Domain:</b> Smoothing and Sharpening frequency domain filters – Ideal, Butterworth and Gaussian filters.	9
III	<b>IMAGE RESTORATION AND SEGMENTATION</b> <b>Restoration:</b> Image Restoration degradation model– Mean Filters – Inverse Filtering – Wiener filtering- Geometric transformations-spatial transformations. <b>Segmentation:</b> point, line,edge detection-Edge Linking via Hough transformation – Region based segmentation: Region Growing, Region splitting and merging - Practical applications –process an image using various segmentation techniques.	9
IV	<b>MORPHOLOGICAL PROCESSING AND IMAGE COMPRESSION</b> <b>Morphological processing-</b> Dilation and Erosion-Segmentation by morphological watersheds. <b>Compression:</b> Fundamentals – Error Free Compression – Variable Length Coding: Huffman coding, Arithmetic Coding – Compression Standards: JPEG and MPEG.	9
V	<b>PATTERN CLASSIFICATION</b> Feature extraction-Boundary representation – Chain Code, Signature, skeleton –boundary descriptor-shape number- Patterns classification methods- supervised and unsupervised neural networks in Pattern recognition.	9

**Total Instructional Hours 45**

After completion of the course the learner will be able to

- Course Outcome**
- CO1: Explain and relate the concepts of digital image fundamentals.
  - CO2: Choose appropriate technique for image enhancement both in spatial and frequency domains.
  - CO3: Restore good quality images from the degraded one and Segment different aspects of the image
  - CO4: Categorize various compression techniques and interpret image compression standards.
  - CO5: Represent the image with various features and recognize an image from its features.

**TEXT BOOKS:**

- T1- Rafael C Gonzalez, Richard E Woods, “Digital Image Processing”, Pearson Education Inc, Fourth Edition, 2018. (Units I – V)
- T2- Anil K- Jain, “Fundamentals of Digital Image Processing”, Pearson/Prentice Hall of India, 2002.

*P. Hayla*  
CHAIRPERSON/BOS  
**Chairman - Bos**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**



**REFERENCE BOOKS:**

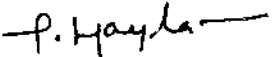
R1- Annadurai and Shanmughalakshmi, "Fundamentals of Digital Image Processing", Pearson India, 2006.

R2 - S.Jayaraman, S.Esakkirajan, T.Veerakumar, "Digital Image Processing", TMH New Delhi, 2009

R3 - Kenneth R. Castleman, "Digital Image Processing", Pearson, 2006

R4- Rafael C. Gonzalez, Richard E. Woods, Steven Eddins, "Digital Image Processing using MATLAB ", Pearson Education, Inc., 2004.

R5- William K. Pratt, "Digital Image Processing", John Wiley, New York, 2002.

  
CHAIRPERSON/BOS  
Chairman - SoS  
ECE - HICET



  
DEAN (ACADEMICS)  
HICET

Programme	Course Code	Name of the Course	L	T	P	C
BE	19EC7202	Optical and Microwave Engineering	3	0	0	3

The student should be able to

**Course Objective**

1. To facilitate the knowledge about optical fiber sources and transmission techniques
2. To understand the concepts of signal degradation in optical fibers.
3. To inculcate understanding of the fiber optical sources, receivers and coupling.
4. To understand the functional behavior of microwave semiconductor devices and microwave tubes
5. To enhance the knowledge in various Measurements of Microwave network

Unit	Description	Instructional Hours
	<b>INTRODUCTION TO OPTICAL FIBERS</b>	
I	Elements of an Optical fiber communication system- Optical laws and definitions- optical modes and configurations -mode analysis for optical propagation through fibers modes in planar wave guide-modes in cylindrical optical fiber - Fiber materials--single mode fiber – multimode fiber-graded index fiber.	9
	<b>TRANSMISSION CHARACTERISTIC OF OPTICAL FIBER</b>	
II	Attenuation-absorption --scattering losses-bending losses-core and cladding losses-signal dispersion –Inter symbol interference and bandwidth-Intra modal dispersion-Material dispersion- Waveguide dispersion-Polarization mode dispersion-Intermodal dispersion- Dispersion optimization of single mode fiber-characteristics of single mode fiber-R-I Profile-cutoff wave length-dispersion calculation-mode field diameter.	9
	<b>OPTICAL SOURCES, DETECTORS, RECEIVER AND COUPLING</b>	
III	Sources: - surface emitting LED-Edge emitting LED-quantum efficiency and power-modulation of LED –LASER diodes -modes and threshold conditions-Rate equations-external quantum efficiency- Detectors: PIN photo detector-Avalanche photo diodes- noise-SNR- detector response time-Avalanche multiplication noise-temperature effects - preamplifiers-digital receiver performance-probability of error and receiver sensitivity-quantum limit. - Lensing Schemes for Coupling Management- -LED Coupling to Single Mode Fibers	9
	<b>MICROWAVE PASSIVE COMPONENTS AND SEMICONDUCTOR DEVICES</b>	
IV	Microwave Passive components: Directional Coupler, Power Divider, Magic Tee, attenuator, resonator, Principles of Microwave Semiconductor Devices: Gunn Diodes, IMPATT diodes, Schottky Barrier diodes, PIN diodes, Microwave tubes: Klystron, TWT, Magnetron.	9
	<b>MICROWAVE MEASUREMENTS</b>	
V	Measuring Instruments – VSWR meter, Power meter, Spectrum Analyser, Network Analyser – Principles; Measurement of Impedance, Frequency, Power, VSWR, Q factor, Dielectric Constant, S Parameter- Hazards of microwaves.	9
<b>Total Instructional Hours</b>		<b>45</b>

*P. Mary La*  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**



**Course  
Outcome**

After completion of the course the learner will be able to

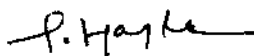
- CO1: Realize basic elements in optical fibers, different modes and configurations  
CO2: Analyze the transmission characteristics associated with dispersion and polarization techniques.  
CO3: Design optical sources, detectors and coupling techniques with their use in optical communication system  
CO4: Analyze various microwave semiconductor devices.  
CO5: Analyze various waveguide components and performance of microwave tubes and Measurements

**TEXT BOOKS:**

- T1- Gerd Keiser "Optical Fiber Communication", McGraw Hill Education (India) Private Limited. Fifth Edition, Reprint 2013. (UNIT I, II, III)  
T2 - Annapurna Das and Sisir K Das, "Microwave Engineering", Mc Graw Hill Inc., 2004. (UNIT IV, V)

**REFERENCE BOOKS:**

- R1. John M.Senior, —Optical fiber communication, Pearson Education, second edition.2007. (UNIT I, II, III)  
R2 - D.M.Pozar, "Microwave Engineering.", John Wiley & sons, Inc., 2006. . (UNIT IV, V)  
R3- Samuel Y Liao, "Microwave Devices & Circuits", Prentice Hall of India, 2006. (UNIT IV, V)

  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

Programme	Course Code	Name of the course	L	T	P	C
BE	19EC7251	Wireless Communication	2	0	2	3

- Course Objective**
- To impart knowledge on Wireless communication.
  - To understand the performance of digital Modulation over wireless channel.
  - To interpret the various challenges in multi carrier modulation and design issues.
  - To provide an outline on cellular concepts and system design fundamentals.
  - To Study various Multiple Access techniques for wireless channels

**Unit**

	<b>Introduction to Wireless Communications</b>	
I	Overview of wireless systems –Wireless Spectrum –Path Loss and Shadowing –Radio wave propagation –Transmit and Receive signal Models –Free-Space path loss- ray tracing- Empirical Path Loss model path loss models- Shadow fading.	6
	<b>Performance of Digital Modulation over Wireless Channel and Diversity</b>	
II	AWGN Channels--Fading– Outage Probability– Average Probability of Error — Combined Outage and Average Error Probability – Doppler Spread – Inter symbol Interference. Realization of Independent Fading Paths – Receiver Diversity – Selection and Threshold Combining–Transmitter Diversity – Channel known at Transmitter – Channel unknown at Transmitter – The Alamouti Scheme	6
	<b>Multicarrier Modulation</b>	
III	Challenges in Multicarrier Systems-Data transmission using multiple carrier-Multicarrier modulation with Overlapping subchannels-Mitigation of subcarrier Fading- Discrete Implementation of Multicarrier Modulation-OFDM	6
	<b>Cellular Architecture-System Design Fundamentals</b>	
IV	Cellular concepts, Frequency reuse, channel assignment strategies, handoff strategies, interference and system capacity, improving coverage and capacity in cellular systems.	6
	<b>Multiple Access Techniques for Wireless Communication</b>	
V	Introduction to Multiple Access- Frequency Division Multiple Access (FDMA)-Time Division Multiple Access(TDMA)-Spread Spectrum Multiple Access-Code division Multiple Access (CDMA)-Space Division Multiple Access (SDMA)	6
	<b>Total Hours</b>	30

**List of Experiments**

**Study of wireless Communications using Communication Trainer Kits**

- To study the FHSS Modulation and Demodulation Techniques
- To study the DS spread spectrum Modulation and Demodulation Technique

*P. Hay*  
 CHAIR PERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
 DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

3. To study the Code Division Multiple Access (CDMA) with Multiuser

4. To study Baseband Communication

5. To study and implement Adaptive Linear Equalizer

**Wireless Path loss Computations - Study of Propagation Path loss Models (Using Mat lab Programming)**

6. Free Space Propagation – Path Loss Model

7. Link Budget Equation for Satellite Communication

**Total Instructional Hours 15**

**Total Hours 30+15=45**

**Course  
Outcome**

CO1: Demonstrate the signal propagation over wireless radio channel.

CO2: Illustrate the performance of digital modulation technique over AWGN channels

CO3: Infer the idea of multicarrier modulation in wireless system.

CO4: Describe the cellular concepts for solving spectral congestion and user capacity.

CO5: Summarize various Multiple Access Techniques for wireless channel.

#### TEXT BOOKS

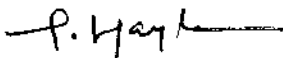
T1. Andrea Goldmith, "Wireless Communication", Cambridge University Press, 2005.(Unit I ,II and III)

T2. T.S. Rappaport, "Wireless Communication, Principles and Practice", Pearson Education, Second Edition, 2002 (UNIT IV and UNIT V)

#### REFERENCE BOOKS:

R1. David Tse and Pramod Viswanath, "Fundamentals of Wireless Communication", Cambridge University Press, 2005.

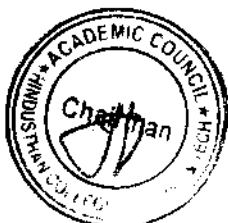
R2. William C Y Lee, "Wireless and Cellular Communications", Tata McGraw Hill Publishing Company Limited, Third Edition, 2006..



CHAIRPERSON/BOS

**Chairman - BoS**

**ECE - HICET**



  
**Dean (Academics)**  
**HICET**





Programme	Course Code	Name of the Course	L	T	P	C
BE	19EC7001	Digital Image Processing Lab	0	0	3	1.5

- Course Objective
1. To manipulate the image parameters
  2. To improve the quality of an image
  3. To detect and segment the boundary in an image.
  4. To compress image using coding techniques.
  5. To classify different pattern classes.

**S.NO LIST OF EXPERIMENTS**

**Simulation using MATLAB / EQUIVALENT SOFTWARE PACKAGE**

1. Program for extraction of color components from RGB color image.
2. Program for an image enhancement using pixel operation.
3. Program for image enhancement using histogram equalization.
4. Program to filter an image using averaging low pass filter in spatial domain and median filter.
5. Program to sharpen an image using 2-D Laplacian high pass filter in spatial domain.
6. Program to smooth an image using low pass filter and high pass filter in frequency domain (Butterworth LPF and HPF)
7. Program for morphological image operations-erosion, dilation, opening & closing
8. Program for image segmentations using region based segmentation technique
9. Program for image compression using Huffman coding
10. Program for Pattern classification methods.

TOTAL HOURS 45

- Course Outcome
- CO1: Analyze color image processing
  - CO2: Enhance the visual quality of an image
  - CO3: Detect the edges and boundary in an image
  - CO4: Demonstrate the applications of segmentation algorithms
  - CO5: Classify different pattern classes.

*P. Mayi*  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

Programme	Course Code	Name of the Course	L	T	P	C
BE	19EC7002	Optical Communication and Microwave Lab	0	0	3	1.5

### LIST OF EXPERIMENTS

#### OPTICAL EXPERIMENTS

1. DC Characteristics of LED and PIN Photo diode
2. Coupling and bending losses of Fibers
3. Fiber optic Analog and Digital Link
4. Numerical Aperture determination for Fibers
5. Attenuation Measurement in Fibers

#### MICROWAVE EXPERIMENTS

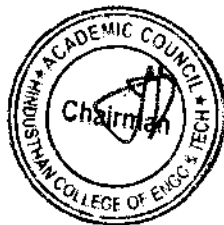
6. Characteristics of Gunn diode
7. Characteristics of Reflex Klystron
8. Directional Coupler Characteristics.
9. S-parameter Measurement of the following microwave components (Isolator, Circulator, E plane Tee, H Plane Tee, Magic Tee)
10. Radiation Pattern of Horn Antenna.

Course  
Outcome

- CO1: Analyze the performance of various microwave links.  
 CO2: Analyze the performance of various optical links.  
 CO3: Test microwave components  
 CO4: Analyze the radiation of pattern of antenna.  
 CO5: Test optical components

TOTAL HOURS 45

*P. Hayte*  
 CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
 DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

Programme	Course Code	Name of the Course	L	T	P	C
BE	19EC7901	Project Work –Phase I	0	0	4	2

- The student should be able to
- Course Objective**
1. Work in teams to propose, formulate, and solve a challenging open-ended design problem of significant scope, depth, and breadth.
  2. Understand and incorporate engineering standards and multiple realistic constraints, within realistic design time, budget, and performance objectives.
  3. Develop a prototype of the proposed design and demonstrate the prototype in accordance with the specifications.
  4. Effectively communicate information relating to all aspects of the design process in written, oral, and graphical form.

- | S.No | Guidelines   |
|------|--|
| 1.   | Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.  |
| 2.   | Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.  |
| 3.   | Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.                           |
| 4.   | Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.   |
| 5.   | Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.  |
| 6.   | Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.  |
| 7.   | Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.  |
| 8.   | Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.   |
| 9.   | Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.  |
| 10.  | Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions. |
| 11.  | Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.  |
| 12.  | Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.  |

**Course Outcome**

- CO1. Formulate a real-world problem, identify the requirement and develop the design solutions.
- CO2. Identify technical ideas, strategies and methodologies.
- CO3. Utilize the new tools, algorithms, techniques that contribute to obtain the solution of the project. Test and validate through conformance of the developed prototype and analysis the cost effectiveness.
- CO4. Prepare report and present the oral demonstrations.

*S. Hayat*

CHAIRPERSON/BOS

**Chairman - BoS  
ECE - HICET**



**Dean (Academics)  
HICET**

**Programme** BE      **Course Code** 19EC7302      **Name of the Course** ASIC Design      **L** 3      **T** 0      **P** 0      **C** 3

**Course Objective**

1. To learn the fundamentals of ASIC and CMOS logic design
2. To familiarize with the various principles of programmable ASIC design
3. To impart knowledge on ASIC architecture and various logic synthesis techniques
4. To provide an insight on the concepts of delay models and logic simulation.
5. To familiarize with the concepts of floor planning and system partitioning

**Unit**

**Description**

**Instructional Hours**

I	<b>ASIC AND CMOS LOGIC DESIGN</b> Types of ASICs - Design flow - CMOS transistors - Combinational Logic Cell – Sequential logic cell - Data path logic cell - Transistors as Resistors - Transistor Parasitic Capacitance.	9
II	<b>PROGRAMMABLE ASIC</b> Anti fuse - static RAM - EPROM and EEPROM technology - Actel ACT - Xilinx LCA – Altera FLEX - Altera MAX DC & AC inputs and outputs - Clock & Power inputs - Xilinx I/O blocks.	9
III	<b>ASIC ARCHITECTURE AND LOGIC SYNTHESIS</b> Architecture and configuration of Spartan and Virtex FPGAs- Logic Synthesis with an example-Finite State Machine Synthesis - Memory Synthesis.	9
IV	<b>LOGIC SIMULATION</b> Simulation-Logic Systems - Cell Models - Delay Models - Static Timing Analysis - Formal verification - Switch level and Transistor level simulation.	9
V	<b>ASIC CONSTRUCTION</b> System Partitioning – FPGA Partitioning, Partitioning Methods- Kernighan-Lin algorithm. Floor Planning - Placement-min cut & Eigen value algorithm - Routing-Global & Detailed routing.	9

**Total Instructional Hours**

**45**

**Course Outcome**

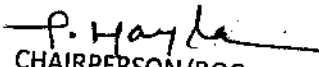
- After completion of the course, the student will be able to
- CO1: Understand the basic ASIC and CMOS logic design.
  - CO2: Understand various types of Programmable ASICs.
  - CO3: Understand the ASIC architecture and logic synthesis.
  - CO4: Understand the various techniques used in the logic simulation and delay models.
  - CO5: Understand the various methods system floor planning and partitioning.

**TEXT BOOKS:**

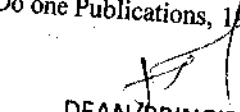
T1- M.J.S.Smith, " Application - Specific Integrated Circuits", Pearson, 2003. -UNIT I,II,III,IV,V

**REFERENCE BOOKS:**

- R1. Steve Kilts, "Advanced FPGA Design," Wiley Inter-Science.
- R2. Roger Woods, John McAllister, Dr. Ying Yi, Gaye Lightbod, "FPGA-based Implementation of Signal Processing Systems", Wiley, 2008.
- R3. Mohammed Ismail and Terri Fiez, "Analog VLSI Signal and Information Processing ", McGraw Hill, 1994
- R4. Douglas J. Smith, HDL Chip Design, Madison, AL, USA: Do one Publications, 1996.

  
 CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HICET**



  
 DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

Programme	Course Code	Name of the Course	L	T	P	C
B.E./B.Tech.	19EC7305	DIGITAL DESIGN VERIFICATION	2	0	2	3

- Course Objective**
1. To understand the basics of HDL - Verilog
  2. To comprehend the basics verification language and system Verilog
  3. To get insights into the system Verilog constraints and arrays
  4. To be familiar with inter process communication, interface and coverage
  5. To get through the verification architecture and environment construction

Unit	Description	Instructional Hours
I	<b>BASICS OF HDL - VERILOG</b> Introduction - Data types (reg,wire) - Operators - Assignments (Blocking, Non Blocking, Continuous, Procedural statement, delay) - Control Construct (if-else, case, forever, repeat, while, for loop) - Structure (Module, port, signals) - Procedural Block (always, initial, function, task) - Exercise on Designing Digital Circuit (combinational, sequential).	9
II	<b>BASICS OF VERIFICATION CONSTRUCTS , DATA TYPES AND OOPS</b> Importance of Verification language and System Verilog(SV) – Difference between Verilog and SV – SV Data types – 2 State Data types – 4 State Data types – User-defined data types - OOPs – Encapsulation – Polymorphism – Inheritance - Built-in Methods: Randomization, object Creation (new) – User Defined Methods: print method, copy methods (copy-by-handle, Shallow copy, Deep Copy). Exercise on implementing SV Constructs.	9
III	<b>SV CONSTRAINTS AND ARRAYS</b> Simple Constraints – Weighted Distributed Constraint – Implication Operator Based Constraint – If-else Constraint – Variable Order Based Constraint – Soft Constraint – Inline Constraint – Unique Constraint – For each Constraint – Inside Constraint – SV Arrays – Pack & Unpack Arrays – Static Array – Dynamic Arrays (Dynamic, Associative and Queue Array) - Exercise on implementing transactions with constraints.	9
IV	<b>INTER PROCESS COMMUNICATION (IPC), INTERFACE AND COVERAGE</b> IPC - Semaphore - Mailbox - Events - Interface - Virtual Interface - Modport - Clocking Block - Coverage - Functional Coverage - Code Coverage - Cross Coverage - Coverage option - Exercise on Constructing Synchronization module between Testbench Components.	9
V	<b>VERIFICATION ARCHITECTURE AND ENVIRONMENT CONSTRUCTION</b> SV Verification Blocks - Generator Block – Bus Functional Model(BFM) Block – Interface Block – Monitor Block – Scoreboard Block – Coverage Block – Environment Block – Program Block – Top Module - Exercise on constructing SV environment for Combinational and Sequential circuit.	9

**Total Instructional Hours** 45

**Lab Experiments**

15

1. Implement a Parameterized simple FIFO with queue Inputs: data\_in, w\_fifo, r\_fifo; Outputs: empty, full, data\_out. Manage them with built in methods and show the output.
2. Design a Content Addressable Memory (CAM) using Associate Array and show their output in System verilog.
3. Declare a register to perform write read operation using Pre and post randomization methods.
4. Design an Inter process Synchronization - Mailbox between Generator and Driver component to receive the transaction using System Verilog.
5. Design an Interface with Modport and Clocking Block using System verilog.

*P. Hayla*  
CHAIRPERSON/BOS  
**Chairman - BOS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL

**Dean (Academics)**  
**HICET**

<b>Course Outcome</b>	CO1:Understand the basics of HDL - Verilog
	CO2:Comprehends the basics verification language and system Verilog
	CO3: Get insights into the system Verilog constraints and arrays
	CO4: Familiar with inter process communication, interface and coverage
	CO5:Get through the verification architecture and environment construction

**TEXT BOOKS:**

T1-S.Palnitkar,Verilog HDL:A Guide to Digital Design and Synthesis,Prentice Hall, 2nd edition, 2003.

T2- Chris Spear and Greg Tumbush , System Verilog for Verification, Third Edition, Springer US ,2012

**REFERENCE BOOKS:**

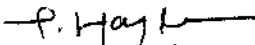
R1-Stuart Sutherland , Simon Davidmann , Peter Flake , SystemVerilog for Design, Second Edition, Springer New York, NY,2006.

R2-<https://www.chipverify.com/systemverilog/systemverilog-tutorial/>


R3-<https://verificationguide.com/systemverilog/systemverilog-tutorial/>

R4-<http://www.testbench.in/>

R5-System Verilog LRM

  
CHAIRPERSON/BOS  
**Chairman - BoS**  
**ECE - HiCET**



  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HiCET**

Programme	Course code	Name of the course	L	T	P	C
BE	19EC7306	<b>EMBEDDED CONTROLLERS AND IOT SYSTEM DESIGN</b>	2	0	2	3

Course Objective

- 1.To introduce the concept of Embedded controllers.
- 2.To gain knowledge about MSP430 Peripheral Interfacing
- 3.To gain knowledge about multi tasking and the real time operating system.
- 4.To learn the protocols of IoT and fundamentals of Embedded Networking
- 5.To understand the CC3200 SimpleLink Wi-Fi Module.

Unit	Description	Instructional Hours
I	<p><b>Introduction to Embedded Controllers</b>            Definition, History, Classification, Characteristics and quality attributes of Embedded System, Application Case study: Automobile industry.MSP430 Family Architecture, Features of embedded C as applicable to MSP430, development environment, Fundamentals of Physical Interfacing. Digital Output, Configuration of Digital Output, Introduction to LED, different connection methods of LED. Digital Input, Configuration of Digital Output, Introduction to Switches, different connection methods of Switch.</p> <p><b>MSP430 Peripheral Interfacing</b>            Introduction to analog signal, DAC basics, PWM signal duty cycle calculation, analog output Exercises, ADC basics, analog input Exercises, Introduction to Sensors, Analog sensor interfacing (Temperature Sensor), Digital Sensor Interfacing (PIR Sensor), Ultrasonic Sensor interfacing.</p>	6
II	<p><b>MSP430 Timer, Communication Protocols &amp; RTOS</b>            Timers: Classification of timers, Watchdog Timer, interrupts, Communication Protocols: UART, SPI, I2C.The challenge of multitasking and real time, multitasking with sequential programming, State machines, Real time operating system, RTOS services</p>	6
III	<p><b>IOT Introduction</b>            Definition and Characteristics of IoT, Things in IoT, IoT Protocols, Functional Blocks, Communication Models, IPv6 over IPv4. Embedded Networking fundamentals. IoT overview and architecture. Cloud of Things</p>	6
IV	<p><b>IOT Implementation</b>            Introduction to CC3200 SimpleLink Wi-Fi Module – Block Diagram, Features, applications. Overview of wireless sensor networks and design examples. Various wireless protocols and its applications. Introduction to HTTP, IoT Case Studies and Challenges Associated with IoT.</p>	6
V	<p><b>List of Experiments</b></p> <ol style="list-style-type: none"> <li>1. To blink the inbuilt LED.</li> <li>2. To interface a external LED.               <ol style="list-style-type: none"> <li>a) Positive Logic Connection of LED</li> <li>b) Negative Logic Connection of LED</li> </ol> </li> <li>3. To glow inbuilt LED using inbuilt switch.</li> <li>4. To interface external push button.               <ol style="list-style-type: none"> <li>a) Positive Logic Connection of switch.</li> <li>b) Negative Logic Connection of switch.</li> </ol> </li> <li>5. To fade out an inbuilt led using analog output.</li> <li>6. Introduction to Serial Monitor.</li> <li>7. To read the analog voltage using ADC.</li> <li>8. To interface analog temperature sensor.</li> </ol>	15

*P. Hark*  
 CHAIRPERSON/BOS  
**Chairman - F.O.S**  
**ECE - HICET**



*[Signature]*  
 DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

9. To detect occupancy of an area using PIR sensor.
10. To measure the distance of an object using ultrasonic sensor
11. Introduction to serial communications with two devices.
12. HTTP data uploading to webpage.
13. HTTP data controlling using webpage.
14. Uploading data to cloud using HTTP.

**Hardware Requirement:**

1. MSP430 Launchpad kit.
2. CC3200 Launchpad kit.
3. Temperature Sensor-LM35/TMP36
4. PIR sensor
5. Ultrasonic sensor
6. Push Switch
7. LED
8. Resistor 220 ohm
9. Pot 1 K ohm
10. Bread Board, Jumper and Connection Wires.

**Software Requirement:**

1. Energia IDE open source

**Total Instructional Hours**

45

**On the completion of the course the students could able to:**

After completion of the course the learner will be able to

Course Outcome

- CO1: Understand the MSP430 Family Architecture, Features of embedded C.
- CO2: Gain knowledge about MSP430 Peripheral Interfacing
- CO3: Apply the concept of multitasking and RTOS in embedded system design
- CO4: Understanding the protocols of IoT and fundamentals of Embedded Networking
- CO5: Get insights of CC3200 SimpleLink Wi-Fi Module

**TEXT BOOKS**

- T1-MSP430 Microcontroller Basics - John Davies, Elsevier, 2008.
- T2- Sudip Mishra, Anandarup Mukherjee, Arijit Roy: Introduction to IOT, Cambridge University Press

**REFERENCE BOOKS**

- R1-Microcontroller Programming and Interfacing with Texas Instruments MSP430FR2433 and MSP430FR5994-Steven F. Barrett, Daniel J. Pack, Morgan & Claypool, 2019
- R2-Texas Instrument MSP430 reference Page:  
[http://www.ti.com/lstds/ti/microcontrollers\\_16-bit\\_32-bit/msp/overview.page](http://www.ti.com/lstds/ti/microcontrollers_16-bit_32-bit/msp/overview.page)
- R3-Energia is an open-sourced, community-driven IDE- <http://energia.nu/guide/>
- R4-Embedded Software Premier David Simon (Pearson)
- R5-<https://www.ti.com/lit/pdf/swru372>
- R6-[https://www.ti.com/lit/ds/symlink/cc3200.pdf?ts=1685867757024&ref\\_url=https%253A%252F%252Fwww.google.com%252F](https://www.ti.com/lit/ds/symlink/cc3200.pdf?ts=1685867757024&ref_url=https%253A%252F%252Fwww.google.com%252F)
- R7-Arshdeep Bahga, Vijay Madiseti, "Internet of Things, A Hands-on-Approach", 1st Edition, Universities press Pvt. Ltd., India, 2015

*P. Hari*  
CHAIRPERSON/BOS

**Chairman - BoS  
ECE - HICET**

*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**



Programme	Course Code	Name of the Course	L	T	P	C
BE	21EC6401	Consumer Electronics	3	0	0	3

**Course Objective**

The student should be able to

1. Sketch and describe operating principles of different types of microphones.
2. Learn various components of video system and displays.
3. Describe working of Washing machine, Microwave ovens, Refrigerators.
4. Understand the working principles of power supplies.
5. Learn various standards in product compliances.

Unit	Description	Instructional Hours
I	<b>AUDIO SYSTEMS</b> Microphones, loudspeakers baffle and enclosure, Acoustics, mono, stereo, Quad, Amplifying System, Equalizers and Mixers, Synthesizers, Commercial Sound, Theater Sound System.	9
II	<b>VIDEO SYSTEMS AND DISPLAYS</b> Monochrome, Color TV standards, TFT, Plasma, HDTV, LCD, LED TV, Direct-To Home (DTH- Set Top Box), Video Telephone and Video Conferencing.	9
III	<b>DOMESTIC AND CONSUMER APPLIANCES</b> Washing machines, Microwave ovens, Air-conditioners and Refrigerators, Computers office System, Telephone & Mobile Radio System.	9
IV	<b>POWER SUPPLIES</b> SMPS/UPS and Preventive Maintenance and others systems such as Remote controls, Bar codes, RFID.	9
V	<b>PRODUCT COMPLIANCE</b> Product safety and liability issues; standards related to electrical safety and fire hazards, EMI/EMC requirements, design techniques for ESD, RF interference and immunity, line current harmonics and mains voltage surge.	9
<b>Total Instructional Hours</b>		<b>45</b>

**Course Outcome**

After completion of the course the learner will be able to

1. Understand electronics engineering concepts used in audio systems.
2. Identify and explain working of various colour TV and Display blocks.
3. Understand the basic functions of various domestic and consumer electronic goods.
4. Understand various types of power supplies, Remote and RFID.
5. Use different product safety, compliance standards and techniques associated with electronic products.

**TEXT BOOKS:**

- T1 - SP Bali, "Consumer Electronics", Pearson Education, 2008  
T2 - J.S. Chitode, "Consumer Electronics", Technical Publications, 2007

**REFERENCE BOOKS:**

- R1 - Jan Holler, VlasiosTsiatsis, Catherine Mulligan, Stefan Avesand, Stamatis Karnouskos, David Boyle, "From Machine-to-Machine to the Internet of Things: Introduction to a New Age of Intelligence", 1st Edition, Academic Press, 2014.  
R2 - Marco Schwartz, "Internet of Things with the Arduino Yun", Packt Publishing, 2014.

*P. Nayak*  
CHAIRPERSON/BOS

**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

Name of the Course	L	T	P	C
Introduction to IOT (Common to all Branches)	3	0	0	3

to  
 tals of Internet of Things.  
 bedded system using Arduino / Raspberry Pi or equivalent boards.  
 net of Things in the real world scenario  
 m with specifications and requirements.  
 stem using IoT

currencies, Smart Contracts, and

**Description**

**Instructional Hours**

**n Overview**  
 s-Physical design - Protocols – Logical design –  
 IoT Levels – Domain Specific IoTs: Home 9

it – IoT Platforms Design Methodology –  
 and Application Development. 9

y Pi Interfaces – Programming – Other IoT 9

**o/Arduino**  
 ring the Linux Console - Arduino IDE –  
 io Language Reference and APIs – Servo API. 9

**se Studies**  
 ud Offerings: WAMP – Django – Amazon Web  
 art Lighting – Weather Monitoring System –  
 ter. 9

**Total Instructional Hours 45**

course the learner will be able to  
 various tools.  
 IoT using Arduino/ equivalent boards and relevant protocols.  
 es to access/control IoT devices.  
 ication and connect to the cloud.  
 is of IoT in real time scenario

et of Things – A hands-on approach”, Universities Press, 2015. (Unit

ad Intel® Galileo Gen 2: API Features and Arduino Projects for Linux

Aulligan, Stefan Avesand, Stamatis Karnouskos, David Boyle, “From  
 s: Introduction to a New Age of Intelligence”, 1st Edition, Academic

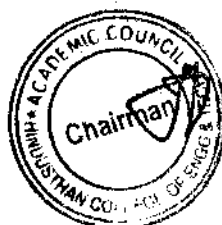
the Arduino Yun”, Packt Publishing, 2014.

Approved By

DEAN

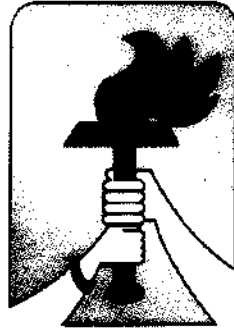
ACADEMICS

**Dean (Academics),  
 HICET**



DEAN/PRINCIPAL  
**Dean (Academics)**  
**HICET**

**HINDUSTHAN  
EDUCATIONAL AND**



**CHARITABLE TRUST**

**HICET**

***HINDUSTHAN***  
***COLLEGE OF ENGINEERING AND TECHNOLOGY***

**(An Autonomous Institution)**

**Coimbatore – 641032**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
ENGINEERING**

**2022 REGULATIONS**

**2023-2024**





# Hindusthan College of Engineering and Technology

Approved by AICTE, New Delhi and Accredited with 'A' Grade by NAAC

(An Autonomous Institution, Affiliated to Anna University, Chennai)

Othakalmandapam Post, Coimbatore



## VISION OF THE DEPARTMENT

To evolve as a centre of excellence in Electronics and Communication Engineering, to cater the global industrial needs.

## MISSION OF THE DEPARTMENT

1. To expand frontiers of knowledge through the provision of inspiring learning environment.
2. To develop the intellectual skills towards employability by fostering innovation, and creativity in learning.
3. To provide a quality system for wholesome learning to achieve progress and prosperity in life along with moral values





# Hindusthan College of Engineering and Technology

Approved by AICTE, New Delhi and Accredited with 'A' Grade by NAAC

(An Autonomous Institution, Affiliated to Anna University, Chennai)



Othakalmandapam Post, Coimbatore

## PROGRAMME OUTCOMES

- 1. ENGINEERING KNOWLEDGE** : Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- 2. PROBLEM ANALYSIS** : Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences and engineering sciences.
- 3. DESIGN/ DEVELOPMENT OF SOLUTIONS** : Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate considerations for the public health and safety, and the cultural, societal and environmental consideration.
- 4. CONDUCT INVESTIGATIONS OF COMPLEX PROBLEMS**: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data and synthesis of the information to provide valid conclusions.
- 5. MODERN TOOL USAGE** : Create, select and apply appropriate techniques, resources and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.
- 6. THE ENGINEER AND SOCIETY** : Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
- 7. ENVIRONMENT AND SUSTAINABILITY**: understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
- 8. ETHICS**: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
- 9. INDIVIDUAL AND TEAM WORK**: Function effectively as an individual, and as a member





or leader in diverse teams and in multidisciplinary settings.

10. **COMMUNICATION:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. **PROJECT MANAGEMENT AND FINANCE:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work , as a member and leader in a team, to manage projects and in multidisciplinary environment.
12. **LIFE LONG LEARNING:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

#### **PROGRAMME SPECIFIC OUTCOMES**

**PSO 1:** Graduates will be able to disseminate the knowledge in Communication Engineering towards Technical Incubation.

**PSO 2:** Graduates will have the perseverance to learn the modern design tools for Electronic system design and analysis.

#### **PROGRAMME EDUCATIONAL OBJECTIVES**

**PEO1:** Exhibit their technical skills and knowledge in their working environment, higher studies and research.

**PEO2:** Succeed in multidisciplinary dimensions by excelling through life-long learning.

**PEO3:** Become leaders and innovators by devising engineering solutions for social issues and problems.





# Hindusthan College of Engineering and Technology

Approved by AICTE, New Delhi and Accredited with 'A' Grade by NAAC  
(An Autonomous Institution, Affiliated to Anna University, Chennai)



Othakalmandapam Post, Coimbatore

---

## B.E ELECTRONICS AND COMMUNICATION ENGINEERING (UG) CO'S, PO'S & PSO'S MAPPING

**SEMESTER I**

**22MA1101 MATRICES AND CALCULUS**

PO CO	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	3	3	3	-	-	-	-	-	-	2	2	1
CO2	3	3	3	2	2	-	-	-	-	-	-	2	2	2
CO3	3	3	3	2	3	-	-	-	-	-	-	2	2	2
CO4	3	3	3	3	3	-	-	-	-	-	-	2	2	3
CO5	3	3	3	3	3	-	-	-	-	-	-	2	1	2
AV G	3	3	3	2.6	2.8	-	-	-	-	-	-	2	1.8	2

**22CY1151 CHEMISTRY FOR CIRCUIT ENGINEERING**

PO & PS O	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO8	PO9	PO10	PO11	PO 12	PSO1	PS O2
CO 1	2	3	3	1	1	1	1	-	1	-	1	2	-	-
CO 2	2	3	2	1	1	1	1	-	1	-	1	2	-	-
CO 3	2	2	2	2	1	1	1	-	1	-	1	2	-	-
CO 4	2	2	3	1	2	1	1	-	1	-	2	2	-	1
CO 5	2	3	3	2	2	1	1	-	1	-	1	2	-	-
Av g	2	2.6	2.6	1.4	1.4	1	1	-	1	-	1.2	2	-	1

22HE1151 ENGLISH FOR ENGINEERS

PO & PSO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	2					1	2	2	2	3	1	1	1	2
CO2	2	1			1	1	1	2	2	3		2		2
CO3	2	1			1	1	2	3	3	3		1	1	2
CO4	2	1				1	2	2	2	3	1	1		
CO5	2					1	1	2	3	3		1	1	2
Av g	2	1	-	-	1	1	1.6	2.2	2.4	3	1	1.2	1	2

22EC1151 ELECTRON DEVICES

PO & PSO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO2	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO3	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO4	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO5	3	2	2	-	1	-	-	-	-	-	-	2	2	2

<b>AV G</b>	3	2	2		1							2	2	2
-----------------	---	---	---	--	---	--	--	--	--	--	--	---	---	---

**22IT1151/ 22CS1152 PYTHON PROGRAMMING AND PRACTICES/ OBJECT ORIENTED PROGRAMMING USING PYTHON(IBM STUDENTS ONLY)**

<b>PO &amp; PS O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>P O 9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO 12</b>	<b>PS O11</b>	<b>PS O12</b>
<b>C O1</b>	2	3	3	-	2	-	-	-	-	-	-	2	2	2
<b>C O2</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O3</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O4</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O5</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>Av g</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2

**22HE1071 UHV**

<b>PO &amp; PS O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>P O 9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO 12</b>	<b>PS O11</b>	<b>PS O12</b>
<b>C O1</b>	2	3	3	-	2	-	-	-	-	-	-	2	2	2
<b>C</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2

O2														
C O3	2	3	3	-	2	-	-	-	2	-	-	2	2	2
C O4	2	3	3	-	2	-	-	-	2	-	-	2	2	2
C O5	2	3	3	-	2	-	-	-	2	-	-	2	2	2
Av g	2	3	3	-	2	-	-	-	2	-	-	2	2	2

22HE1072 ENTREPRENEURSHIP & INNOVATION

PO & PS O	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO1 0	PO1 1	PO1 2	PSO 1	PSO 2
CO 1	3	2	2	-	-	-	-	-	-	-	-	1	2	2
CO 2	3	2	2	-	-	-	-	-	-	-	-	1	2	2
CO 3	3	2	2	-	-	-	-	-	-	-	-	1	2	2
CO 4	3	2	2	-	-	-	-	-	-	-	-	1	2	2
CO 5	3	2	2	-	-	-	-	-	-	-	-	1	2	2
AV G	3	2	2									1	2	2

22MC1091/22MC1092 தமிழரூம் ததழில் நுட்பமும்/Indian Constitution

P O & PS O	P O 1	P O 2	P O 3	P O 4	P O 5	PO6	PO7	PO8	P O 9	PO10	PO11	PO 12	PS O1	PS O2
C O1	2	3	3	-	2	-	-	-	-	-	-	2	2	2

<b>C O 2</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O 3</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O 4</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O 5</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>A v g</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2



**SEMESTER III**

**22MA3102 COMPLEX ANALYSIS AND TRANSFORMS (common to ECE,EEE,EIE)**

<b>PO &amp; PSO</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO 10</b>	<b>PO 11</b>	<b>PO 12</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO1</b>	3	3	3	2	2	-	-	-	-	-	-	2	2	2
<b>CO2</b>	3	3	3	2	3	-	-	-	-	-	-	2	2	2
<b>CO3</b>	3	3	3	3	3	-	-	-	-	-	-	2	2	2
<b>CO4</b>	3	3	3	3	3	-	-	-	-	-	-	2	1	2
<b>CO5</b>	3	3	3	3	3	-	-	-	-	-	-	2	2	1
<b>Avg</b>	3	3	3	2.6	2.8	-	-	-	-	-	-	2	1.8	1.8

**22EC3201 ELECTRONIC CIRCUITS**

<b>PO CO</b>	<b>P O 1</b>	<b>PO 2</b>	<b>P O 3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO9</b>	<b>PO 10</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO1</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>CO2</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>CO3</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>CO4</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>CO5</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>AV G</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**22EC3202 SIGNALS AND SYSTEMS**

<b>PO &amp; PS O</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>C O1</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>C O2</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>C O3</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>C O4</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>C O5</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>AV G</b>	3	2	2									1	2	-

**22EC3203 DIGITAL ELECTRONICS**

<b>PO &amp; PS O</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>CO 2</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>CO 3</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>CO 4</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>CO 5</b>	3	2	2	-	-	-	-	-	-	-	-	1	2	-
<b>AV G</b>	3	2	2									1	2	-

**22EC3204 CIRCUITS AND NETWORKS**

PO & PSO	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO1 0	PO1 1	PO1 2	PSO 1	PSO 2
CO 1	3	2	2	-	-	-	-	-	-	-	-	1	2	1
CO 2	3	2	2	-	-	-	-	-	-	-	-	1	2	1
CO 3	3	2	2	-	-	-	-	-	-	-	-	1	2	1
CO 4	3	2	2	-	-	-	-	-	-	-	-	1	2	1
CO 5	3	2	2	-	-	-	-	-	-	-	-	1	2	1
AV G	3	2	2									1	2	1

**22EC3251/22IT3252 OOPS USING JAVA/RELATIONAL DATABASE MANAGEMENT SYSTEM (IBM STUDENTS ONLY)**

PO CO	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO 1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
AV G	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**22EC3001 ELECTRONIC CIRCUITS LABORATORY**

O O	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
O1	3	3	3	2	-	2	-	3	-	-	2	3	2	2
O2	3	3	3	2	-	2	-	3	-	-	2	3	2	2
O3	3	3	3	2	-	2	-	3	-	-	2	3	2	2
O4	3	3	3	2	-	2	-	3	-	-	2	3	2	2
O5	3	3	3	2	-	2	-	3	-	-	2	3	2	2
V	3	3	3	2	-	2	-	3	-	-	2	3	2	2

**22EC3002 DIGITAL ELECTRONICS LABORATORY**

<b>PO CO</b>	<b>P O 1</b>	<b>PO 2</b>	<b>P O 3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO9</b>	<b>PO 10</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
CO1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>AV G</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>-</b>	<b>3</b>	<b>-</b>	<b>3</b>	<b>-</b>	<b>-</b>	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>

**22HE3071 SOFT SKILLS -2**

<b>P O &amp; PS O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>P O 9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO 12</b>	<b>PS O11</b>	<b>PS O12</b>
<b>C O1</b>	2	3	3	-	2	-	-	-	-	-	-	2	2	2
<b>C O2</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O3</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O4</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O5</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>Av g</b>	<b>2</b>	<b>3</b>	<b>3</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>-</b>	<b>-</b>	<b>2</b>	<b>-</b>	<b>-</b>	<b>2</b>	<b>2</b>	<b>2</b>



**22EC5203 MICROPROCESSORS AND MICROCONTROLLERS**

PO CO	P O 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	3	2	2	2	3	3	3	-	-	3	3	3
CO2	3	3	3	2	2	2	3	3	3	-	-	3	3	3
CO3	3	3	3	2	2	2	2	3	3	-	-	3	3	3
CO4	3	3	3	2	2	2	2	3	3	-	-	3	3	3
CO5	3	3	3	2	2	2	2	3	3	-	-	3	3	3
<b>AV G</b>	3	3	3	2	2	2	2	3	3	-	-	3	3	3

**22EC5251 ANGULAR JS(IBM STUDENTS ONLY)**

PO CO	P O 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>AV G</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**22EC5001 MICROPROCESSORS AND MICROCONTROLLERS LAB**

PO CO	P O 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
<b>AV G</b>	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**22EC5002 DIGITAL COMMUNICATION LAB**

PO CO	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	2	2	2		2						2	2	3
CO2	3	2	2	2		2						2	2	3
CO3	3	2	2	2		2						2	2	3
CO4	3	2	2	2		2						2	2	3
CO5	3	2	2	2		2						2	2	3
AV G	3	2	2	2		2						2	2	3

**22HE5071 SOFT SKILLS -4 / FOREIGN LANGUAGES**

PO CO	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
AV G	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**SEMESTER VII**
**22EC7201 WIRELESS COMMUNICATION NETWORKS**

PO CO	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	2	3	-	3	-	3	-	-	1	3	2	3
CO2	3	3	2	3	-	3	-	3	-	-	1	3	2	3
CO3	3	3	2	3	-	3	-	3	-	-	1	3	2	3
CO4	3	3	2	3	-	3	-	3	-	-	1	3	2	3
CO5	3	3	2	3	-	3	-	3	-	-	1	3	2	3
AV G	3	3	2	3	-	3	-	3	-	-	1	3	2	3

**22EC7251 BLOCKCHAIN(IBM STUDENTS ONLY)**

PO CO	P O 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
AV G	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**22EC7001 EMBEDDED SYSTEMS AND IOT**

PQ CO	P O 1	PO 2	P O3	PO4	PO5	PO6	PO 7	PO 8	PO9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
CO1	3	2	3	3	-	3	-	2	-	-	1	3	2	3
CO2	3	2	3	3	-	3	-	2	-	-	1	3	2	3
CO3	3	2	3	3	-	3	-	2	-	-	1	3	2	3
CO4	3	2	3	3	-	3	-	2	-	-	1	3	2	3
CO5	3	2	3	3	-	3	-	2	-	-	1	3	2	3
AV G	3	2	3	3	-	3	-	2	-	-	1	3	2	3

**22EC7001 OPTICAL COMMUNICATION AND MICROWAVE ENGINEERING**

PO CO	P O 1	PO 2	P O 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS C1	PS O2
CO 1	3	2	2	3	2	2	2					1	3	1
CO 2	3	2	2	3	2	2	2					1	3	1
CO 3	3	2	2	3	2	2	2					1	3	1
CO 4	3	2	2	3	2	2	2					1	3	1
CO 5	3	2	2	3	2	2	2					1	3	1
AV G	3	2	2	3	2	2	2					2	2	1



**22EC7901 INTERNSHIP**

PO CO	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS O1	PS O2
CO 1	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 2	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 3	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 4	3	3	3	3	-	3	-	3	-	-	1	3	2	3
CO 5	3	3	3	3	-	3	-	3	-	-	1	3	2	3
AV G	3	3	3	3	-	3	-	3	-	-	1	3	2	3

**Mapping of Course Outcome and Programme Outcome:**

Year	Se m	Course code & Name	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PS O 1	PSO 2
I	I	22MA1101 MATRICES AND CALCULUS	3	3	3	2. 6	2. 8	-	-	-	-	-	-	2	1. 8	2
		22CY1151 CHEMISTRY FOR CIRCUIT ENGINEERING	2	2. 6	2. 6	1. 4	1. 4	1	1	-	1	-	1. 2	2	-	1
		22HE1151 ENGLISH FOR ENGINEERS	2	1	-	-	1	1	1. 6	2. 2	2. 4	3	1	1. 2	1	2
		22EC1151 ELECTRON DEVICES	3	2	2		1							2	2	2
		22IT1151/ 22CS1152 PYTHON PROGRAMMING AND PRACTICES/ OBJECT ORIENTED PROGRAMMING USING PYTHON (IBM STUDENTS ONLY)	2	3	3	-	2	-	-	-	2	-	-	2	2	2



		JS (IBM STUDENTS ONLY)															
		22EC5001 MICROPROCESSORS AND MICROCONTROLLER S LAB	3	3	3	3	-	3	-	3	-	-	1	3	2	3	
		22EC5002 DIGITAL COMMUNICATION LAB	3	2	2	2		2					2	2	3		
		22HE5071 SOFT SKILLS -4 / FOREIGN LANGUAGES	3	3	3	3	-	3	-	3	-	-	1	3	2	3	
IV	V II	22EC7201 WIRELESS COMMUNICATION NETWORKS	3	3	2	3	-	3	-	3	-	-	1	3	2	3	
		22EC7251 BLOCKCHAIN (IBM STUDENTS ONLY)	3	3	3	3	-	3	-	3	-	-	1	3	2	3	
		22EC7001 EMBEDDED SYSTEMS AND IOT	3	2	3	3	-	3	-	2	-	-	1	3	2	3	
		22EC7001 OPTICAL COMMUNICATION AND MICROWAVE ENGINEERING	3	2	2	3	2	2	2					2	2	1	
		22EC7901 INTERNSHIP	3	3	3	3	-	3	-	3	-	-	1	3	2	3	

P. Hayla  
Chairman - BoS  
ECE - HICET



Dean (Academics)  
HICET

ECA

# **Hindusthan College of Engineering and Technology**

(An Autonomous Institution, Affiliated to Anna University, Chennai  
Approved by AICTE, New Delhi & Accredited by NAAC with 'A' Grade)  
Valley Campus, Pollachi Highways, Coimbatore, Tamilnadu.

## **DETAILS OF CHANGES CoPos**

### **CBCS PATTERN**

### **UNDERGRADUATE PROGRAMMES**

### **B.E ELECTRONICS AND COMMUNICATION ENGINEERING (UG)**

### **REGULATION-2022 (Revised on June 2021)**

#### **Amendments on June 2023**

#### **SEMESTER I**

#### **22MA1101 MATRICES AND CALCULUS**

<b>PO CO</b>	<b>P O 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO9</b>	<b>PO 10</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO1</b>	3	3	3	3	3	-	-	-	-	-	-	2	2	1
<b>CO2</b>	3	3	3	2	2	-	-	-	-	-	-	2	2	2
<b>CO3</b>	3	3	3	2	3	-	-	-	-	-	-	2	2	2
<b>CO4</b>	3	3	3	3	3	-	-	-	-	-	-	2	2	3
<b>CO5</b>	3	3	3	3	3	-	-	-	-	-	-	2	1	2
<b>AV G</b>	3	3	3	2.6	2.8	-	-	-	-	-	-	2	1.8	2

#### **22CY1151 CHEMISTRY FOR CIRCUIT ENGINEERING**

<b>PO &amp; PS O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>P O 6</b>	<b>P O 7</b>	<b>PO8</b>	<b>PO9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO 12</b>	<b>PSO1</b>	<b>PS O2</b>
<b>CO 1</b>	2	3	3	1	1	1	1	-	1	-	1	2	-	-

<b>CO 2</b>	2	3	2	1	1	1	1	-	1	-	1	2	-	-
<b>CO 3</b>	2	2	2	2	1	1	1	-	1	-	1	2	-	-
<b>CO 4</b>	2	2	3	1	2	1	1	-	1	-	2	2	-	1
<b>CO 5</b>	2	3	3	2	2	1	1	-	1	-	1	2	-	-
<b>Av g</b>	2	2.6	2.6	1.4	1.4	1	1	-	1	-	1.2	2	-	1

## 22HE1151 ENGLISH FOR ENGINEERS

PO P S O	P O 1	P O 2	P O 3	PO4	PO 5	PO 6	PO7	PO8	PO 9	PO1 0	PO11	PO 12	PS O1	PS O2
C O 1	2	1				1	2	2				1	1	2
C O 2	2	1			1	1	1	2				2		2
C O 3	2	1			1	1	2	3				1	1	2
C O 4	2	1				1	2	2				1		
C O 5	2	1				1	1	2				1	1	2
A v g	2	1	-	-	1	1	1.6	2.2				1.2	1	2

## 22EC1151 ELECTRON DEVICES

PO & PS O	P O 1	P O 2	P O 3	PO4	PO 5	PO 6	PO7	PO8	PO 9	PO1 0	PO11	PO 12	PSO 1	PS O2
CO 1	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO 2	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO 3	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO 4	3	2	2	-	1	-	-	-	-	-	-	2	2	2
CO 5	3	2	2	-	1	-	-	-	-	-	-	2	2	2
AV G	3	2	2		1							2	2	2

PROGRAMMING USING PYTHON(IBM STUDENTS ONLY)

<b>PO &amp; PS O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>P O 9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO 12</b>	<b>PS O1</b>	<b>PS O2</b>
<b>C O1</b>	2	3	3	-	2	-	-	-	-	-	-	2	2	2
<b>C O2</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O3</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O4</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O5</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>Av g</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2

22HE1071 UHV

<b>PO &amp; PS O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>P O 9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO 12</b>	<b>PS O1</b>	<b>PS O2</b>
<b>C O1</b>	2	3	3	-	2	-	-	-	-	-	-	2	2	2
<b>C O2</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O3</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2



<b>O4</b>														
<b>C</b>														
<b>O5</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>Av</b>														
<b>g</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2

**22HE1072 ENTREPRENEURSHIP & INNOVATION**

<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PSO</b>	<b>PSO</b>
<b>CO</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>9</b>	<b>10</b>	<b>1</b>	<b>2</b>	<b>1</b>	<b>2</b>
<b>CO1</b>	3	3	3	3	3	-	-	-	-	-	-	2	2	1
<b>CO2</b>	3	3	3	2	2	-	-	-	-	-	-	2	2	2
<b>CO3</b>	3	3	3	2	3	-	-	-	-	-	-	2	2	2
<b>CO4</b>	3	3	3	3	3	-	-	-	-	-	-	2	2	3
<b>CO5</b>	3	3	3	3	3	-	-	-	-	-	-	2	1	2
<b>AV</b>														
<b>G</b>	3	3	3	2.6	2.8	-	-	-	-	-	-	2	1.8	2

**22MC1091/22MC1092 தமிழரூம்ததமிழில்நுட்பமும்/Indian Constitution**

<b>P</b>	<b>P</b>	<b>P</b>	<b>P</b>	<b>P</b>	<b>P</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>P</b>	<b>PO</b>	<b>PO</b>	<b>PO</b>	<b>PS</b>	<b>PS</b>
<b>O</b>	<b>O</b>	<b>O</b>	<b>O</b>	<b>O</b>	<b>O</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>O</b>	<b>10</b>	<b>11</b>	<b>12</b>	<b>O1</b>	<b>O2</b>
<b>PS</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>				<b>9</b>					
<b>CO</b>														
<b>O1</b>	2	3	3	-	2	-	-	-	-	-	-	2	2	2
<b>CO</b>														
<b>O2</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>CO</b>														
<b>O3</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2

<b>C O 4</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>C O 5</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2
<b>A v g</b>	2	3	3	-	2	-	-	-	2	-	-	2	2	2

### SEMESTER III

#### 22MA3102 COMPLEX ANALYSIS AND TRANSFORMS (common to ECE,EEE,EIE)

<b>PO &amp; PSO</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO 10</b>	<b>PO 11</b>	<b>PO 12</b>	<b>PSO 1</b>	<b>PS O2</b>
<b>CO1</b>	3	3	3	2	2	-	-	2	-	2	-	2	2	2
<b>CO2</b>	3	3	3	2	3	-	-	2	-	2	-	2	2	2
<b>CO3</b>	3	3	3	3	3	-	-	2	-	2	-	2	2	2
<b>CO4</b>	3	3	3	3	3	-	-	2	-	2	-	2	1	2
<b>CO5</b>	3	3	3	3	3	-	-	2	-	2	-	2	2	1
<b>Avg</b>	3	3	3	2.6	2.8	-	-	2	-	2	-	2	1.8	1.8

#### 22EC3201 ELECTRONIC CIRCUITS

<b>PO CO</b>	<b>P O 1</b>	<b>PO 2</b>	<b>P O3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO9</b>	<b>PO 10</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO1</b>	<b>3</b>	3	3	3	-	3	-	3	-	2	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>
<b>CO2</b>	<b>3</b>	3	3	3	-	3	-	3	-	2	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>
<b>CO3</b>	<b>3</b>	3	3	3	-	3	-	3	-	2	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>
<b>CO4</b>	<b>3</b>	3	3	3	-	3	-	3	-	2	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>
<b>CO5</b>	<b>3</b>	3	3	3	-	3	-	3	-	2	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>
<b>AV G</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	-	<b>3</b>	-	<b>3</b>	-	2	<b>1</b>	<b>3</b>	<b>2</b>	<b>3</b>

**22EC3202 SIGNALS AND SYSTEMS**

<b>PO &amp; PS O</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 2</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 3</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 4</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 5</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>AV G</b>	3	2	2					2		2		1	2	-

**22EC3203 DIGITAL ELECTRONICS**

<b>PO &amp; PS O</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 2</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 3</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 4</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>CO 5</b>	3	2	2	-	-	-	-	2	-	2	-	1	2	-
<b>AV G</b>	3	2	2					2		2		1	2	-

**22EC3204 CIRCUITS AND NETWORKS**

<b>PO &amp; PSO</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>P O 6</b>	<b>P O 7</b>	<b>P O 8</b>	<b>P O 9</b>	<b>PO 10</b>	<b>PO 1 1</b>	<b>PO 1 2</b>	<b>PS O 1</b>	<b>PS O 2</b>
<b>CO 1</b>	3	2	2	1	1	1	1	2	1	2	1	2	2	1
<b>CO 2</b>	3	2	2	1	1	1	1	2	1	2	1	2	2	1
<b>CO 3</b>	3	2	2	1	1	1	1	2	1	2	1	2	2	1
<b>CO 4</b>	3	2	2	1	1	1	1	2	1	2	1	2	2	1
<b>CO 5</b>	3	2	2	1	1	1	1	2	1	2	1	2	2	1
<b>AVG</b>	3	2	2	1	1	1	1	2	1	2	1	2	2	1

**22EC3251/22IT3252 OOPS USING JAVA/RELATIONAL DATABASE MANAGEMENT SYSTEM (IBM STUDENTS ONLY)**

<b>PO CO</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>P O 7</b>	<b>P O 8</b>	<b>PO9</b>	<b>P O 10</b>	<b>PO 11</b>	<b>PO 12</b>	<b>PS O 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	3	3	3	-	3	-	3	-	1	1	3	2	3
<b>CO 2</b>	3	3	3	3	-	3	-	3	-	1	1	3	2	3
<b>CO 3</b>	3	3	3	3	-	3	-	3	-	1	1	3	2	3
<b>CO 4</b>	3	3	3	3	-	3	-	3	-	1	1	3	2	3
<b>CO 5</b>	3	3	3	3	-	3	-	3	-	1	1	3	2	3
<b>AVG</b>	3	3	3	3	-	3	-	3	-	1	1	3	2	3

22EC3001 ELECTRONIC CIRCUITS LABORATORY

<b>PO C O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>P O 7</b>	<b>P O 8</b>	<b>PO 9</b>	<b>P O 10</b>	<b>PO 11</b>	<b>PO 12</b>	<b>PS O 1</b>	<b>PSO 2</b>
CO1	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO2	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO3	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO4	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO5	3	3	3	3	1	3	-	3	1	1	1	3	2	3
<b>AV G</b>	3	3	3	3	1	3	-	3	1	1	1	3	2	3

21MC1191 Essence of Indian Tradition Knowledge

<b>PO C O</b>	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>P O 7</b>	<b>P O 8</b>	<b>PO 9</b>	<b>P O 10</b>	<b>PO 11</b>	<b>PO 12</b>	<b>PS O 1</b>	<b>PSO 2</b>
CO1	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO2	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO3	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO4	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO5	3	3	3	3	1	3	-	3	1	1	1	3	2	3
<b>AV G</b>	3	3	3	3	1	3	-	3	1	1	1	3	2	3

22EC3002 DIGITAL ELECTRONICS LABORATORY

<b>PO CO</b>	<b>P O 1</b>	<b>PO 2</b>	<b>P O3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO9</b>	<b>PO 10</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
CO1	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO2	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO3	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO4	3	3	3	3	1	3	-	3	1	1	1	3	2	3
CO5	3	3	3	3	1	3	-	3	1	1	1	3	2	3
<b>AV G</b>	3	3	3	3	1	3	-	3	1	1	1	3	2	3

22HE3071 SOFT SKILLS -2

	<b>P O 1</b>	<b>P O 2</b>	<b>P O 3</b>	<b>P O 4</b>	<b>P O 5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>PO9</b>	<b>PO1 0</b>	<b>PO11</b>	<b>PO12</b>	<b>PSO 1</b>	<b>PS 2</b>
<b>C O1</b>	2	3	3	-	2	-	-	1	2	1	-	2	2	2
<b>C O2</b>	2	3	3	-	2	-	-	1	2	1	-	2	2	2
<b>C O3</b>	2	3	3	-	2	-	-	1	2	1	-	2	2	2
<b>C O4</b>	2	3	3	-	2	-	-	1	2	1	-	2	2	2
<b>C O5</b>	2	3	3	-	2	-	-	1	2	1	-	2	2	2
<b>Av g</b>	2	3	3	-	2	-	-	1	2	1	-	2	2	2

22EC3901 MINI PROJECT 1

PO & PSO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	2	3	2	2	2	3	3	3	3	1	3	3
CO2	3	2	2	3	2	2	2	3	3	3	3	1	3	3
CO3	3	2	2	3	2	2	2	3	3	3	3	1	3	3
CO4	3	2	2	3	2	2	2	3	3	3	3	1	3	3
CO5	3	2	2	3	2	2	2	3	3	3	3	1	3	3
AVG	3	2	2	3	2	2	2	3	3	3	3	1	3	3

SEMESTER V

21EC5201 Microprocessor and Microcontroller

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	2	2	2	2	2	3	3	1	-	2	3	3
CO2	3	3	2	2	2	2	2	3	3	1	-	2	2	3
CO3	3	3	2	2	2	2	2	3	3	1	-	3	3	3
CO4	3	3	3	2	2	2	2	3	3	1	-	3	3	3
CO5	3	3	2	2	3	2	2	3	3	1	-	3	3	3
AVG	3	3	2	2	2	2	2	3	3	1	-	3	3	3

21EC5202 Transmission lines and WaveGuides

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	3	1	2	1	2	1	-	2	-	-	3	3
CO2	2	2	2	1	3	1	2	1	-	2	-	-	3	2
CO3	3	3	2	1	2	2	2	1	-	2	-	-	3	2
CO4	3	3	2	1	3	2	2	1	-	2	-	-	2	2

<b>CO 5</b>	3	3	1	1	1	2	2	1	-	2	-	-	2	1
<b>AV G</b>	2.8	2.8	2	1	2.2	1.6	2	1	-	2	-	-	2.6	2

21EC5203 VLSI Design

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PS O1	PS O2
<b>CO 1</b>	3	3	2	3	2	2	3	1	2	3	1	2	3	2
<b>CO 2</b>	3	3	2	3	2	2	2	1	1	2	2	2	2	2
<b>CO 3</b>	3	2	3	3	2	2	2	1	-	2	-	2	3	2
<b>CO 4</b>	3	2	3	2	2	2	2	1	2	2	1	2	2	2
<b>CO 5</b>	3	2	3	3	2	2	2	1	-	2	1	2	2	3
<b>AV G</b>	3	3	3	3	1.8	1.8	1.4	1	1	1.8	1	2.4	3	3

21EC5301 Measurement and Instrumentation

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PS O1	PS O2
<b>CO 1</b>	3	3	2	3	2	2	3	1	2	3	1	2	3	2
<b>CO 2</b>	3	3	2	3	2	2	2	1	1	2	2	2	2	2
<b>CO 3</b>	3	2	3	3	2	2	2	1	-	2	-	2	3	2
<b>CO 4</b>	3	2	3	2	2	2	2	1	2	2	1	2	2	2
<b>CO 5</b>	3	2	3	3	2	2	2	1	-	2	1	2	2	3
<b>AV G</b>	3	3	3	3	1.8	1.8	1.4	1	1	1.8	1	2.4	3	3

21EC5251 Data Communication and Networks

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO1 1	PO1 2	PSO 1	PSO 2
--	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------



<b>CO 1</b>	3	3	3	3	-	3	-	3	3	3	2	3	3	2
<b>CO 2</b>	3	3	3	3	3	3	-	3	-	3	-	3	3	3
<b>CO 3</b>	3	3	3	3	3	-	3	-	-	-	-	3	3	3
<b>CO 4</b>	3	3	3	3	-	-	2	3	3	-	2	-	3	3
<b>CO 5</b>	3	3	3	3	3	3	2	-	3	3	2	3	3	2
<b>AV G</b>	3	3	3	3	1.8	1.8	1.4	1.8	1.8	1.8	2	2.4	3	3

### 21EC5252 Digital Signal Processing

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PSO 1	PSO 2
<b>CO 1</b>	3	3	2	3	2	3	3	1	-	1	-	1	2	3
<b>CO 2</b>	3	3	3	3	2	3	2	1	-	1	-	1	3	3
<b>CO 3</b>	3	3	2	3	3	3	3	1	-	1	-	1	3	3
<b>CO 4</b>	3	3	3	3	3	3	2	1	-	1	-	1	3	3
<b>CO 5</b>	3	3	3	3	3	3	3	1	-	1	-	1	2	3
<b>AV G</b>	3	3	2.6	3	2.6	3	2.6	1	-	1	-	1	2.6	3

### 21EC5001 VLSI Design Lab

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PSO 1	PSO 2
<b>CO 1</b>	3	3	3	3	3	3	3	1	-	1	-	1	2	3
<b>CO 2</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 3</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 4</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3

<b>CO 5</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>AV G</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3

21EC5002 Microprocessors and Microcontrollers Lab

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PSO 1	PSO 2
<b>CO 1</b>	3	3	2	3	2	3	3	1	-	1	-	1	2	3
<b>CO 2</b>	3	3	3	3	2	3	2	1	-	1	-	1	3	3
<b>CO 3</b>	3	3	2	3	3	3	3	1	-	1	-	1	3	3
<b>CO 4</b>	3	3	3	3	3	3	2	1	-	1	-	1	3	3
<b>CO 5</b>	3	3	3	3	3	3	3	1	-	1	-	1	2	3
<b>AV G</b>	3	3	2.6	3	2.6	3	2.6	1	-	1	-	1	1	3

21HE5071 Soft Skills - I

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PSO 1	PSO 2
<b>CO 1</b>	3	3	3	3	3	3	3	1	-	1	-	1	2	3
<b>CO 2</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 3</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 4</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 5</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>AV G</b>	3	3	3	3	3	3	3	1	-	1	-	1	3	3

21HE5072 Design Thinking

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO1 2	PSO 1	PSO 2
--	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------

<b>CO 1</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	2	3
<b>CO 2</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 3</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 4</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>CO 5</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>AVG</b>	3	3	3	3	3	3	3	3	1	-	1	-	1	3	3

### SEMESTER VII

#### 19EC7201 Digital Image Processing

<b>PO&amp;PSO</b> →	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO 10</b>	<b>PO 11</b>	<b>PO 12</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO1</b>	3	3	3	3	3	1		1	2			2	2	3
<b>CO2</b>	3	3	3	3	3	1		1	2			2	2	3
<b>CO3</b>	3	3	3	3	3	1		1	2	3		2	2	3
<b>CO4</b>	3	3	3	2	3	1		1	2	3		2	2	3
<b>CO5</b>	3	3	3	3	3	1		1	2	3		2	2	3
<b>AVG</b>	3	3	3	3		1		1	2	1.4		2	1	3

#### 19EC7202 Optical and Microwave Engineering

<b>PO &amp; PSO</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO12</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	2	2	3	2	2	2	1		1		1	3	1
<b>CO 2</b>	3	2	2	3	2	2	2	1		1		1	3	1
<b>CO 3</b>	3	2	2	3	2	2	2	1		1		1	3	1

<b>CO 4</b>	3	2	2	3	2	2	2	1		1		1	3	1
<b>CO 5</b>	3	2	2	3	2	2	2	1		1		1	3	1
<b>AV G</b>	3	2	2	3	2	2	2	1		1		2	2	1

19EC7251 Wireless Communication

	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	3	3	3	2	2	-	1	-	3	3	1	3	3
<b>CO 2</b>	3	3	3	3	2	2	-	1	-	3	3	1	3	3
<b>CO 3</b>	3	3	3	2	2	2	-	1	-	3	3	1	3	3
<b>CO 4</b>	3	3	3	3	2	3	-	1	-	3	3	1	3	3
<b>CO 5</b>	3	3	3	3	2	2	-	1	-	3	3	1	3	3
<b>AV G</b>	3	3	3	3	2	2		1		3	3	1	3	3

19EC7001 Digital Image Processing Lab

<b>PO &amp; PS 0</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 2</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 3</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 4</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 5</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>AV G</b>	3	2	2	3	2	2	2	3	3	3	3	2	2	1

19EC7002 Optical and Microwave Engineering Lab

<b>PO &amp; PSO</b>	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 2</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 3</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 4</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>CO 5</b>	3	2	2	3	2	2	2	3	3	3	3	1	3	1
<b>AV G</b>	3	2	2	3	2	2	2	3	3	3	3	2	2	1

19EC7401 Introduction to IOT

	<b>PO 1</b>	<b>PO 2</b>	<b>PO 3</b>	<b>PO 4</b>	<b>PO 5</b>	<b>PO 6</b>	<b>PO 7</b>	<b>PO 8</b>	<b>PO 9</b>	<b>PO1 0</b>	<b>PO1 1</b>	<b>PO1 2</b>	<b>PSO 1</b>	<b>PSO 2</b>
<b>CO 1</b>	3	3	3	3	3	3	-	1	-	3	3	1	3	3
<b>CO 2</b>	3	3	3	3	3	2	-	1	-	3	3	1	3	3
<b>CO 3</b>	3	3	3	2	3	2	-	1	-	3	3	1	3	3
<b>CO 4</b>	3	3	3	3	3	2	-	1	-	3	3	1	3	3
<b>CO 5</b>	3	3	3	3	3	2	-	1	-	3	3	1	3	3
<b>AV G</b>	3	2	3	3	3	3	-	1	-	3-	1	3	2	3

19EC7901 Project Work – Phase I

PO & PS O	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO1 0	PO1 1	PO1 2	PSO 1	PSO 2
CO 1	3	2	2	3	2	2	2	3	3	3	3	1	3	1
CO 2	3	2	2	3	2	2	2	3	3	3	3	1	3	1
CO 3	3	2	2	3	2	2	2	3	3	3	3	1	3	1
CO 4	3	2	2	3	2	2	2	3	3	3	3	1	3	1
CO 5	3	2	2	3	2	2	2	3	3	3	3	1	3	1
AV G	3	2	2	3	2	2	2	3	3	3	3	2	2	1

### Mapping of Course Outcome and Programme Outcome:

Ye ar	Se m	Course code & Name	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO 10	PO 11	PO 12	PSO 1	PSO 2
I	I	22MA1101 MATRICES AND CALCULUS	3	3	3	2.6	2.8	-	-	-	-	-	-	2	1.8	2
		22CY1151 CHEMISTRY FOR CIRCUIT ENGINEERING	2	2.6	2.6	1.4	1.4	1	1	-	1	-	1.2	2	-	1
		22HE1151 ENGLISH FOR ENGINEERS	2	1	-	-	1	1	1.6	2.2	2.4	3	1	1.2	1	2
		22EC1151 ELECTRON DEVICES	3	2	2		1							2	2	2
		22IT1151/ 22CS1152 PYTHON PROGRAMMING AND PRACTICES/ OBJECT ORIENTED PROGRAMMING USING PYTHON(IBM STUDENTS ONLY)	2	3	3	-	2	-	-	-	2	-	-	2	2	2
		22HE1071 UHV	2	3	3	-	2	-	-	-	2	-	-	2	2	2
I		22HE1072 ENTREPRENEURSHIP & INNOVATION	3	2	2								1	2	2	

		22MC1091/ 22MC1092 □□□□□□□□□□□□□□□□□□ □□□□□/Indian Constitution	2	3	3	-	2	-	-	-	2	-	-	2	2	2
	<b>III</b>	22MA3102 COMPLEX ANALYSIS AND TRANSFORMS (common to ECE,EEE,EIE)	3	3	3	2.6	2.8	-	-	2	-	2	-	2	1.8	1.8
		22EC3201 ELECTRONIC CIRCUITS	3	3	3	3	-	3	-	3	-	2	1	3	2	3
		22EC3202 SIGNALS AND SYSTEMS	3	2	2					2		2		1	2	-
		22EC3203 DIGITAL ELECTRONICS	3	2	2					2		2		1	2	-
		22EC3204 CIRCUITS AND NETWORKS	3	2	2	1	1	1	1	2	1	2	1	2	2	1
		22EC3251/22IT3252 OOPS USING JAVA/RELATIONAL DATABASE MANAGEMENT SYSTEM (IBM STUDENTS ONLY)	3	3	3	3	-	3	-	3	-	1	1	3	2	3
		22EC3001 ELECTRONIC CIRCUITS LABORATORY	3	3	3	2	1	2	1	3	1	1	2	3	2	2
<b>II</b>		22EC3002 DIGITAL ELECTRONICS LABORATORY	3	3	3	3	1	3	-	3	1	1	1	3	2	3
		21MC1191 Essence of Indian Tradition Knowledge	3	3	3	3	1	3	-	3	1	1	1	3	2	3
		22HE3071 SOFT SKILLS - 2	2	3	3	-	2	-	-	-	2	-	-	2	2	2
		22EC3901 MINI PROJECT 1	3	3	3	3	3	3	3	1	-	1	-	1	3	3
<b>III</b>	<b>V</b>	21EC5201 Microprocessor and Microcontroller	3	3	2	2	2	2	2	3	3	1	-	3	3	3
		21EC5202 Transmission lines and WaveGuides	3	3	2	1	3	2	2	1	-	2	-	-	2	2
		21EC5203 VLSI Design	3	3	3	3	1.8	1.8	1.4	1	1	1.8	1	2.4	3	3
		21EC5301 Professional Elective –I (Measurement and Instrumentation)	3	3	3	3	1.8	1.8	1.4	1	1	1.8	1	2.4	3	3
		21EC5251 Data Communication and Networks	3	3	3	3	1.8	1.8	1.4	1.8	1.8	1.8	2	2.4	3	3
		21EC5252 Digital Signal Processing	3	3	2.6	3	2.6	3	2.6	1	-	1	-	1	2.6	3
		21CS5331 Angular JS(for IBM students)														
		21EC5001 VLSI Design Lab	3	3	3	3	3	3	3	1	-	1	-	1	3	3
		21EC5002 Microprocessors and Microcontrollers Lab	3	3	2.6	3	2.6	3	2.6	1	-	1	-	1	1	3

		21HE5071 Soft Skills - I	3	3	3	3	3	3	3	3	3	1	-	1	-	1	3
		21HE5072 Design Thinking	3	3	3	3	3	3	3	1	-	1	-	1	3	3	3
IV	VII	19EC7201 Digital Image Processing	3	3	3	3		1		1	2	1.4		2	1	3	
		19EC7202 Optical and Microwave Engineering	3	2	2	3	2	2	2	1		1		2	2	1	
		19EC7401 Introduction to IOT	3	2	3	3	-	3	-	2	-	-	1	3	2	3	
		19EC7251 Wireless Communication	3	3	3	3	2	2		1		3	3	1	3	3	
		19EC7001 Digital Image processing Lab	3	2	2	3	2	2	2	3	3	3	3	2	2	1	
		19EC7002 Optical Communication and Microwave Lab	3	2	2	3	2	2	2	3	3	3	3	2	2	1	
		19EC7901 Project Work – Phase I	3	2	2	3	2	2	2	3	3	3	3	2	2	1	

*P. Hayles*  
**Chairman - BoS**  
**ECE - HICET**



*[Signature]*  
**Dean (Academics)**  
**HICET**